

# Optimal Scheduling for UET-UCT Grids Into Fixed Number of Processors

Theodore Andronikos, Nectarios Koziris,  
George Papakonstantinou and Panayiotis Tsanakas  
National Technical University of Athens  
Dept. of Electrical and Computer Engineering  
Computer Science Division  
Zografou Campus, Zografou 15773, Greece  
e-mail: {tedandr, nkoziris, papakon}@cslab.ece.ntua.gr

## Abstract

*The  $n$ -dimensional grid is one of the most representative patterns of data flow in parallel computation. Many scientific algorithms, which require nearest neighbor communication in a lattice space, are modeled by a task graph with the properties of a simple or enhanced grid. In this paper we consider an enhanced model of the  $n$ -dimensional grid by adding extra diagonal edges and allowing unequal boundaries for each dimension. First, we calculate the optimal makespan for the generalized UET-UCT (Unit Execution Time – Unit Communication Time) grid topology and, then, we establish the minimum number of processors required, to achieve the optimal makespan. We present the optimal time schedule, using unbounded and bounded number of processors, without allowing task duplication. This paper proves that UET-UCT scheduling of generalized  $n$ -dimensional grids into fixed number of processors is low complexity tractable.*

## 1 Introduction

Task scheduling is one of the most important and difficult problems in parallel systems. Since the general scheduling problem is known to be NP-complete (see Ullman [15]), researchers have given attention to other methods such as heuristics, approximation algorithms etc. In their paper Papadimitriou and Yannakakis [13] proved the intractability of the general scheduling problem of a task graph with arbitrary communication and computation times and proposed a clever heuristic with guaranteed worst performance twice the optimum makespan. In addition to this, Gerasoulis and Yang have proposed in [10], [17] the Dominant Sequence Clustering, a low complexity heuristic for general task graph scheduling, which is based on the critical path of tasks. On the other

hand, by restricting the general scheduling problem to instances with simple properties, we may come up with tractable solutions. For example, Jung et al. in [9] have presented a polynomial algorithm that finds the optimal makespan when the communication cost is constant and task duplication is allowed.

When both computation and communication times are restricted to have unit time length, it is known that scheduling general UET-UCT graphs with bounded number of processors is NP-complete, as Rayward-Smith proved in [14], or Picouleau in [13] by reduction from the unbounded UET-UCT instance. Even the case of unlimited processors, when no task duplication is allowed, is in general polynomially intractable [13]. On the other hand, using task duplication, Colin et Chretienne in [8] have presented a polynomial optimal schedule for arbitrary task graphs with UET and SCT (Small Communication Times, thus including UCT). Since the arbitrary task graph scheduling with UET-UCT and no duplication with unlimited processors is NP-complete, researchers have focused on special cases of DAGs. In [6] Chretienne presented an algorithm linear in the cardinality of the vertices of the graph, for optimal makespan on SCT in-trees and out-trees (thus covering UCT). In addition to this, there exist polynomial optimal solutions for Series-Parallel digraphs, bipartite graphs and trees with UCT as surveyed in [7].

This paper solves the problem of UET-UCT scheduling for task graphs having the form of a grid, on bounded number of processors, assuming no duplication. Grids and particularly generalized grids are typical task graphs, which model most of the signal processing algorithms and linear algebra methods such as matrix multiplication, LU decomposition etc. Although the general UET-UCT task graph scheduling problem into fixed number of processors is NP-complete, we restrict ourselves to grids which, as we prove, have low

complexity tractable schedules. We extend the simple grid model of [4] by considering generalized n-dimensional grids. In [1], [2] we have proved that the problem of time and space scheduling for generalized grids is has a polynomial time closed formula solution, when unbounded number of processors are available. We have calculated the *optimal makespan* for UET-UCT grids and *optimal number of processors*, i.e., the minimum number of processors required to achieve the optimal makespan. In this paper we extend the scheduling strategy presented in [1],[2] by considering bounded number of processors. We present an optimal time and space scheduling policy for UET-UCT grids. Our schedule partitions the vertices of the grid into disjoint sets that lie on a family of parallel hyperplanes. Each hyperplane contains vertices, which are executed on different processors at the same time if unlimited processors are available. Since the number of available processors is less than the cardinality of vertices in most of the hyperplanes (“large hyperplanes”), we use a folding strategy for space scheduling. If m processors are available, we use shifts of m vertices for each execution step. Our method is based on the fact that for every “large” hyperplane, there are m vertices available for execution, which are ancestors of m already executed points of a previous hyperplane.

The paper is organized as follows: In Section 2 we give the notation and some definitions and in Section 3 we present the optimal UET-UCT makespan for n-dimensional generalized grids and the minimum number of processors adequate for the optimal makespan. In Section 4 we establish a tight lower bound for optimal makespan for UET-UCT grids when the number of processors is fixed and a polynomial scheduling strategy that realizes this lower bound, and, thus, is optimal. Finally, in Section 5 we present an illustrative example of a 2-D and a 3-D grid optimally scheduled on unbounded and fixed number of processors.

## 2 Preliminary Concepts

### 2.1. Notation

In the rest of the paper the following notation is used:

- $N$  is the set of non negative integers (naturals).
- $n$  is the dimension of the grid.
- $G_U$  is the n-dimensional grid with terminal point  $U=(u_1, \dots, u_n)$ .

### 2.2. Basic Concepts

The directed edges of a grid represent **precedence constraints** that have to be satisfied in order to correctly complete the tasks represented by the vertices. The formal definition of the schedule must reflect our intuition that a vertex  $\mathbf{j}$  correctly begins its execution at instant  $k$  iff *all*

the vertices  $\mathbf{i} \in \text{IN}(\mathbf{j})$  have completed their execution and communicated their results (if needed) to  $\mathbf{j}$  by that moment.

- The **initial segment** of  $N^n$  with **terminal point**  $U=(u_1, \dots, u_n)+N^n$ , denoted  $N_U$ , is the set  $\{(k_1, \dots, k_n)+N^n \mid 0+k_i+u_i, 1+i+n\}$ .
- In case  $n=1$ , the initial segment  $\{k+N \mid 0+k+u\}$  is denoted  $N_u$ .
- Let  $\mathbf{e}_i$  be  $(0, \dots, \underbrace{1}_{i\text{-th}}, \dots, 0)$ ,  $1+i+n$ . The (n-dimensional) grid vector set, denoted GVS, is the set  $\{\mathbf{d}=(d_1, \dots, d_n)+N^n \mid \mathbf{d}=\lambda_1\mathbf{e}_1+ \dots +\lambda_n\mathbf{e}_n, \text{ where } \lambda_i \in \{0, 1\} + \sum_{i=1}^{i+1} \lambda_i+0\}$ .

**Definition 2.1.** The generalized n-dimensional grid with terminal point  $U$ , denoted  $G_U$ , is the DAG with vertices the set  $N_U$  and directed edges the set  $\{(\mathbf{i}, \mathbf{j})+N_U^2 \mid \mathbf{j}=\mathbf{i}+\mathbf{d}, \mathbf{d} \in \text{GVS}\}$ . ■

**Definition 2.2.** We impose a **linear ordering** among the points of  $N_U$ , which we call **lexicographic ordering**. Let  $\mathbf{i}=(m_1, \dots, m_n)$  and  $\mathbf{j}=(k_1, \dots, k_n)$  be two points of  $N_U$ . We say that  $\mathbf{i}$  is less than  $\mathbf{j}$  and we write  $\mathbf{i} < \mathbf{j}$  iff  $m_r+k_r$  for some  $r, 1+r+n$ , and, if  $r+1, m_i=k_i, 1+i+r-1$ . ■

In the rest of the paper we use the lexicographic ordering of the vertices, i.e., when we write  $\mathbf{i} < \mathbf{j}$ , we mean that  $\mathbf{i}$  is lexicographically less than  $\mathbf{j}$ .

**Definition 2.3.** For every vertex  $\mathbf{j}$  of a grid  $G_U$ , we define the following sets:

- (1)  $\text{IN}(\mathbf{j}) = \{\mathbf{i}+N_U \mid \mathbf{j}=\mathbf{i}+\mathbf{d}, \text{ where } \mathbf{d} \in \text{GVS}\}$ , and
- (2)  $\text{OUT}(\mathbf{j}) = \{\mathbf{i}+N_U \mid \mathbf{i}=\mathbf{j}+\mathbf{d}, \text{ where } \mathbf{d} \in \text{GVS}\}$ . ■

**Definition 2.4.** A **schedule** for the grid  $G_U$ , denoted  $S(G_U)$ , is an ordered couple (TIME, PROC), where TIME and PROC are the **time** and **processor schedules**, respectively, defined as follows:

- (1) TIME is a function from  $N_U$  onto  $N_k$ , for some  $k+N$ , such that:

TIME( $\mathbf{j}$ )= $t +$  the execution of task  $\mathbf{j}$  begins at moment  $t$ , and

- (2) PROC is a function from  $N_U$  onto  $N_m$ , for some  $m+N$ , such that:

PROC( $\mathbf{j}$ )= $r +$  task  $\mathbf{j}$  is assigned to processor  $r$ , with the additional requirement

- (3)  $\mathbf{i} < \text{IN}(\mathbf{j})$  TIME( $\mathbf{j}$ )  
 $\text{TIME}(\mathbf{i})+p, \dots, \mathbf{i}), \dots, \mathbf{j})$ , where:  
 $\text{TIME}(\mathbf{i})+p+c, \dots, \mathbf{i})+, \mathbf{j})$

$p$  is the processing time and  $c$  the communication delay. ■

**Remark 2.1.**

- (1) In UET-UCT grids we assume  $p=1$  and  $c=1$ .
- (2) Condition (3) is necessary in order to ensure the **validity** of the schedule, i.e., that the precedence constraints are respected. □

**Definition 2.5.** Let TIME and PROC be functions from  $N_U$  onto  $N_k$  and  $N_m$ , respectively, for some  $k, m \in N$ .

□ The **makespan** of the time schedule TIME, denoted  $M_{TIME}$ , is  $k+p$ , where  $p$  is the processing time.

□ The **processor-span** of the processor schedule PROC, denoted  $P_{PROC}$ , is  $m+1$ . ■

Clearly, the makespan gives the completion time of the last task and, hence, the time required for the completion of the whole grid, and the processor-span gives the number of processors required by the specific schedule.

**Definition 2.6.** We now give the following definitions:

□ Assuming unbounded number of processors, a time schedule is optimal, denoted  $TIME_{OPT}$ , iff its makespan is minimal.

□ In this case a processor schedule is optimal, denoted  $PROC_{OPT}$ , iff it realizes  $TIME_{OPT}$  using the minimal processor-span.

□ Assuming  $m$  processors, a time schedule is optimal, denoted  $TIME_{m-OPT}$ , iff its makespan is minimal among all time schedules that use  $m$  processors.

□ Assuming  $m$  processors, a processor schedule is optimal, denoted  $PROC_{m-OPT}$ , iff it realizes  $TIME_{m-OPT}$ . ■

In [2] and [3] the following result was proved:

**Fact 1.** Let  $G_U$  be an  $n$ -dimensional generalized grid with terminal point  $U$  and let TIME be a time schedule for  $G_U$ . Then:  $M_{TIME} = TIME(U)+p$ , where  $p$  is the processing time. □

### 3 Properties of UET-UCT Grids

In order to achieve the execution of a grid in optimal time, we partition its vertices into parallel hyperplanes, taking into account the presence of communication delays. These hyperplanes contain the vertices that can be executed in parallel; if two points belong to the same hyperplane, they can be computed at the same moment and if they belong to successive hyperplanes they can be computed at successive moments.

**Definition 3.1.** Given the  $n$ -dimensional UET-UCT grid  $G_U$ , with  $U=(u_1, \dots, u_n)$ , we give the following definitions:

□ The **length** of  $G_U$  along the  $i$ -th dimension,  $l_i$ , denoted  $L_U^i$ , is  $2(u_1 + \dots + u_{i-1} + u_{i+1} + \dots + u_n) + u_i$ .

□  $\Pi_k^i = \{(k_1, \dots, k_n) \in N_U \mid 2(x_1 + \dots + x_{i-1} + x_{i+1} + \dots + x_n) + x_i = k, k \in N\}$ ,  $l_i$ .

□  $\Pi_1^i = \max\{\Pi_k^i \mid 0 \leq k \leq L_U^i\}$ . ■

Geometrically,  $\Pi_k^i$  consists of the common points of the grid  $G_U$  and the  $n-1$  dimensional hyperplane  $2(x_1 + \dots + x_{i-1} + x_{i+1} + \dots + x_n) + x_i = k$ .

Every vertex  $j=(k_1, \dots, k_n)$  of the grid has a **maximal** coordinate, i.e., a coordinate  $k_i$  for which  $k_i \geq k_j$ . In [1] and [2] several important results for UET-UCT grids were shown. We state these results here, without proof:

**Fact 2.** Let  $G_U$  be an  $n$ -dimensional generalized grid with terminal point  $U$  and let  $j=(k_1, \dots, k_n)$  be a point of  $N_U$  with maximal coordinate  $k_i$ . The earliest

computation time of  $j$  is  $1 + \prod_{k \in \Pi^i} k_k$ .

This result demonstrates that in UET-UCT grids not all the  $n$  dimensions of the grid are equivalent. If our aim is to minimize the execution time of a vertex  $j$ , then we must pay special attention to the “maximal coordinate” of  $j$ , in the sense that the same processor must execute all points along this direction. This scheduling strategy has the effect of eliminating the communication overhead along this direction. Hence, in order to achieve the optimal parallel time of a UET-UCT grid, we have to give precedence to the maximal coordinate of the terminal point.

**Fact 3.** Let  $G_U$  be a UET-UCT grid with terminal point  $U=(u_1, \dots, u_n)$  and let  $TIME_{OPT}$  be an optimal time schedule for  $G_U$ . Then, we have  $M_{TIME_{OPT}} = 2(u_1 + \dots + u_{i-1} + u_{i+1} + \dots + u_n) + u_i + 1 = L_U^i + 1$ .

**Fact 4.** Let  $G_U$  be a UET-UCT grid with terminal point  $U=(u_1, \dots, u_n)$ . Then, the following hold:

$\left| \Pi_k^i \right| = \prod_{h=1}^i \prod_{r=1}^n \prod_{t=1}^n \prod_{s=1}^n \prod_{v=1}^n \dots \prod_{w=1}^n \prod_{x=1}^n \dots \prod_{y=1}^n \prod_{z=1}^n \dots \prod_{\substack{1 \leq k_1 \leq u_1 \\ \vdots \\ 1 \leq k_n \leq u_n}} 1$ , and:

$$\begin{aligned} & \left| \Pi_k^i \right| = \prod_{h=1}^i \prod_{r=1}^n \prod_{t=1}^n \prod_{s=1}^n \prod_{v=1}^n \dots \prod_{w=1}^n \prod_{x=1}^n \dots \prod_{y=1}^n \prod_{z=1}^n \dots \prod_{\substack{1 \leq k_1 \leq u_1 \\ \vdots \\ 1 \leq k_n \leq u_n}} 1 \\ & \left| \Pi_k^i \right| = \prod_{h=1}^i \prod_{r=1}^n \prod_{t=1}^n \prod_{s=1}^n \prod_{v=1}^n \dots \prod_{w=1}^n \prod_{x=1}^n \dots \prod_{y=1}^n \prod_{z=1}^n \dots \prod_{\substack{1 \leq k_1 \leq u_1 \\ \vdots \\ 1 \leq k_n \leq u_n}} 1 \end{aligned}$$

**Fact 5.** Let  $G_U$  be a UET-UCT grid with terminal point  $U=(u_1, \dots, u_n)$ . Then, we have:

$$\left| \Pi_k^i \right| = \prod_{h=1}^i \prod_{r=1}^n \prod_{t=1}^n \prod_{s=1}^n \prod_{v=1}^n \dots \prod_{w=1}^n \prod_{x=1}^n \dots \prod_{y=1}^n \prod_{z=1}^n \dots \prod_{\substack{1 \leq k_1 \leq u_1 \\ \vdots \\ 1 \leq k_n \leq u_n}} 1$$

where  $m_c = \prod_{h=1}^i \prod_{r=1}^n \prod_{t=1}^n \prod_{s=1}^n \prod_{v=1}^n \dots \prod_{w=1}^n \prod_{x=1}^n \dots \prod_{y=1}^n \prod_{z=1}^n \dots \prod_{\substack{1 \leq k_1 \leq u_1 \\ \vdots \\ 1 \leq k_n \leq u_n}} 1$ ,

$$\text{where } m_0 = \left\lfloor \frac{\sum_{i=1}^n \sum_{j=1}^n \sum_{k=1}^n \sum_{l=1}^n \sum_{m=1}^n \sum_{n=1}^n \sum_{o=1}^n \sum_{p=1}^n \sum_{q=1}^n \sum_{r=1}^n \sum_{s=1}^n \sum_{t=1}^n \sum_{u=1}^n \sum_{v=1}^n \sum_{w=1}^n \sum_{x=1}^n \sum_{y=1}^n \sum_{z=1}^n \sum_{\leq k_U^i \leq 1}}{2} \right\rfloor.$$

**Fact 6.** Let  $G_U$  be a UET-UCT grid with terminal point  $U=(u_1, \dots, u_n)$ . Then, we have:

$$\leq_1^i = \left\lfloor \frac{i}{\sum_{\substack{2 \leq k_U^i \\ \leq 1}} \leq} \right\rfloor.$$

**Fact 7.** Let  $G_U$  be a UET-UCT grid with terminal point  $U=(u_1, \dots, u_n)$ , let  $u_i$  be a maximal coordinate of  $U$  and let  $\text{PROC}_{\text{OPT}}$  be an optimal processor schedule for  $G_U$ . Then, we have:

$$P_{\text{PROC}_{\text{OPT}}} = \leq_1^i = \left\lfloor \frac{i}{\sum_{\substack{2 \leq k_U^i \\ \leq 1}} \leq} \right\rfloor.$$

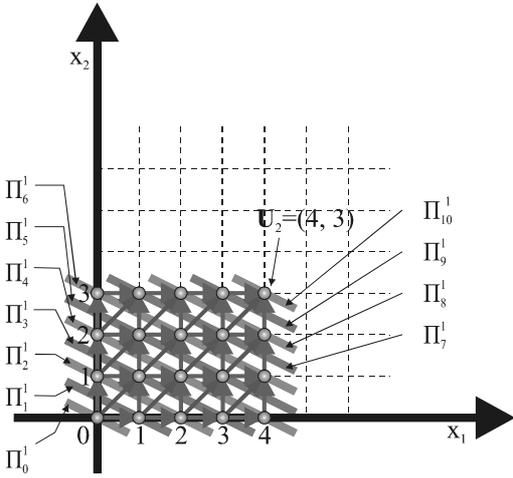


Figure 3.1. An optimal schedule for  $G_{U_2}$ .

**Example 3.1.** In this example we examine the issues involved in scheduling 2-dimensional grids using unbounded number of processors. Given an arbitrary  $j$  which is executed at time  $k$ , consider the set  $\text{OUT}(j)$ . Under any optimal scheduling strategy, *at most one*  $i \in \text{OUT}(j)$  will be executed at time  $k+1$  and all others at later time instants. Obviously, the issue here is the selection of the  $i$  that will lead to the optimal makespan. As we have pointed out the maximal coordinate of the grid determines this selection.

Further, notice that in the UET-UCT case, due to the communication delays, there is no *unique* family of optimal hyperplanes for every grid. Instead, the family of optimal hyperplanes depends on the maximal coordinate of the terminal point. For  $G_{U_2}$  (see Figure 3.1) the optimal hyperplanes are  $x_1+2x_2=k$ , whereas for  $G_{U_2^c}$  (see Figure

3.2) the optimal hyperplanes are  $2x_1+x_2=k$ ,  $0 \leq k \leq 10$ .  $\square$

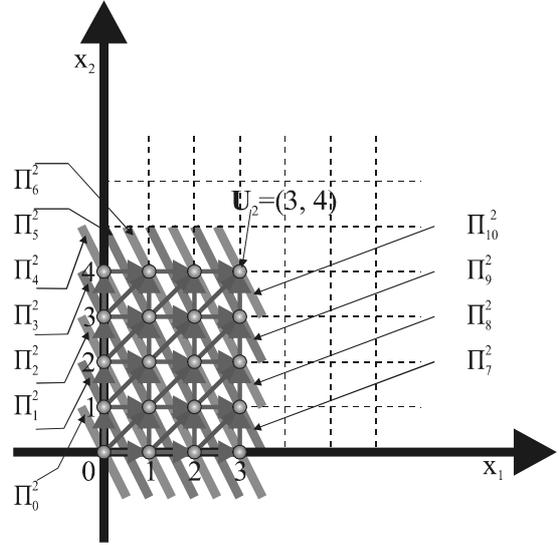


Figure 3.2. An optimal schedule for  $G_{U_2^c}$ .

**Example 3.2.** The optimal schedule for the grid  $G_{U_3}$  is depicted in Figure 3.3 and Figure 3.4. We must stress the fact that in order to achieve the optimal time schedule for the terminal point, we are forced to execute a specific set of points *at a later time*. In the case of  $G_{U_3}$  these points are of the form  $k_2e_2+k_3e_3$ , where  $0 \leq k_2 \leq 2$  and  $0 \leq k_3 \leq 1$  (see Figure 3.3).

Table 1. The optimal time schedule for  $G_{U_3}$ .

	$P_0$	$P_1$	$P_2$	$P_3$	$P_4$	$P_5$
<b>0</b>	(0,0,0)					
<b>1</b>	(1,0,0)					
<b>2</b>	(2,0,0)	(0,0,1)	(0,1,0)			
<b>3</b>	(3,0,0)	(1,0,1)	(1,1,0)			
<b>4</b>		(2,0,1)	(2,1,0)	(0,1,1)	(0,2,0)	
<b>5</b>		(3,0,1)	(3,1,0)	(1,1,1)	(1,2,0)	
<b>6</b>				(2,1,1)	(2,2,0)	(0,2,1)
<b>7</b>				(3,1,1)	(3,2,0)	(1,2,1)
<b>8</b>						(2,2,1)
<b>9</b>						(3,2,1)

Notice that the vertices with the same  $y$  and  $z$  coordinate must be executed on the same processor, which means that the optimal time schedule must execute the vertices of  $\leq_k^1$  at instant  $k$ ,  $0 \leq k \leq 9$  (see Figure 3.4). Consequently, we have  $S_{\text{TIME}_{\text{OPT}}}(U_3)=9$ . The optimal time schedule is given in Table 1. It is important to note that this schedule is not optimal in terms of processors.  $\square$

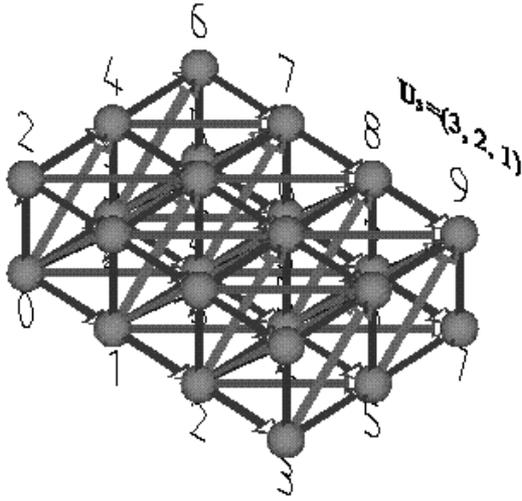


Figure 3.3. Execution Time for  $G_{U_3}$ .

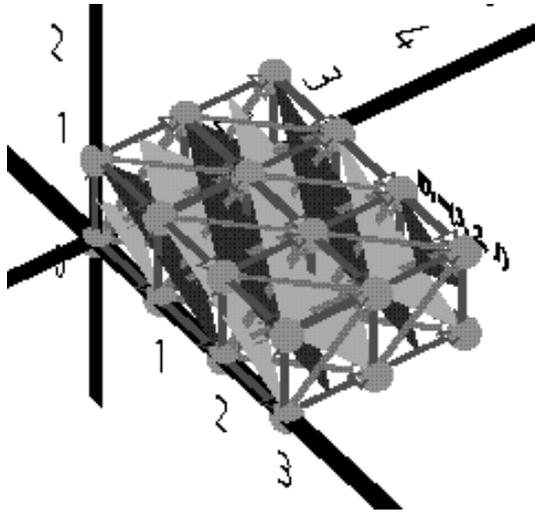


Figure 3.4. An optimal scheduling of  $G_{U_3}$ .

## 4 UET-UCT Grids & Fixed Number of Processors

### 4.1. Lower and Upper Bounds for the Optimal Makespan

In this section we establish lower and upper bounds for the optimal time schedule under the assumption that the number of processors is fixed. In the rest of this section we denote  $m$  the number of processors.

In the rest of the paper we assume that for the terminal point  $U=(u_1, \dots, u_n)$  of the  $n$ -dimensional UET-UCT grid  $G_U$  we have  $u_1 \geq u_2 \geq \dots \geq u_n$ . We can do that without any loss of generality because for every grid  $G_U$ , with terminal point  $U=(u_1, \dots, u_n)$ , there exists an isomorphic grid  $G_{U'}$  in

normal form with terminal point  $U'=(u_{1'}, \dots, u_{n'})$ , where  $(u_{1'}, \dots, u_{n'})$  is a permutation of  $(u_1, \dots, u_n)$ . This assumption will greatly facilitate the statement of the subsequent results of this paper, regarding the UET-UCT case.

**Lemma 4.1.** Let  $G_U$  be a UET-UCT grid with terminal point  $U=(u_1, \dots, u_n)$  such that  $u_1 \geq u_2 \geq \dots \geq u_n$ . Let  $j_1 < \dots < j_p < \dots < j_{|+^1_{k+1}|+1}$  and  $j_1 < \dots < j_q < j_{q+1} < \dots < j_{|+^1_{k+1}|+1}$  be the index points of  $+^1_k$  and  $+^1_{k+1}$ , respectively,  $0+k+1^1_U$ , where  $j_q = j_p + e_1$  and  $j_p + e_1 \in N_U$ . Then, the following hold:

$$(1) \quad q = \left| +^1_{k+1} \right| + 1 \geq p,$$

$$(2) \quad 1 \bigcup_{k+1}^{+^1_{k+1}|+1} 1 j_k \cap +^1_k 1 + 1 j_1 \cap j_p \cap 1, \text{ and}$$

$$(3) \quad 1 \bigcup_{k+1}^{+^1_{k+1}|+1} 1 j_k \cap +^1_k 1 + 1. \quad \square$$

The above Lemma says that any point  $j$  of  $+^1_{k+1}$  either depends on the point  $i=j-e_1$  of  $+^1_k$  or it does not depend on any point of  $+^1_k$  at all. Hence, the execution of  $j$  can begin at moment  $t+1$ , if the execution of  $i=j-e_1$  can begin at moment  $t$ , regardless of whether any point of  $+^1_k$  other than  $i$  began its execution at  $t$  (see Figure 4.1).

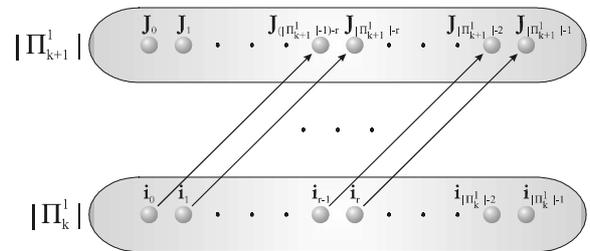


Figure 4.1. The Dependency Relation of  $+^k_k$  and  $+^k_{k+1}$ .

### 4.2. The Scheduling Strategy

What we have shown so far suggests the following optimal scheduling strategy:

Let  $j_1 < \dots < j_{p+1} < \dots < j_{|+^1_{k+1}|+1}$  and  $j_1 < \dots < j_{|+^1_{k+1}|+1}$  be the index points of  $+^1_k$  and  $+^1_{k+1}$ , respectively,  $0+k+1^1_U$ , and let  $j_{|+^1_{k+1}|+1} = i_r + e_1$ . Suppose

that at moment  $t$  the lexicographically first  $p$  points of  $\Pi_k^1$   $\mathbf{i}_{i_1} < \dots < \mathbf{i}_{i_2}$  are computed. We examine the following cases:

- >  $p-1 > r$ , where  $\mathbf{j}_{|\Pi_{k>1}^1|>1} = \mathbf{j}_{i_1}^1$  and  $\mathbf{j}_{i_2}^1$  is undefined. Then **Lemma 4.1** asserts that at moment  $t+1$  the first  $((|\Pi_{k>1}^1|-1) \cdot r) + p > p$  (obviously  $r > |\Pi_{k>1}^1|$ ) points  $\mathbf{j}_{i_1} < \dots < \mathbf{j}_{i_2}$  of  $\Pi_{k>1}^1$  are available for execution.
- >  $p-1 > r$ , where  $\mathbf{j}_{|\Pi_{k>1}^1|>1} = \mathbf{j}_{i_1}^1$  and  $\mathbf{j}_{i_2}^1$  is undefined. Then **Lemma 4.1** asserts that at moment  $t+1$  all the points of  $\Pi_{k>1}^1$  are available for execution.

The conclusion is that either **at least**  $p$  points are available for execution, or **all** the points of  $\Pi_{k>1}^1$  are available for execution at moment  $t+1$ .

Utilizing the aforementioned optimal scheduling strategy, we derive the optimal makespan for any UET-UCT grid, as stated in the following Theorem:

**Theorem 4.1.** Let  $G_U$  be a UET-UCT grid with terminal point  $\mathbf{U}=(u_1, \dots, u_n)$  such that  $u_1 > u_2 > \dots > u_n$ , let  $m$  be the number of processors and let  $\text{TIME}_{m\text{-OPT}}$  be an optimal time schedule for  $G_U$ . Then the following hold:

$$\begin{aligned}
 > \text{TIME}_{m\text{-OPT}}(\mathbf{U}) > r > 1 > \frac{\sum_{k>r} |\Pi_k^1|}{m} > r > 2r > \frac{\sum_{k>r} |\Pi_k^1|}{m} > 1, \\
 > M_{\text{TIME}_{m\text{-OPT}}} > r > 1 > \frac{\sum_{k>r} |\Pi_k^1|}{m} > r > 1 > 2r > \frac{\sum_{k>r} |\Pi_k^1|}{m} > 1,
 \end{aligned}$$

where  $r$  is the least natural such that  $|\Pi_j^1| > m$ .  $\square$

## 5 Case Study

Consider the grids  $G_{U_2}$  and  $G_{U_3}$ , of **Examples 3.1** and **3.2**, with terminal points  $\mathbf{U}_2=(4, 3)$  and  $\mathbf{U}_3=(3, 2, 1)$ , respectively. The maximal coordinate of both terminal points is the first. The grids  $G_{U_2}$  and  $G_{U_3}$  are partitioned into the hyperplanes  $\Pi_k^i$ , whose cardinality is depicted in Table 2 and Table 3, respectively. Incidentally, one can also see that their optimal makespan is 10 and 9, respectively, when unbounded number of processors is available.

Table 2. The cardinality of  $\Pi_k^1$  for  $G_{U_2}$ .

Hyperplane	Cardinality
0	1
1	1
2	2
3	2
4	3
5	2
6	3
7	2
8	2
9	1
10	1

Table 3. The cardinality of  $\Pi_k^1$  for  $G_{U_3}$ .

Hyperplane	Cardinality
0	1
1	1
2	3
3	3
4	4
5	4
6	3
7	3
8	1
9	1

If we have only 2 processors available, then the following Table 4 and Table 5 depict the optimal time schedule for the grids  $G_{U_2}$  and  $G_{U_3}$ , respectively, based on the analysis of Section 4.

Table 4. The optimal UET-UCT schedule for  $G_{U_2}$ .

	$P_0$	$P_1$
0	(0, 0)	
1	(1, 0)	
2	(2, 0)	(0, 1)
3	(3, 0)	(1, 1)
4	(4, 0)	(2, 1)
5	(0, 2)	(3, 1)
6	(1, 2)	(4, 1)
7	(2, 2)	(0, 3)
8	(3, 2)	(1, 3)
9	(4, 2)	(2, 3)
10		(3, 3)
11		(4, 3)

## 6 Conclusion

In this paper we have proved that UET-UCT scheduling of task graphs having the form of  $n$ -

dimensional generalized grids is not only low complexity tractable but it also has a general solution expressed with a closed formula for both time and processors. We have presented an optimal strategy for both time and processor scheduling of these grids with unit communication delays, having bounded number of processors. The proposed scheduling strategy for these UET-UCT graphs is proved to achieve the optimal makespan.

Table 5. The optimal UET-UCT schedule for  $G_{U_3}$ .

	$P_0$	$P_1$
<b>0</b>	(0, 0, 0)	
<b>1</b>	(1, 0, 0)	
<b>2</b>	(2, 0, 0)	(0, 1, 0)
<b>3</b>	(0, 0, 1)	(1, 1, 0)
<b>4</b>	(1, 0, 1)	(3, 0, 0)
<b>5</b>	(2, 0, 1)	(2, 1, 0)
<b>6</b>	(0, 1, 1)	(0, 2, 0)
<b>7</b>	(3, 0, 1)	(3, 1, 0)
<b>8</b>	(1, 1, 1)	(1, 2, 0)
<b>9</b>	(2, 1, 1)	(2, 2, 0)
<b>10</b>	(0, 2, 1)	(3, 2, 0)
<b>11</b>	(1, 2, 1)	(3, 1, 1)
<b>12</b>	(2, 2, 1)	
<b>13</b>	(3, 2, 1)	

## References

1. Andronikos, T., Koziris, N., Papakonstantinou, G. and Tsanakas, P. Optimal Scheduling for UET-UCT Generalized n-Dimensional Grid Task Graphs, *Proceedings of the 11<sup>th</sup> IEEE International Parallel Processing Symposium (IPPS97)*, pp. 146-151, Geneva, Switzerland, 1997.
2. Andronikos, T., Koziris, N., Papakonstantinou, G. and Tsanakas, P. Optimal Scheduling for UET/UCT Generalized n-Dimensional Grid Task Graphs, accepted, to appear in *Journal of Parallel and Distributed Computing*, 1999.
3. Andronikos, T., Koziris, N., Papakonstantinou, G. and Tsanakas, P. UET/UCT Scheduling for Grids, *Technical Report CS-CSLAB-TR-7-96*, Computing Systems Laboratory, Jul. 1996.
4. Bampis, E., Delorme, C., and Konig, J.C. Optimal Schedules for d-D Grid Graphs with Communication Delays. *Symposium on Theoretical Aspects of Computer Science (STACS96)*. Grenoble France 1996.
5. Berman, G., and Fryer, K.D. *Introduction to Combinatorics*. Academic Press, New York, 1972.
6. Chretienne, P. A Polynomial algorithm to Optimally Schedule Tasks over a Virtual Distributed System under Tree-like Precedence Constraints. *Eur. J. Oper. Res.* 43, pp. 225-230, 1989.
7. Chretienne, P. and Picouleau C. (Ed.) Scheduling with Communication Delays: A Survey, in *Scheduling Theory and its Applications pp 65-90. John Wiley & Sons 1995*.
8. Colin, J. Y., and Chretienne, P. CPM Scheduling with Small Communication Delays and Task Duplication. *Oper. Res.* 39, pp. 680-684.
9. Jung, H., Kirousis, L., and Spirakis, P. Lower Bounds and Efficient Algorithms for multiprocessor Scheduling of DAGS with communication Delays. *Proceedings of 1st SPAA 1989*, pp. 254-264, and *Information and Computation 105*, pp 94-104, 1993.
10. Gerasoulis, A., and Yang, T. On the Granularity and Clustering of Directed Acyclic Task Graphs. *IEEE Trans. Parallel Distrib. Syst.* 4, 6, pp. 686-701, 1993.
11. Koziris, N., Andronikos, T., Economakos, G., Papakonstantinou, G., and Tsanakas, P. Automatic Hardware Synthesis of Nested Loops using UET Grids and VHDL, *Proceedings of the International Conference on High Performance Computing and Networking (HPCN97)*, *Lecture Notes in Computer Science*, Springer-Verlag, pp. 888-897, Vienna, Austria 1997.
12. Papadimitriou, C., and Yannakakis, M. Toward an Architecture-Independent Analysis of Parallel Algorithms. *SIAM J. Comput.* 19, pp. 322-328, 1990. *Ext. Abstract in STOC 1988*.
13. Picouleau, C. Etude de Problemes d' Optimization dans les Systemes Distribues. These, Universite Pierre et Marie Curie, 1992.
14. Rayward-Smith, V.J. UET Scheduling with Unit Interprocessor Communication Delays and Unlimited Number of Processors. *Discrete Applied Mathematics*. 18, pp. 55-71, 1987.
15. Ullman, J. NP-Complete Scheduling problems. *Journal of Computer and Syst. Sciences*. 10, pp. 384-393, 1975.
16. Varvarigou, T., Roychowdhury, V., and Kailath, T. Scheduling in and out Forests in the Presence of Communication Delays. *Proceedings of 7th International Parallel Processing Symposium*, pp. 222-229, 1993.
17. Yang, T. and Gerasoulis, A. DSC: Scheduling Parallel Tasks on an Unbounded Number of Processors. *IEEE Trans. Parallel Distrib. Syst.* 5, 9, pp. 951-967, 1994.