

RCU-HTM: Combining RCU with HTM to Implement Highly Efficient Concurrent Binary Search Trees

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Motivation

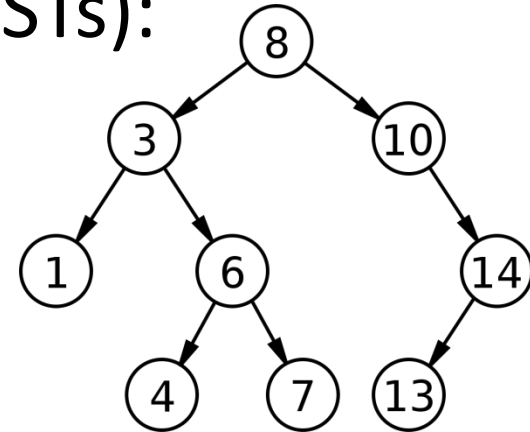
- Multi-cores are ubiquitous



- Multi-threaded applications
 - > Concurrent data structures

- Concurrent Binary Search Trees (BSTs):

- Widely used
- Linux kernel
- Database Index



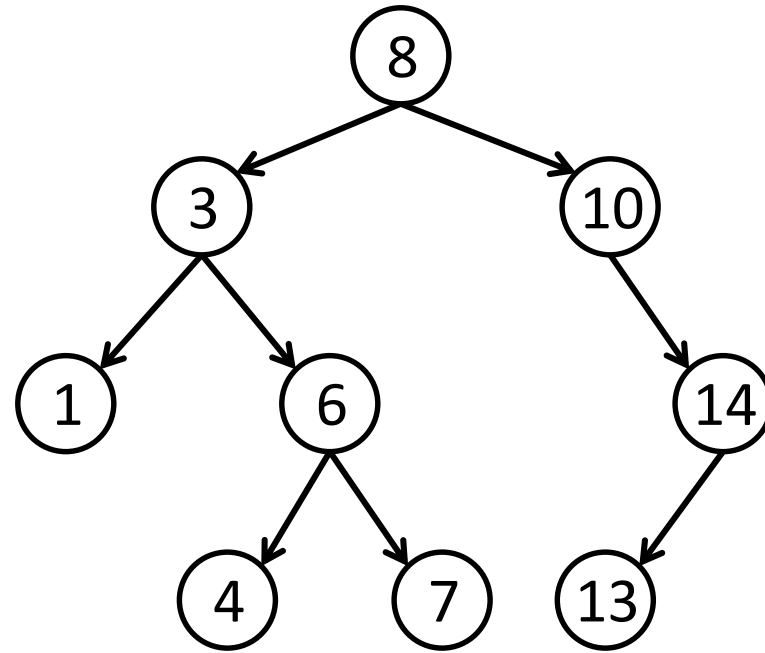
Our Contributions

- We introduce ***RCU-HTM***
 - Combines
 1. Read-Copy-Update (RCU)
 2. Hardware Transactional Memory (HTM)
 - Provides
 - Highly efficient concurrent binary search trees

Our Contributions

- We introduce ***RCU-HTM***
 - Combines
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 2. Hardware Transactional Memory (HTM)
 - Provides
 - Highly efficient concurrent binary search trees
- We apply **RCU-HTM** in AVL and Red-Black trees
 - 18% better performance, on average
 - **Excellent** performance on read-only workloads
 - **Very good** performance on write-intensive workloads

Binary Search Trees (BSTs)



- A classic binary tree with an additional property:
 - Keys in left subtree $<$ root key
 - Keys in right subtree $>$ root key
- Most commonly used to implement *dictionaries*:
 - $\langle \text{key}, \text{value} \rangle$ pairs
 - 3 operations: *lookup(key)*, *insert(key, value)* and *delete(key)*

Serial BSTs

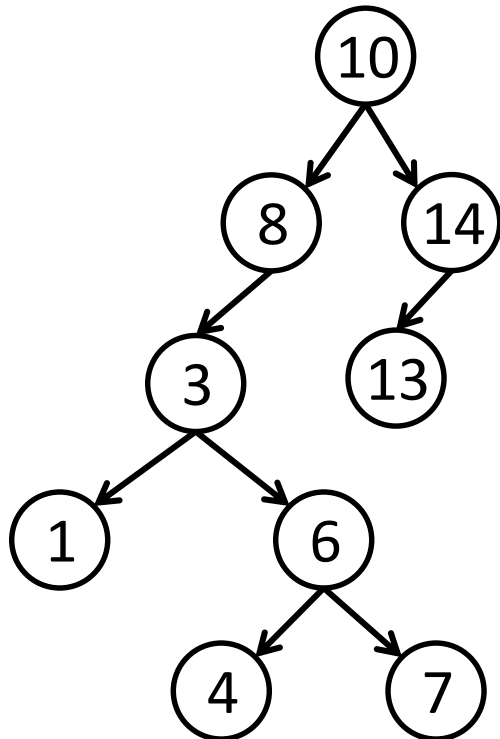
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Serial BSTs

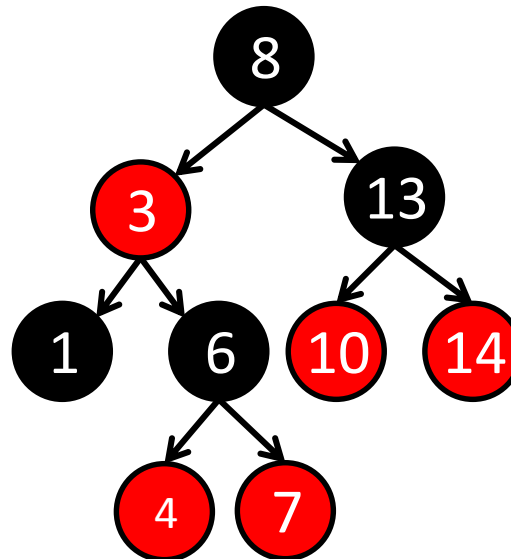
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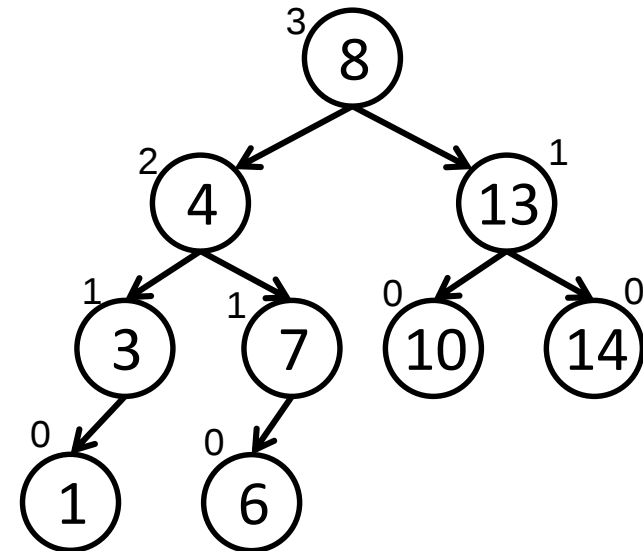
Unbalanced Tree



Red-Black Tree



AVL Tree



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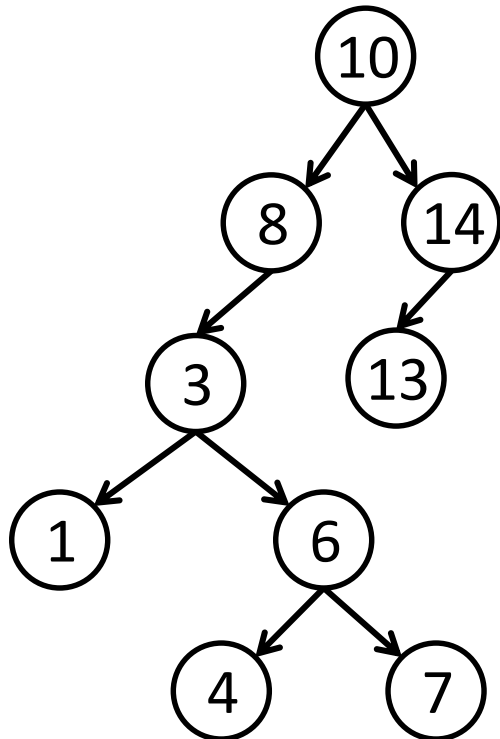
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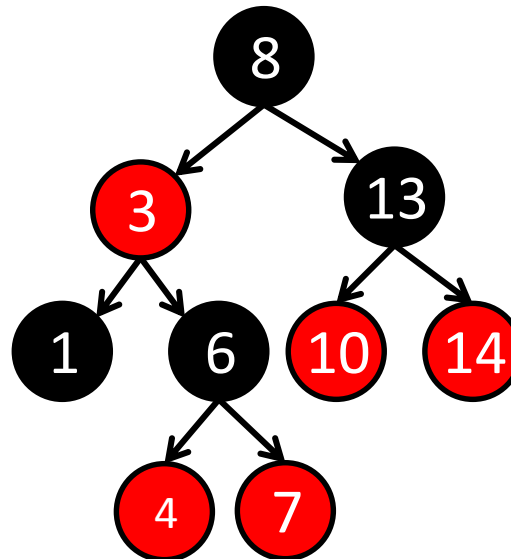
+ Shorter path lengths

- Rebalancing requires additional effort after insertions/deletions

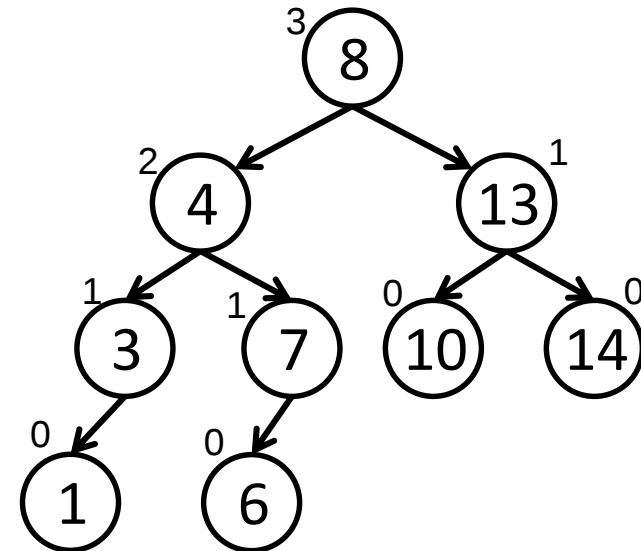
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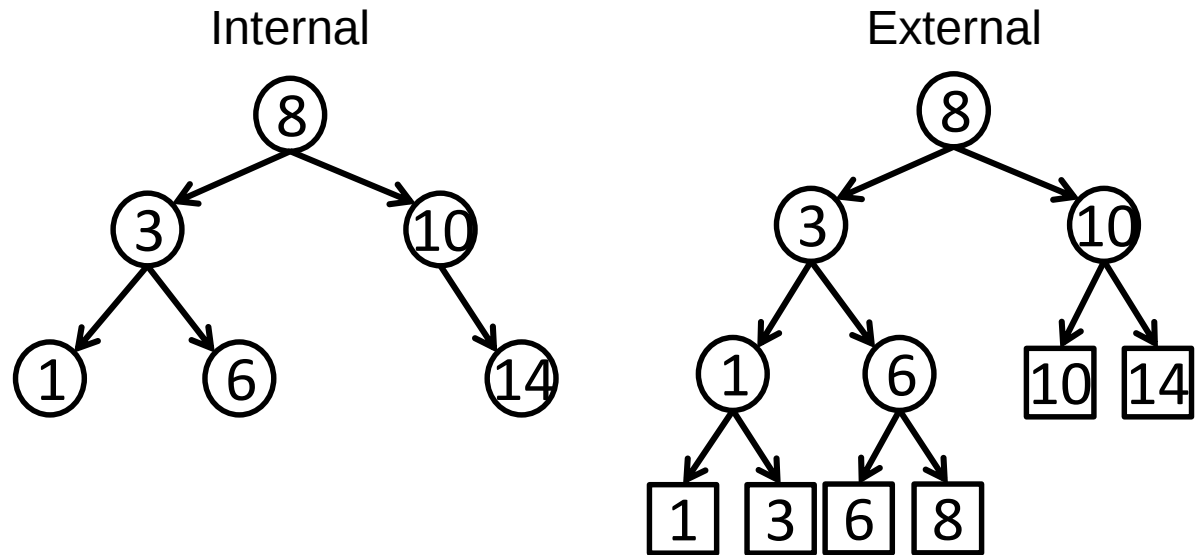


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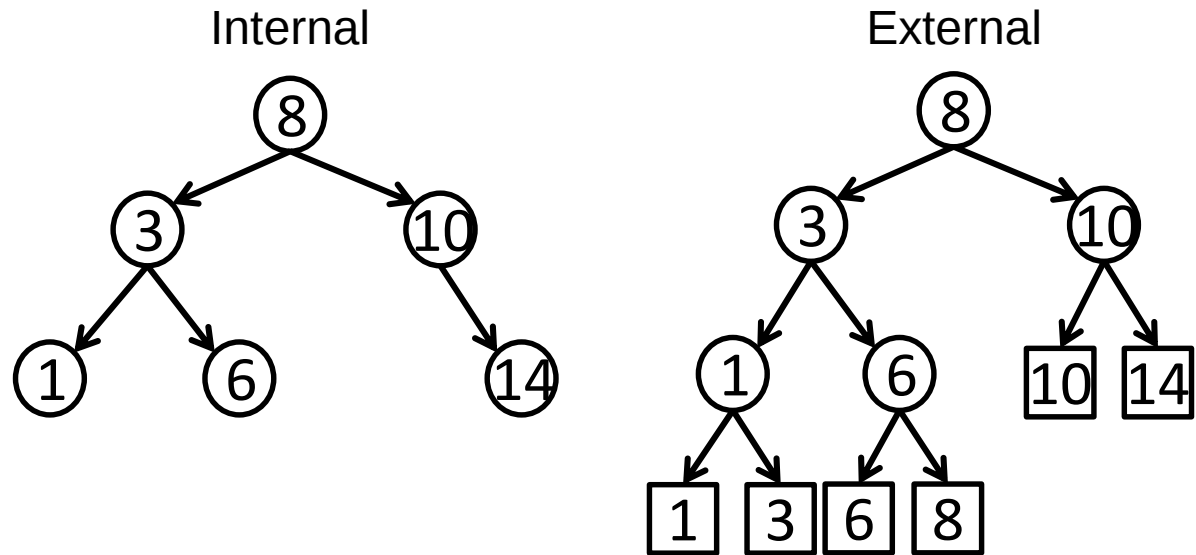


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Typical serial BSTs have the following characteristics that boost their performance:

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- + Shorter path lengths
- + Less memory overhead
- Complexity of *delete()* operation

Serial BSTs

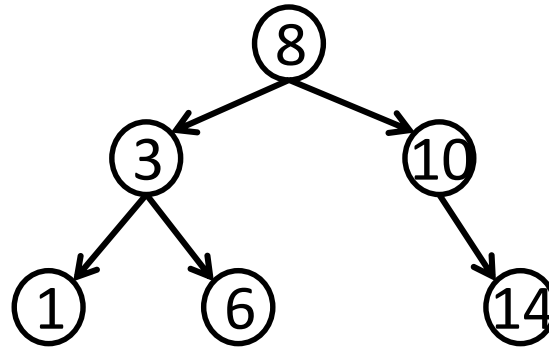
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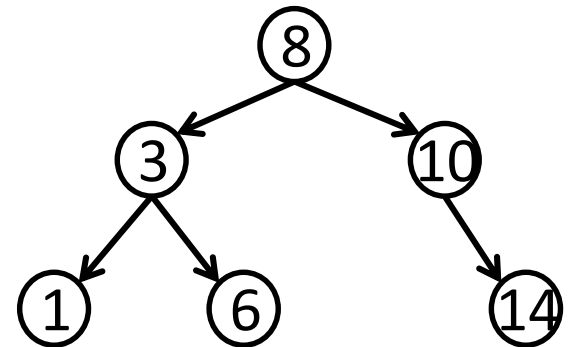
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3. On-time deletion

On-time deletion



Mark deleted nodes



delete(3)

Serial BSTs

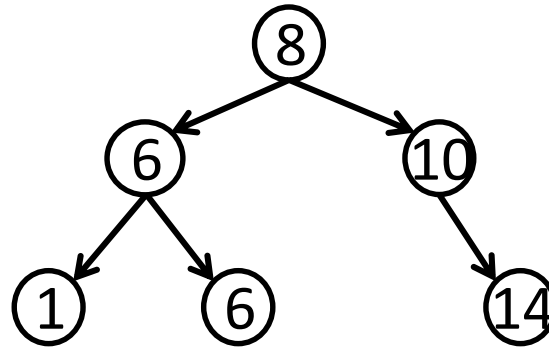
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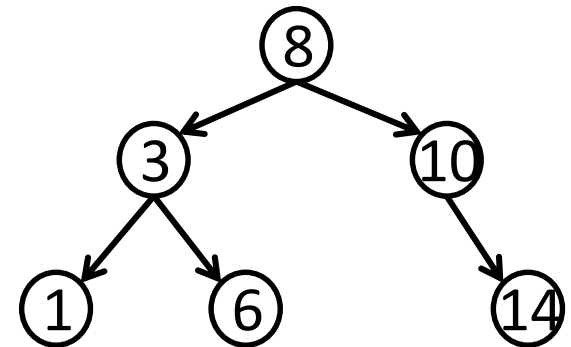
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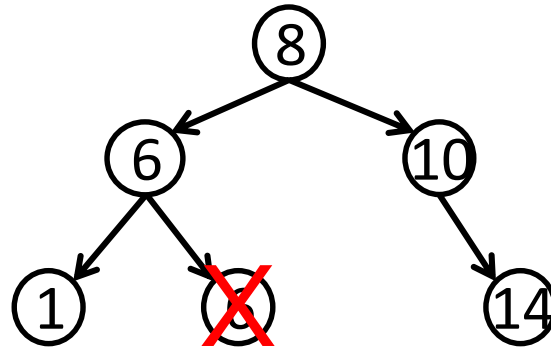
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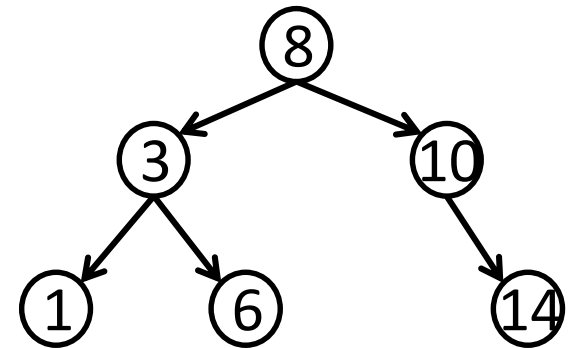
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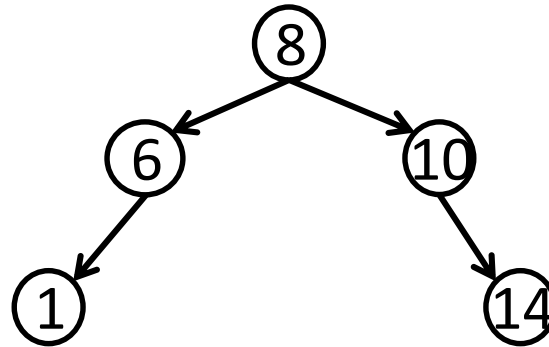
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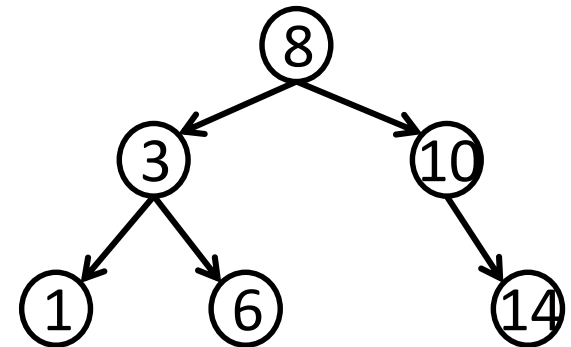
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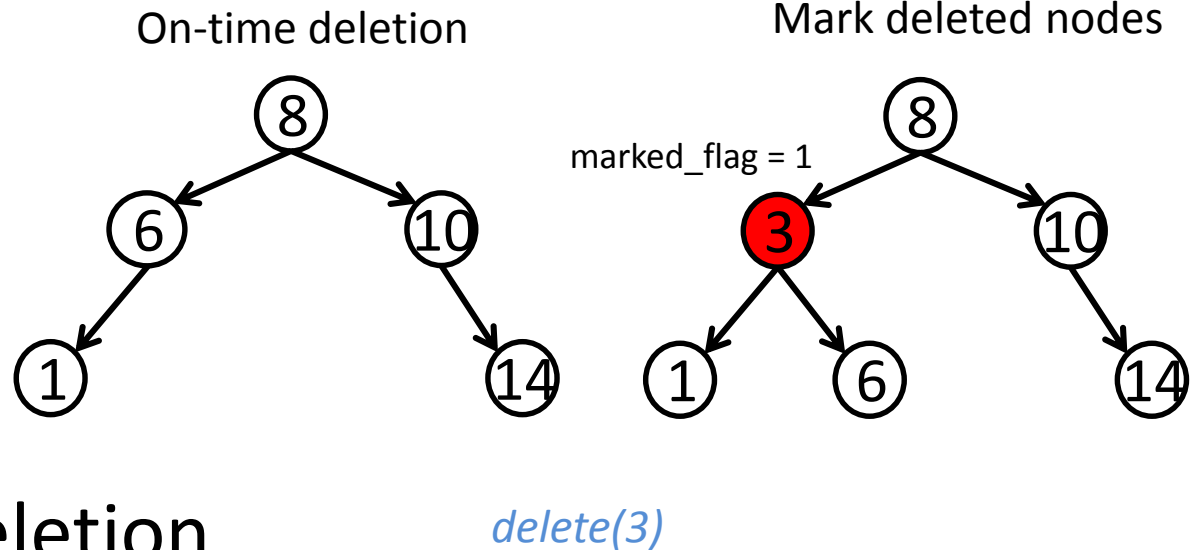
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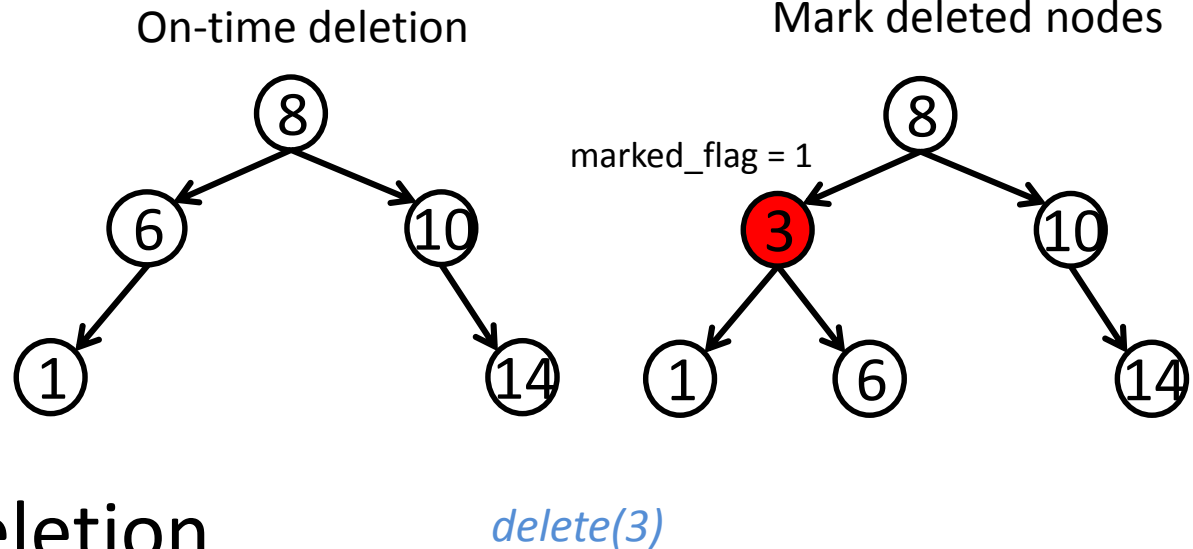
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- Complexity of *delete()* operation

Concurrent BSTs

These 3 characteristics:

- + Boost the performance of serial BSTs
- Are difficult to implement in concurrent BSTs

Why?

Rebalancing and **internal deletion** require multiple node modifications to be performed in a “single atomic” step

Concurrent BSTs

In concurrent BSTs 2 more characteristics are of high importance:

1. Asynchronized traversals

- The most common operation -> need to be fast
- Avoid synchronization overhead

2. Multiple updaters

- Updates on disjoint parts of the tree should be allowed to execute concurrently

RCU-HTM vs previous works

	Name	Balanced	Internal	On-time deletion	Asynchronized traversals	Multiple updaters
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RCU-HTM

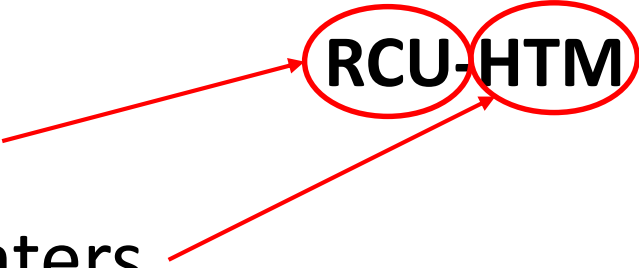
By combining RCU and HTM, **RCU-HTM** enables the implementation of:

1. Balanced
2. Internal BSTs with
3. On-time deletion

That also provide:

4. Asynchronized traversals
5. Multiple concurrent updaters

RCU-HTM



RCU-HTM: Asynchronized traversals

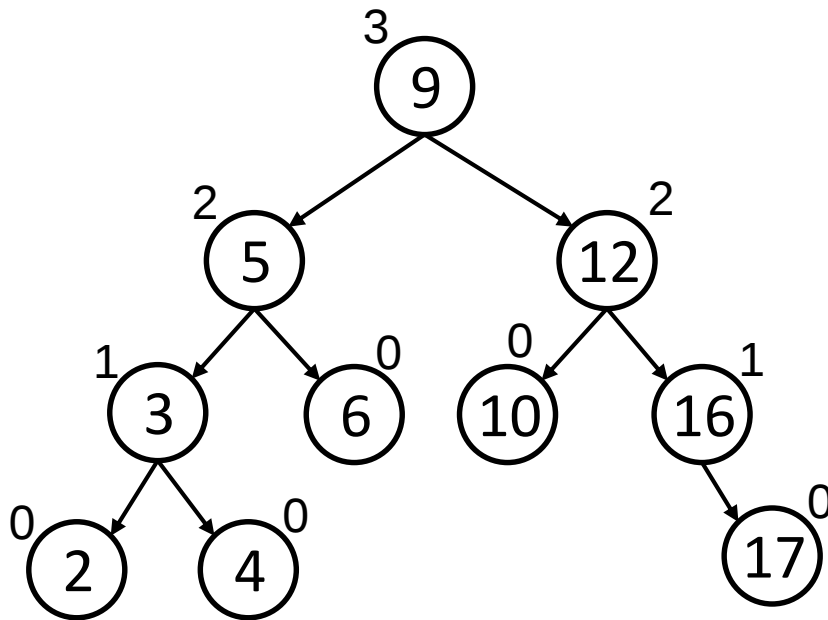
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RCU-HTM: Asynchronized traversals

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Why do we need synchronization for the traversals at the first place?

- Concurrent rotations may lead traversals to a wrong path

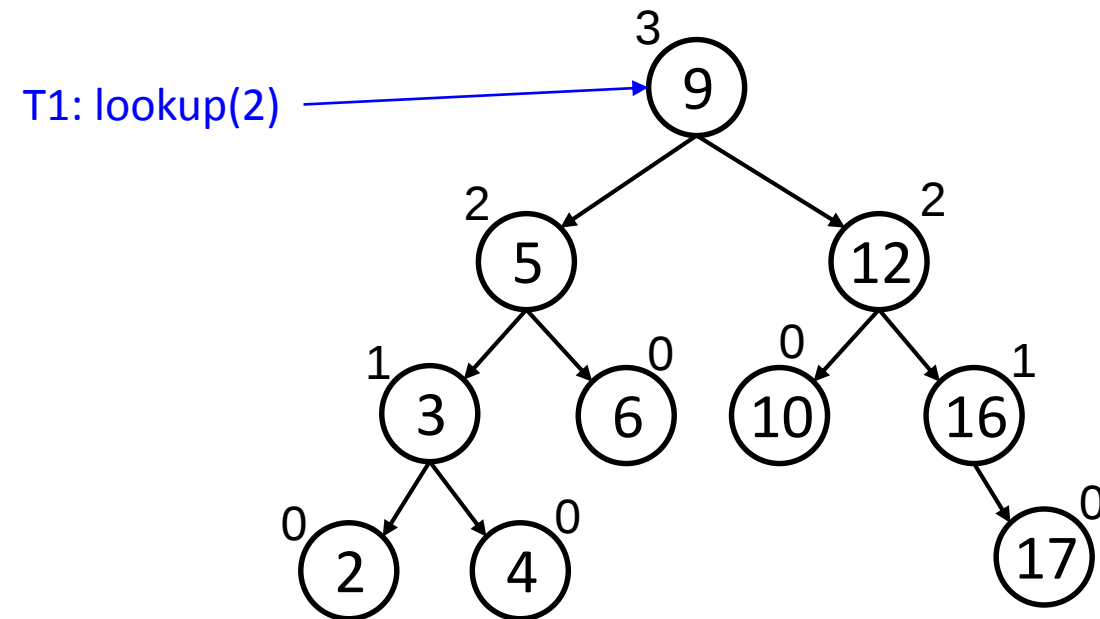


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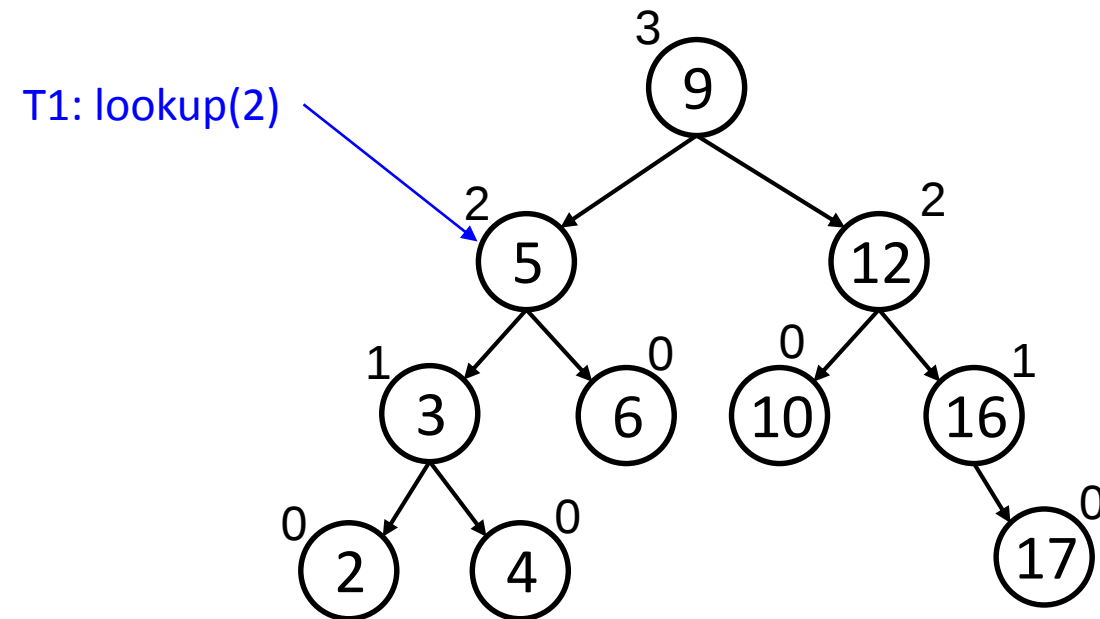


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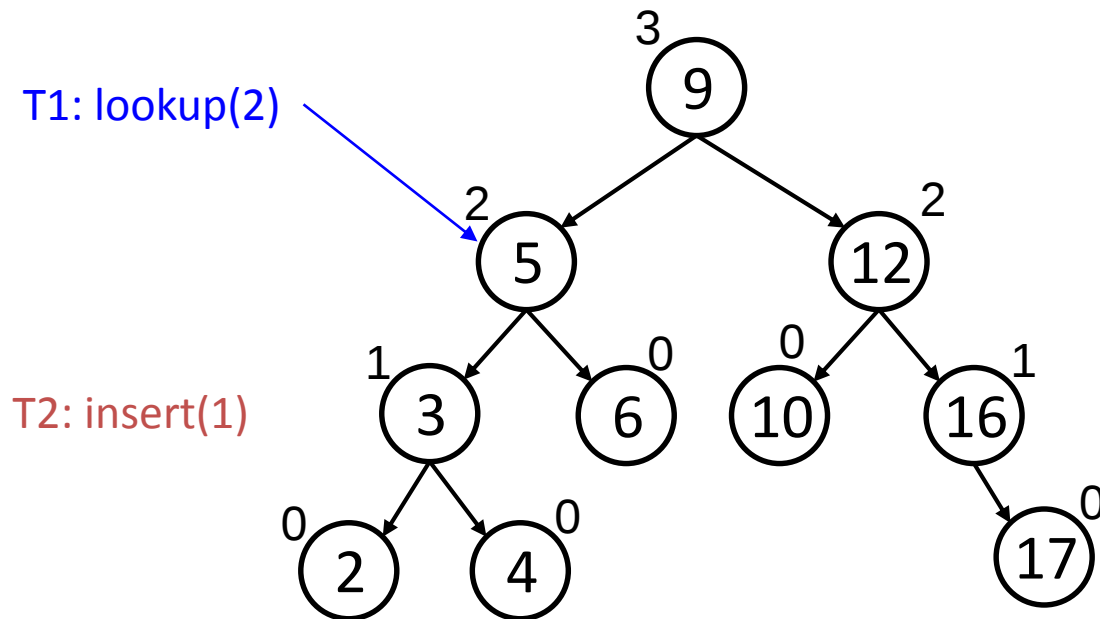


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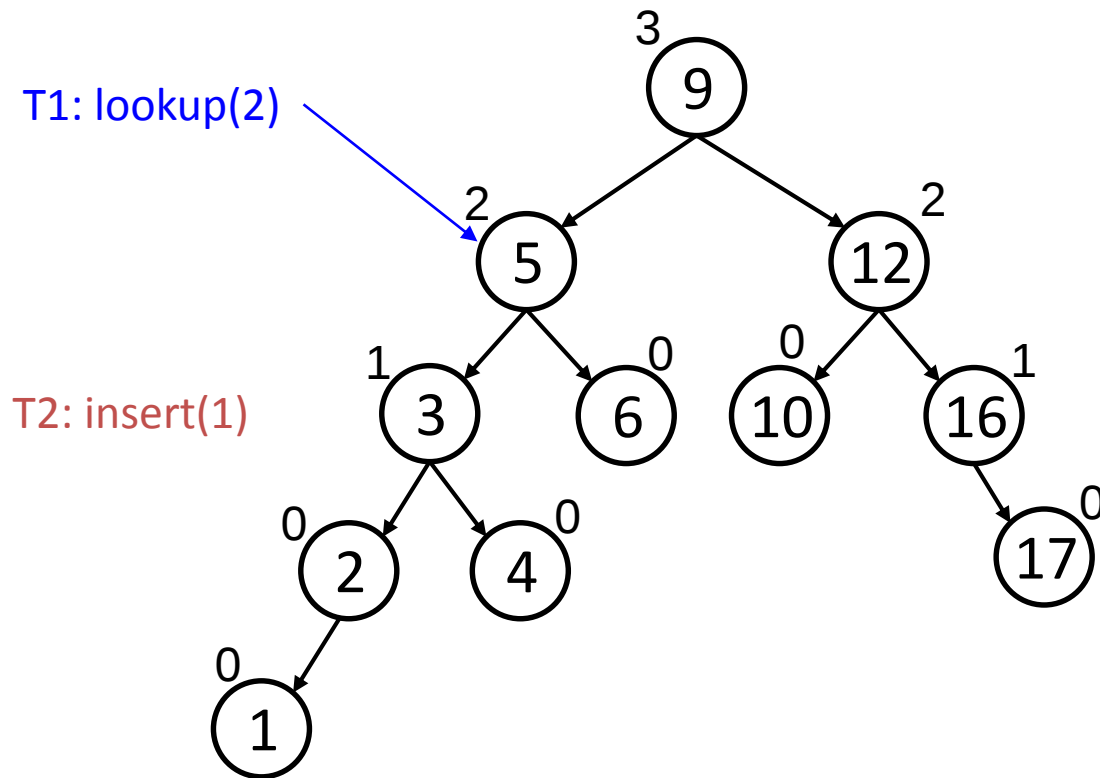


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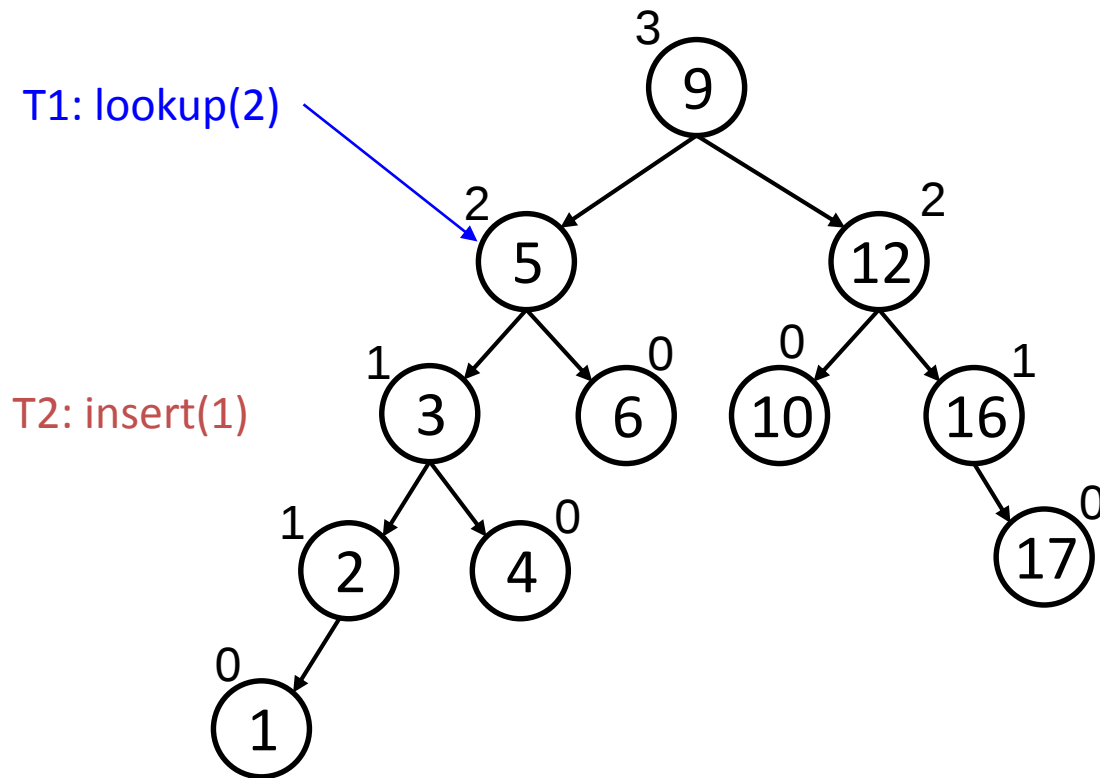


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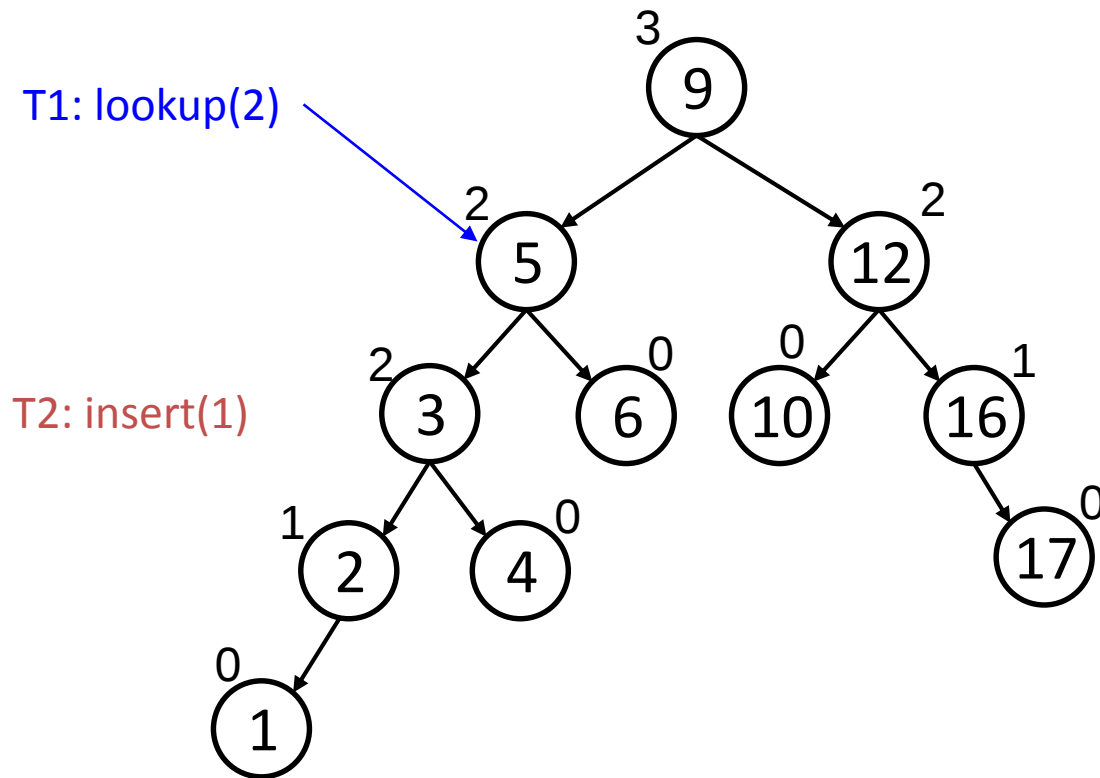


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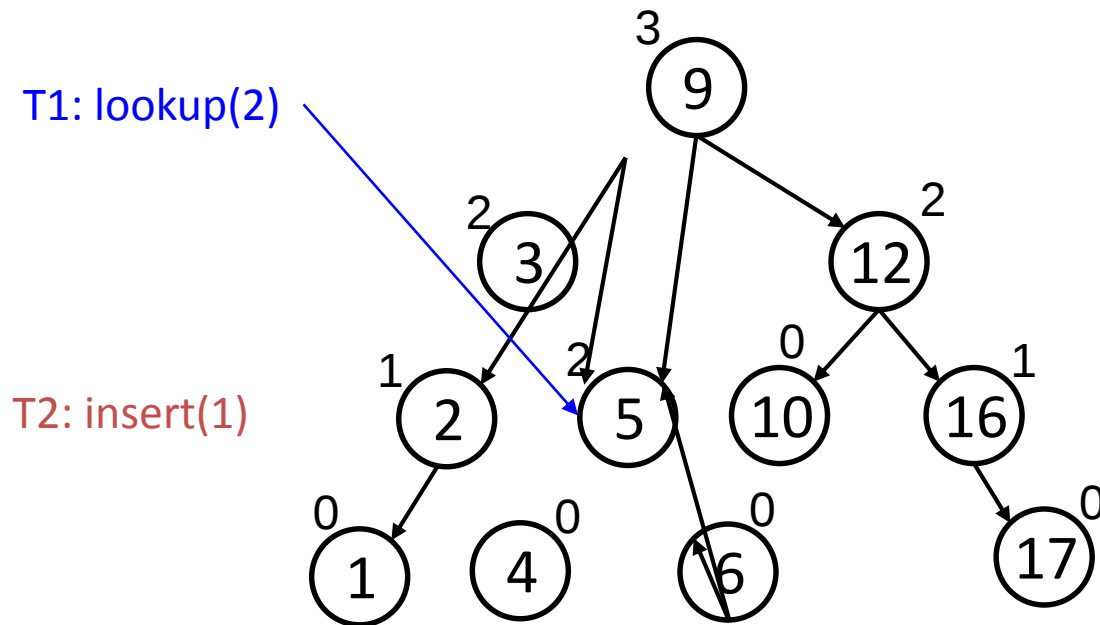


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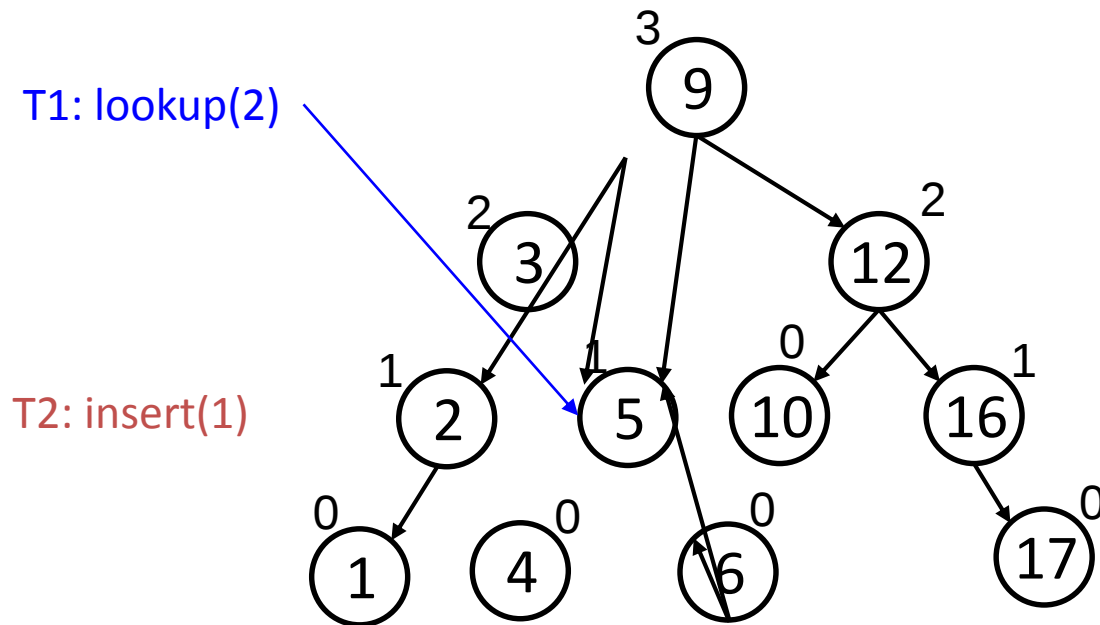


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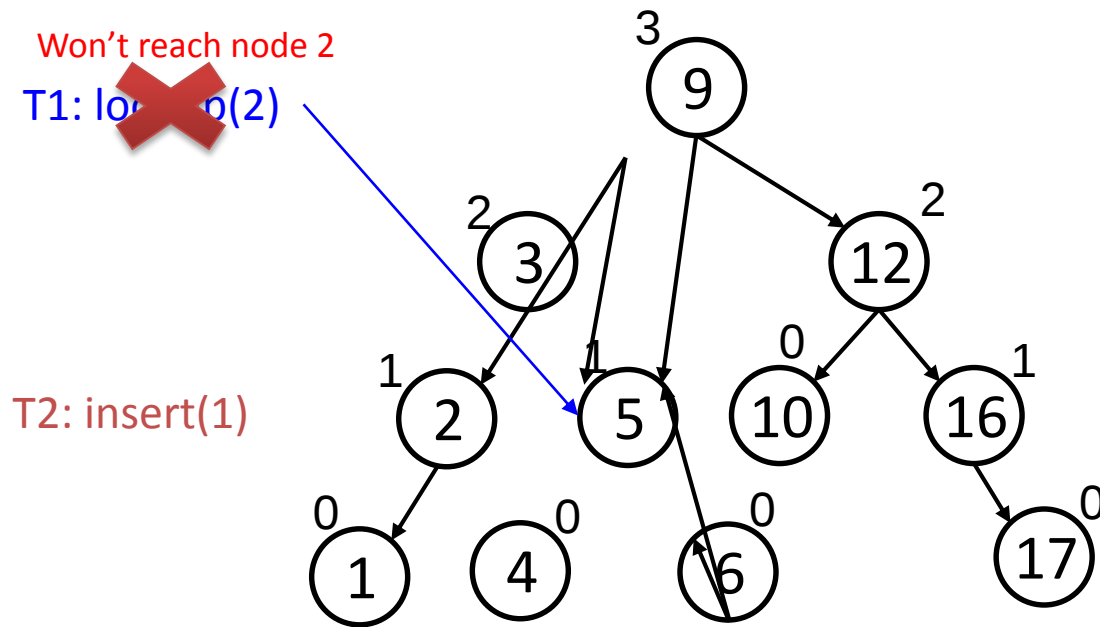


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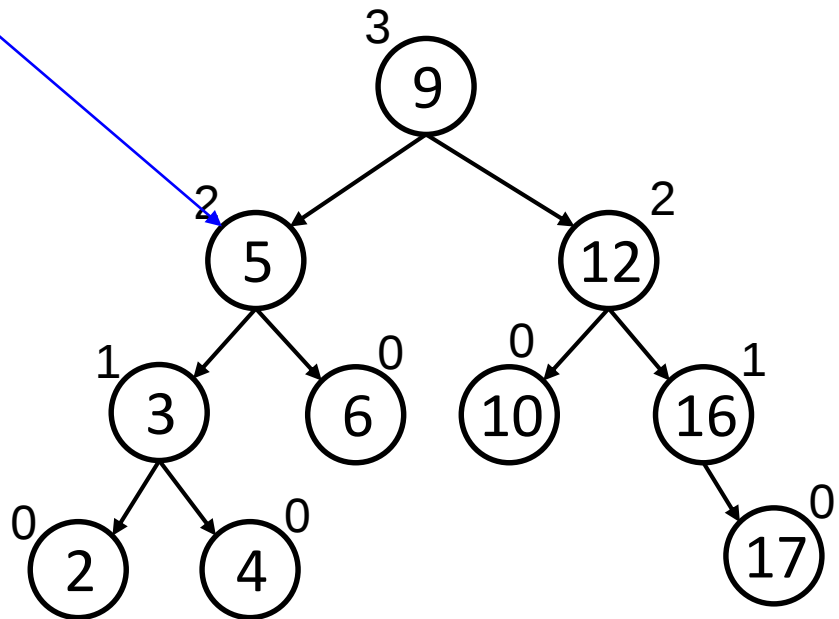
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How does RCU avoid erroneous executions while allowing asynchronized traversals?

- Assume a single updater for now

T1: lookup(2)



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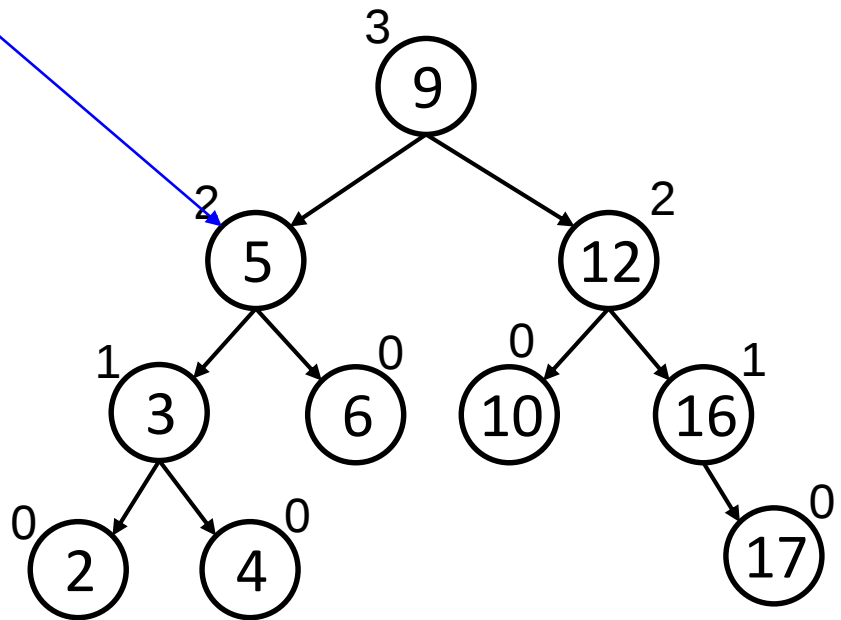
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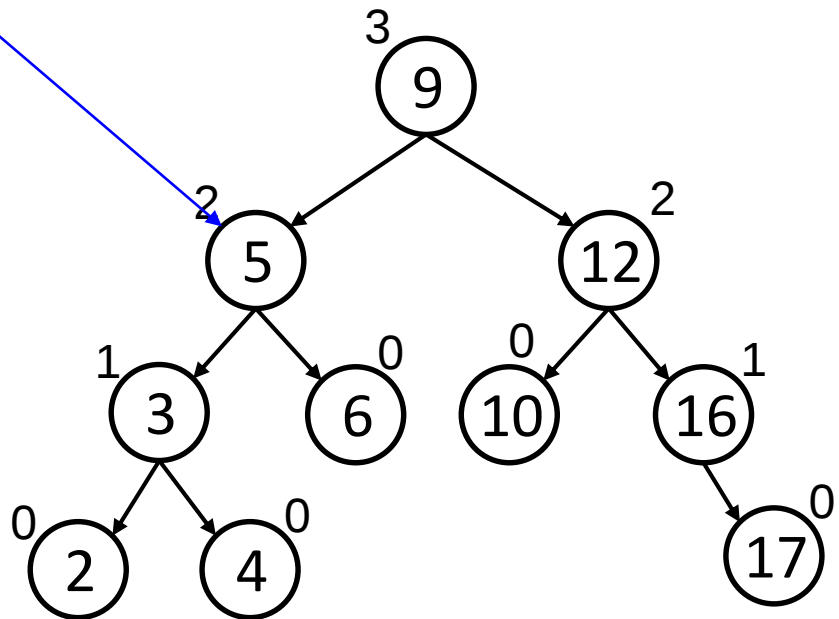
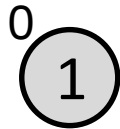
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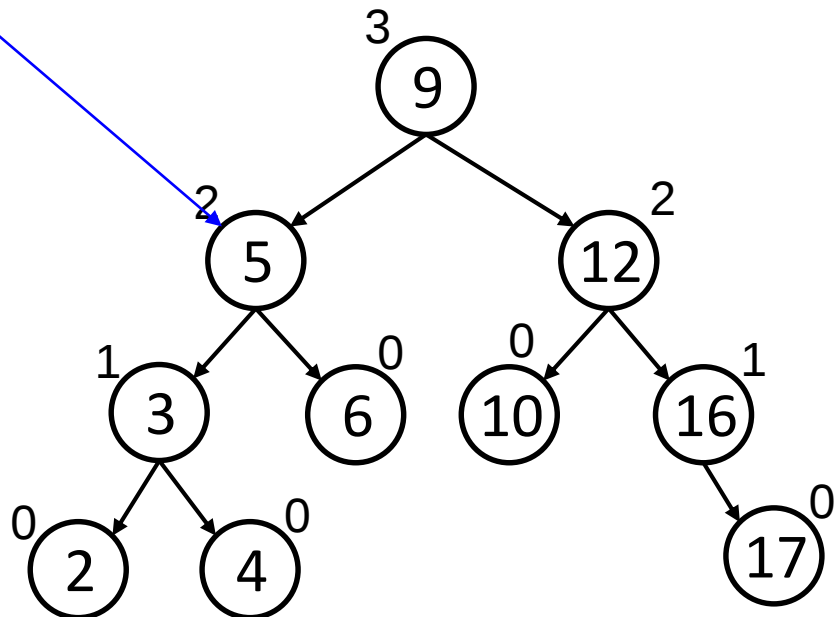
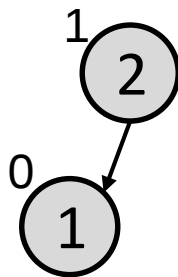
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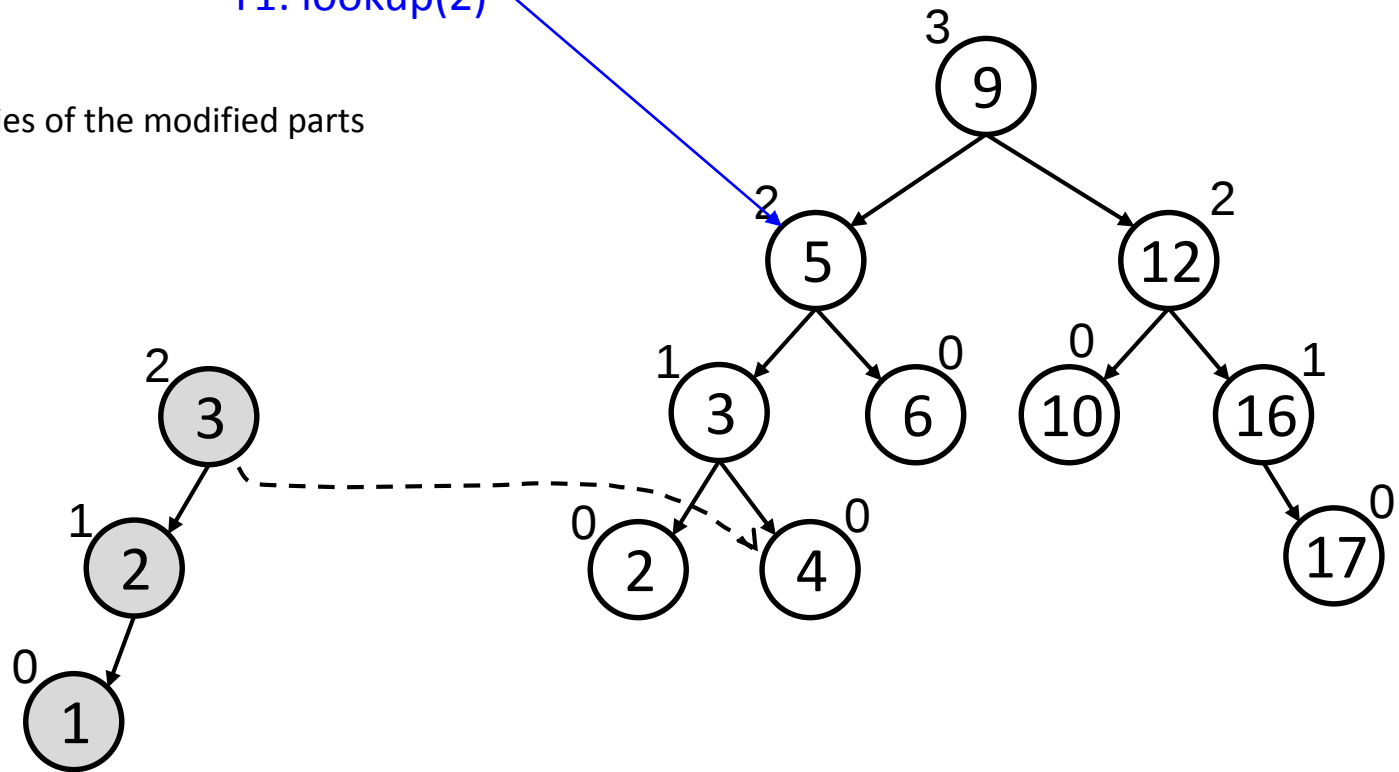
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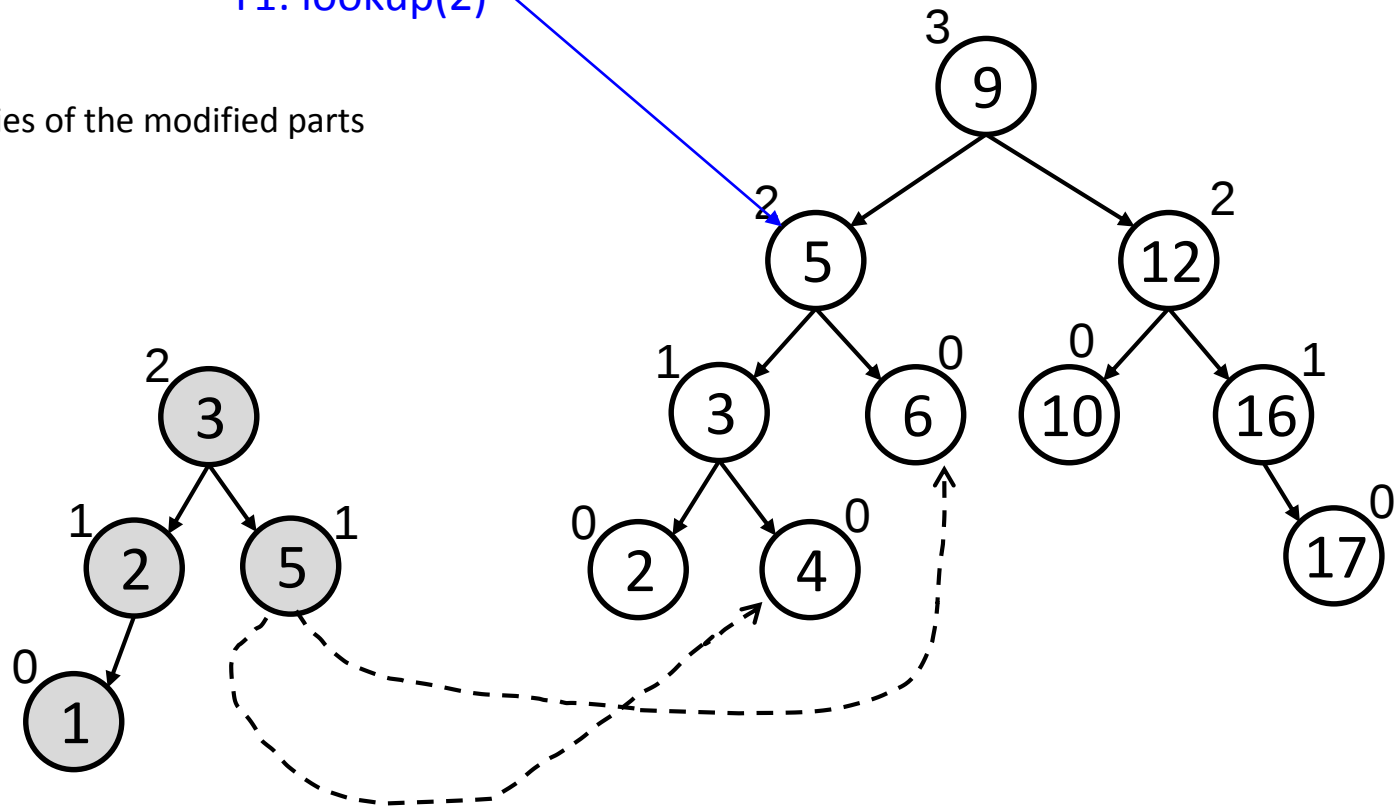
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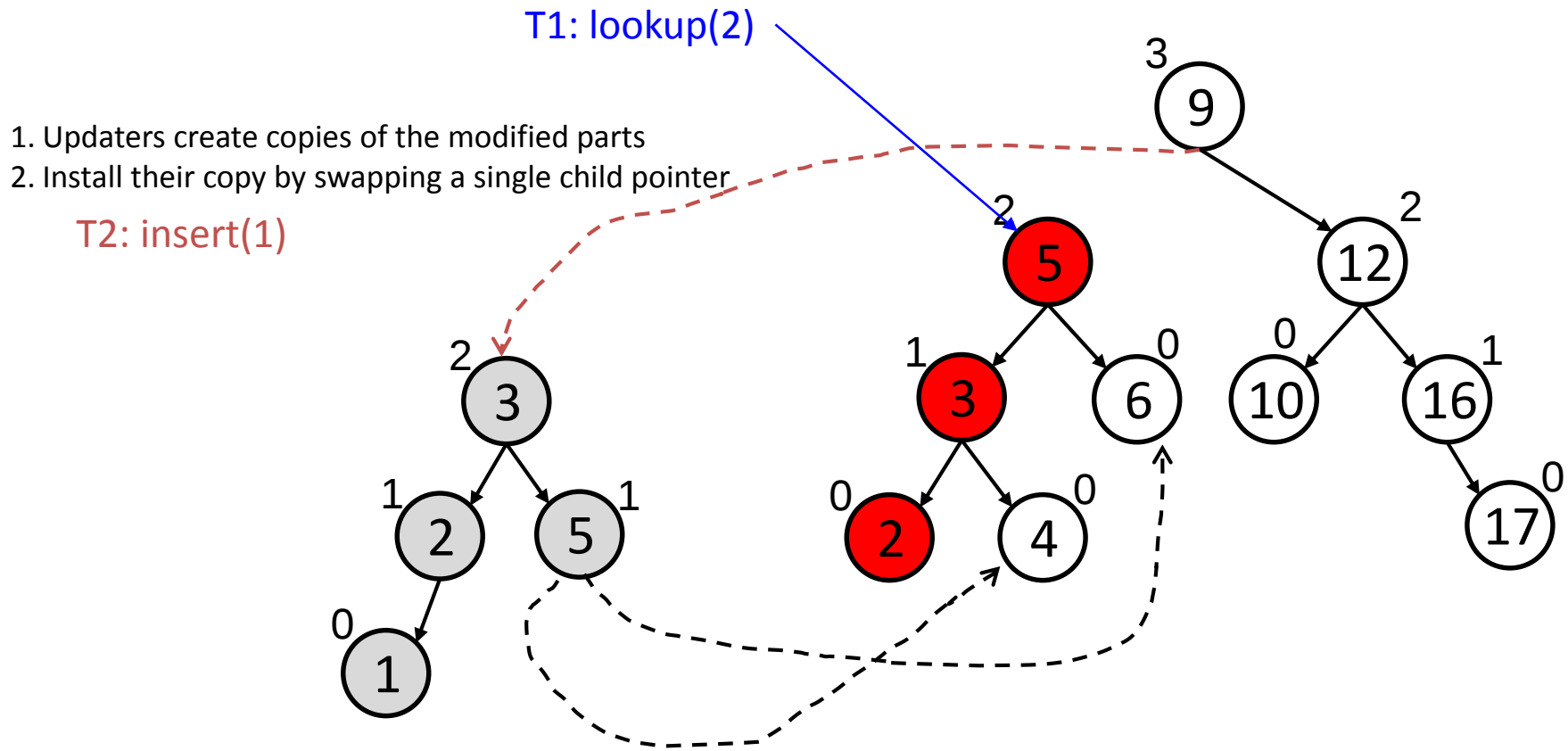


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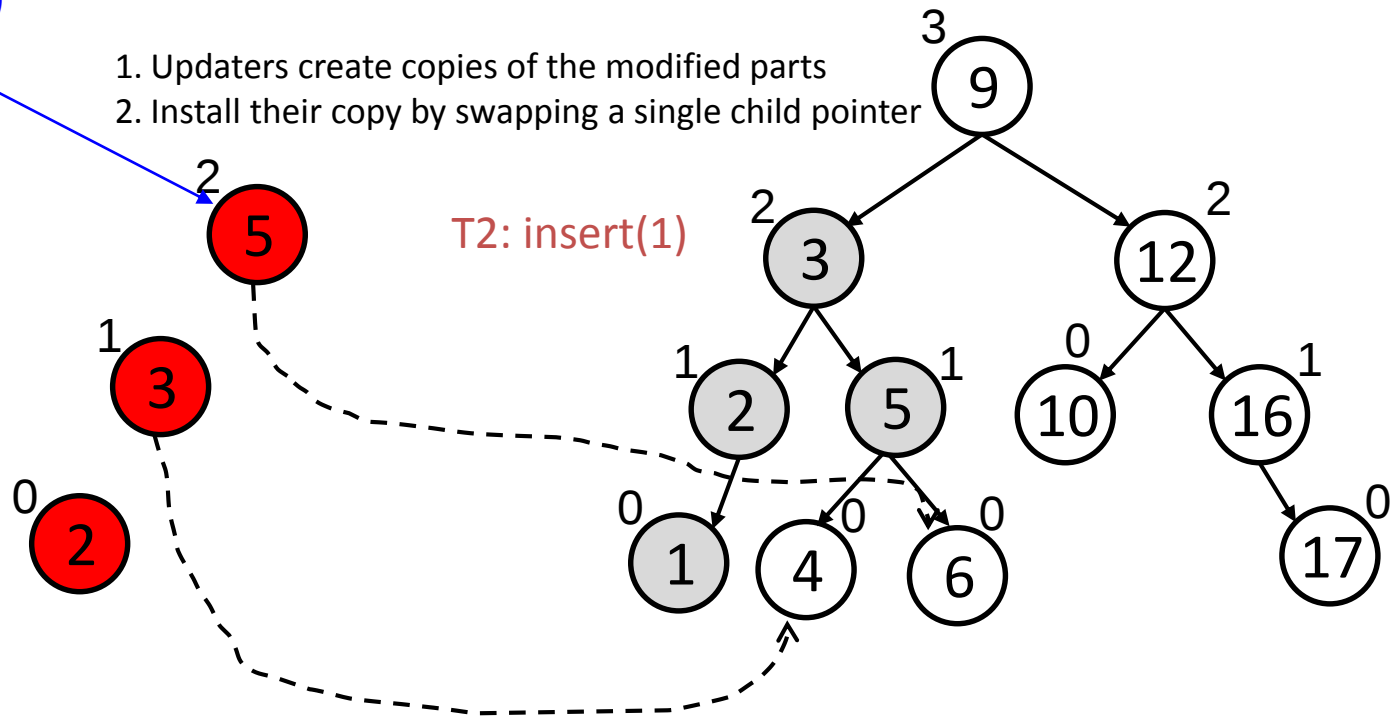
How does RCU avoid erroneous executions while allowing asynchronized traversals?

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2. Install their copy by swapping a single child pointer

T2: insert(1)



RCU-HTM: Asynchronized traversals

RCU-HTM

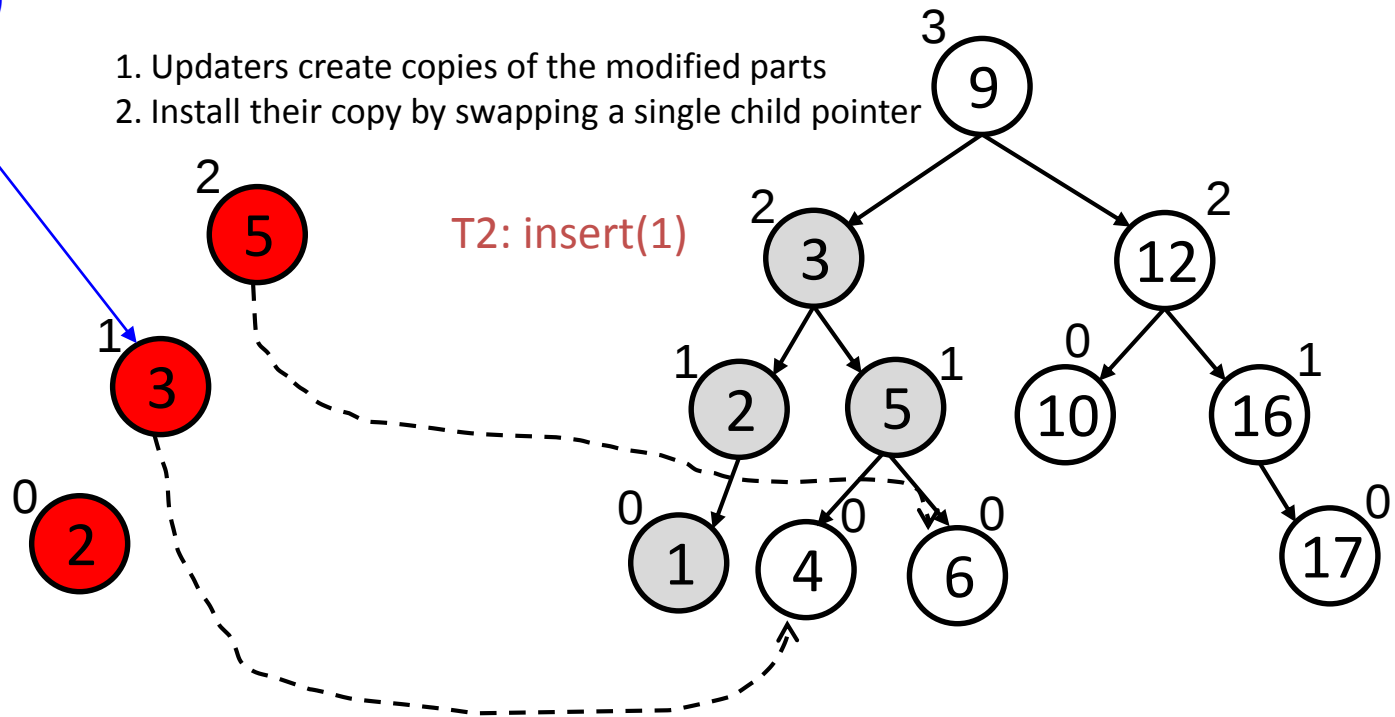
How does RCU avoid erroneous executions while allowing asynchronized traversals?

- Assume a single updater for now

T1: lookup(2)

1. Updaters create copies of the modified parts
2. Install their copy by swapping a single child pointer

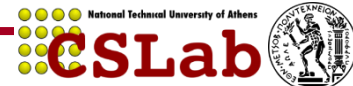
T2: insert(1)



RCU-HTM

- Assume a single updater for now

1. Updaters create copies of the modified parts
2. Install their copy by swapping a single child pointer



RCU-HTM: Asynchronized traversals

RCU-HTM

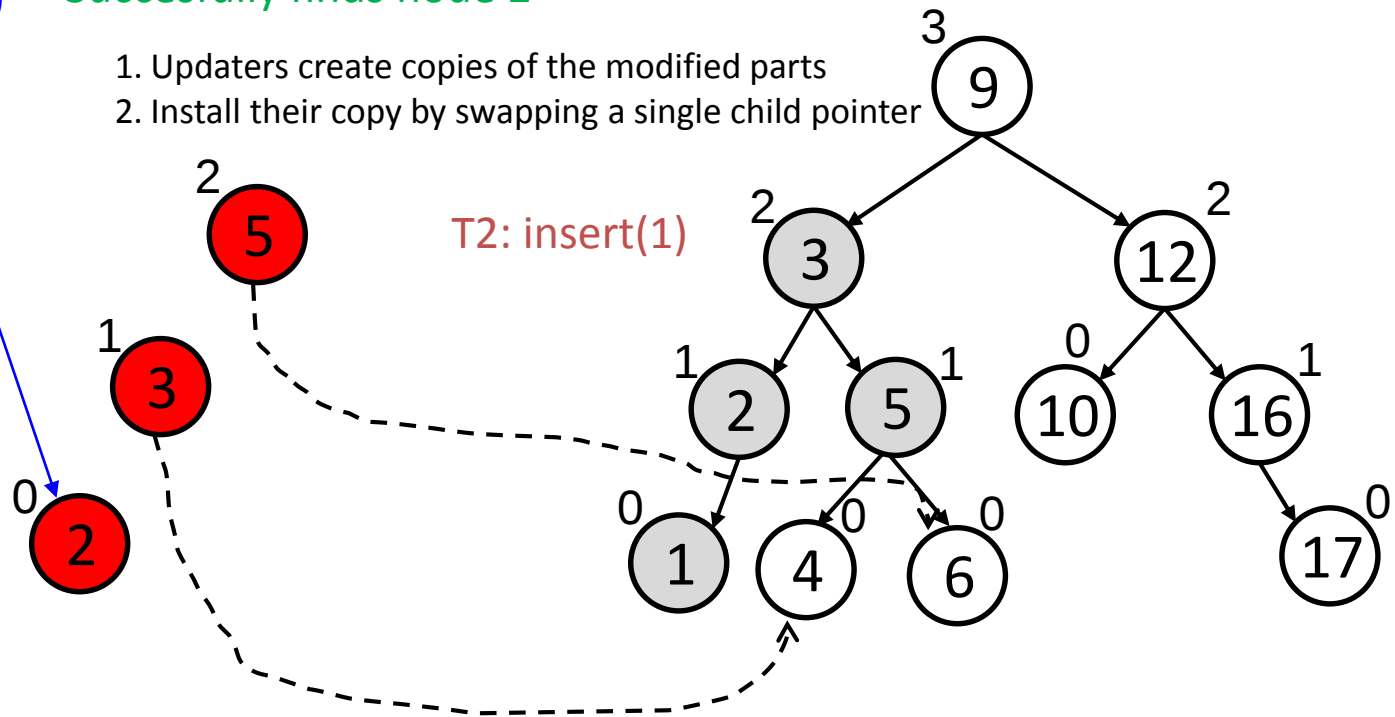
How does RCU avoid erroneous executions while allowing asynchronized traversals?

- Assume a single updater for now

T1: lookup(2) ✓ Successfully finds node 2

1. Updaters create copies of the modified parts
2. Install their copy by swapping a single child pointer

T2: insert(1)

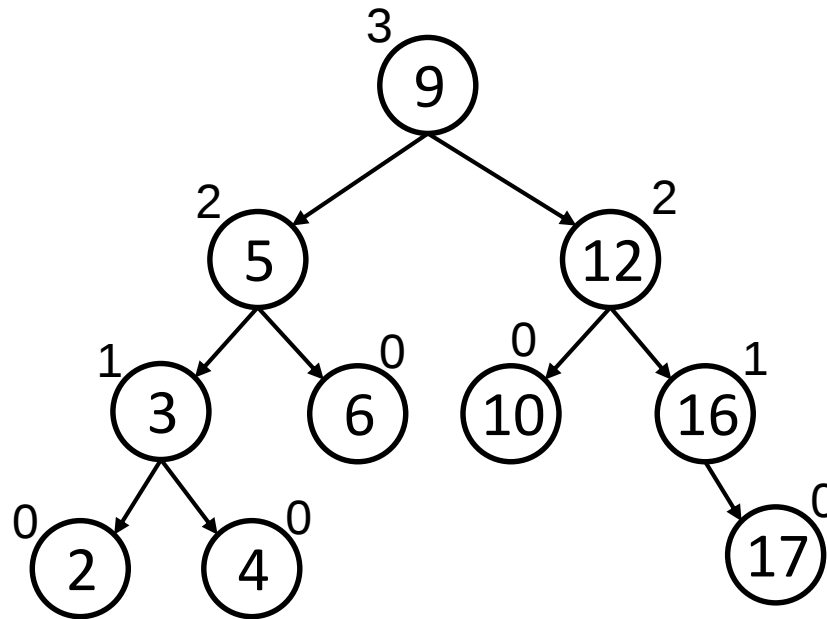


RCU-HTM: Multiple Updaters

RCU-HTM

The previous example assumed a single updater

- If multiple updaters were allowed, modifications could be “lost”



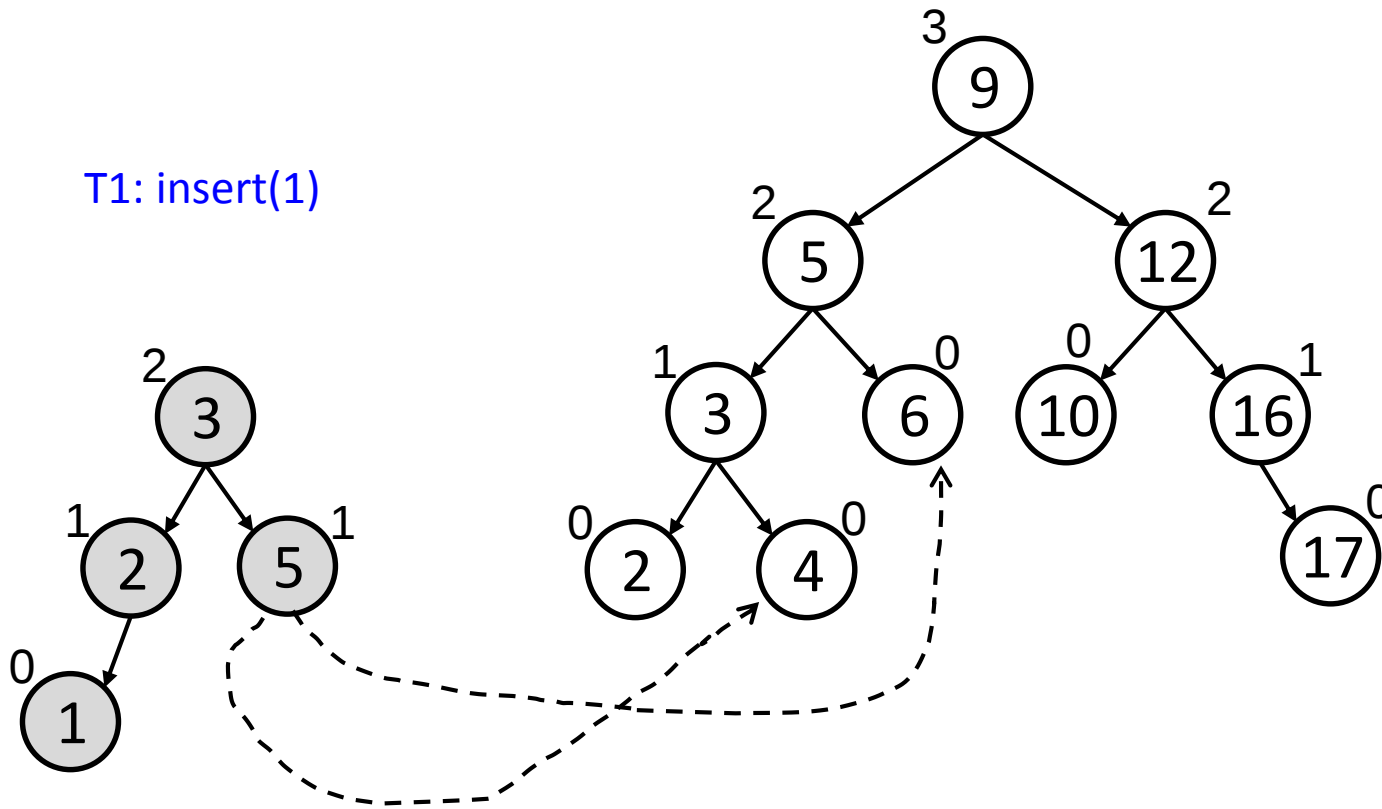
RCU-HTM: Multiple Updaters

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- If multiple updaters were allowed, modifications could be “lost”

T1: insert(1)



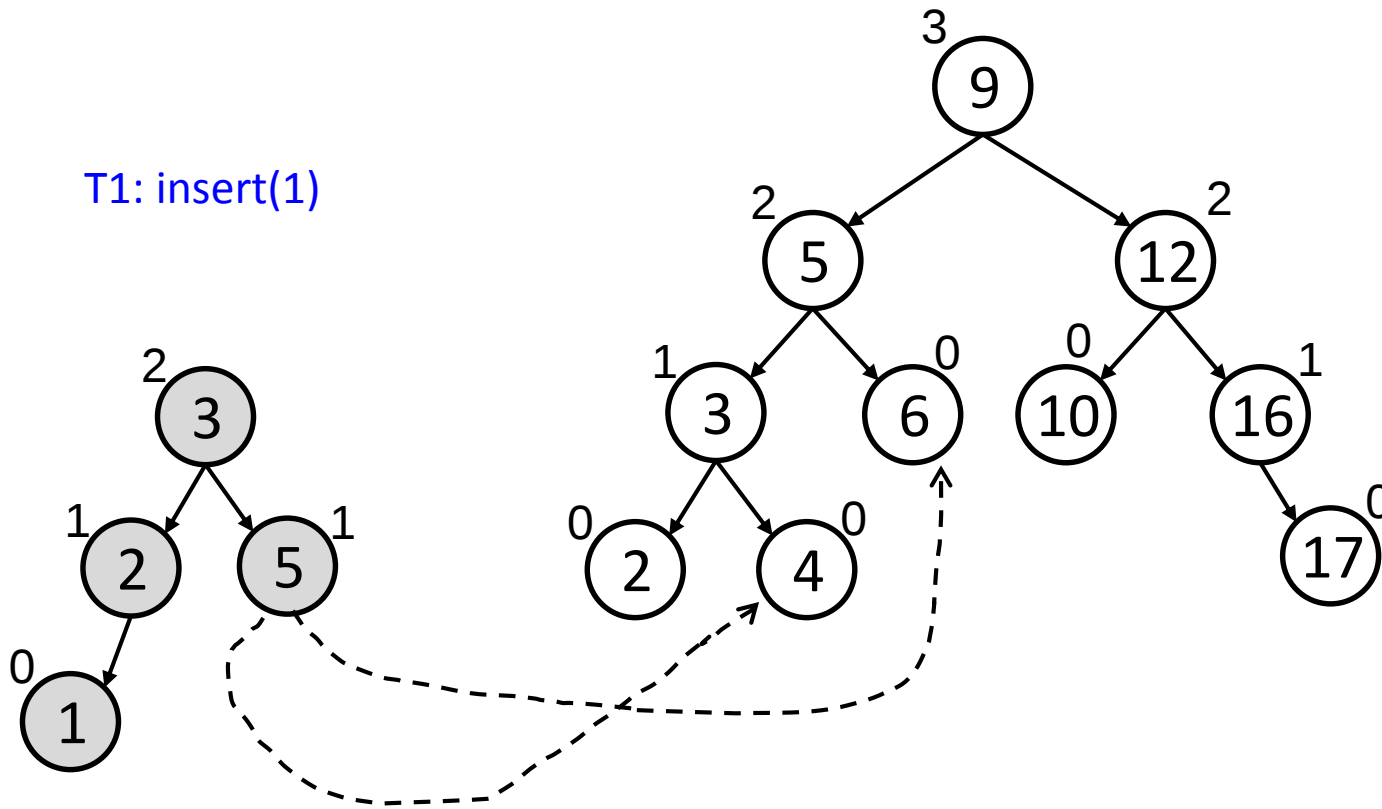
RCU-HTM: Multiple Updaters

RCU-HTM

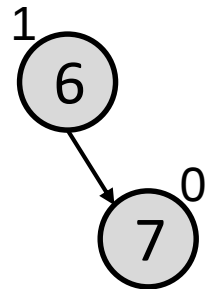
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T1: insert(1)



T2: insert(7)

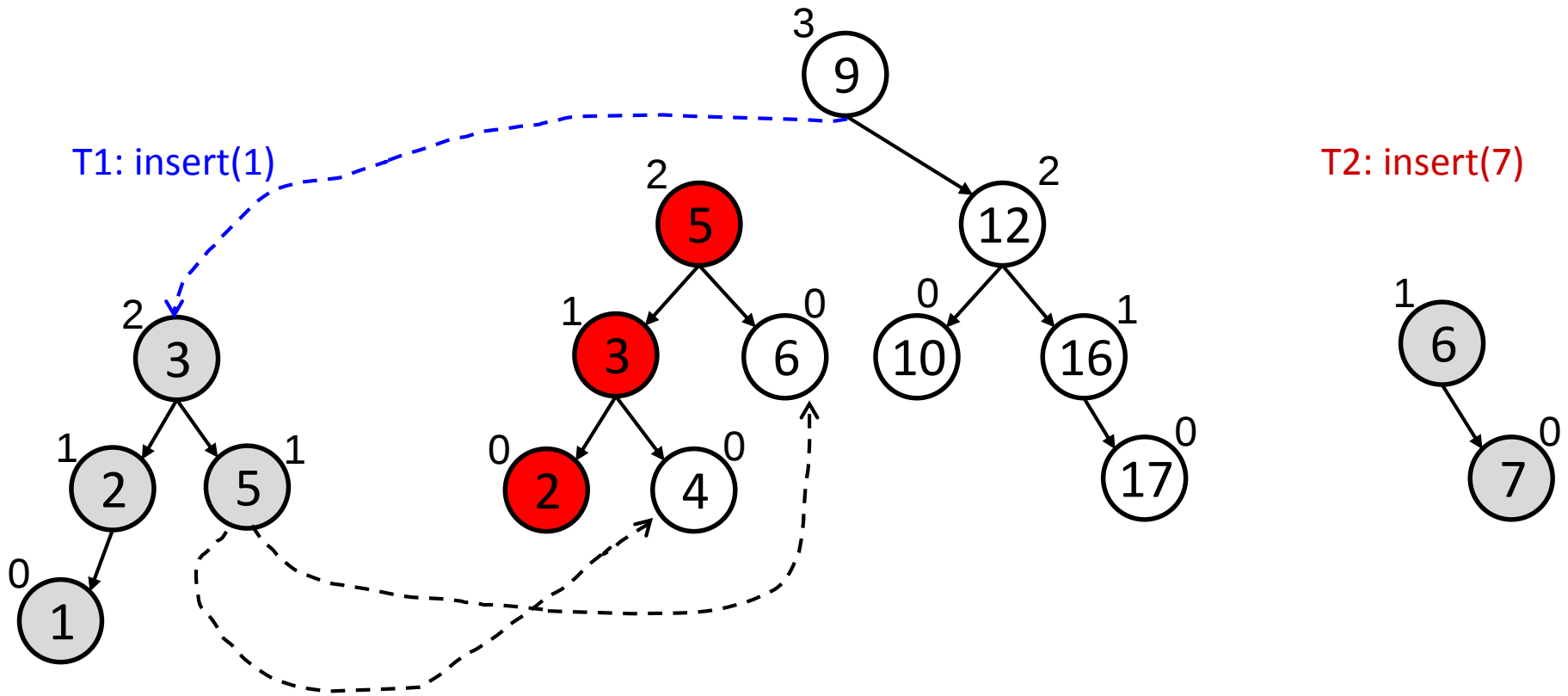


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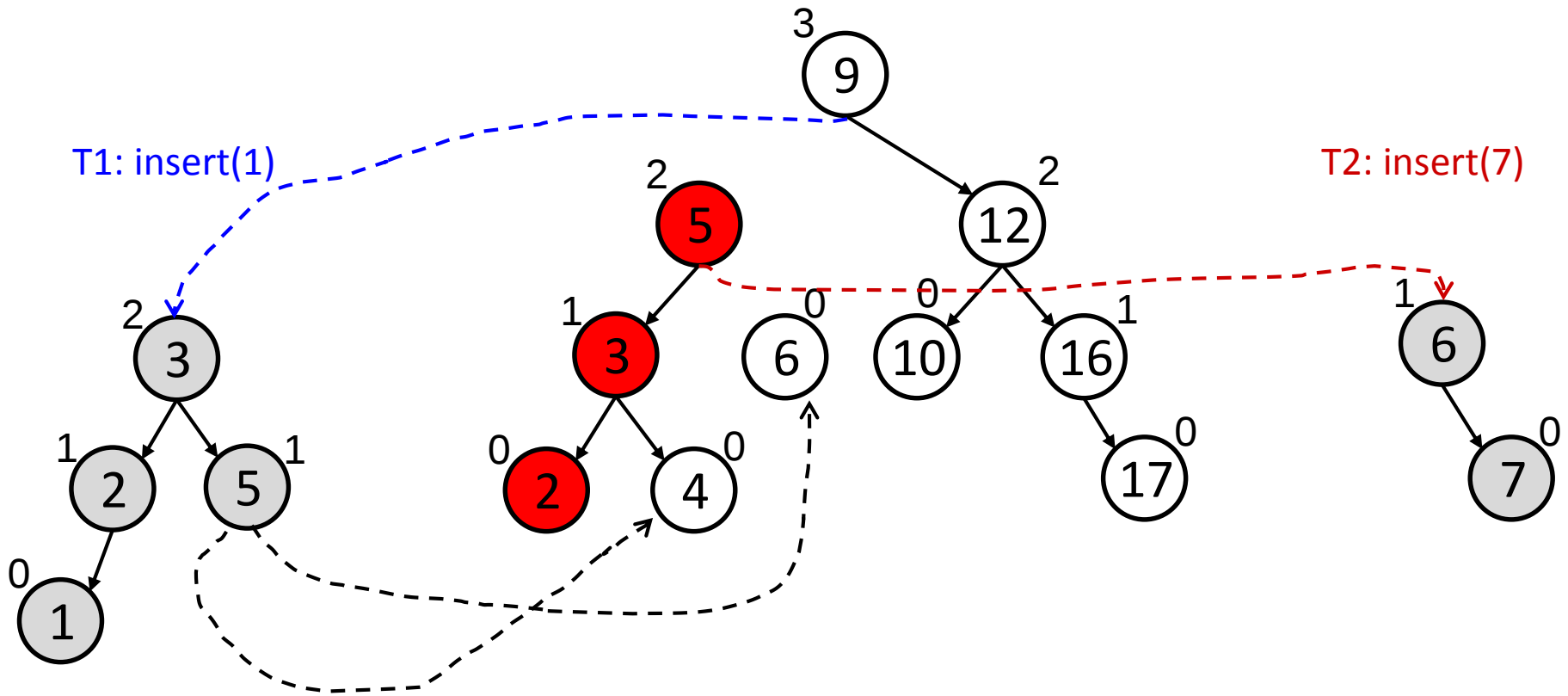


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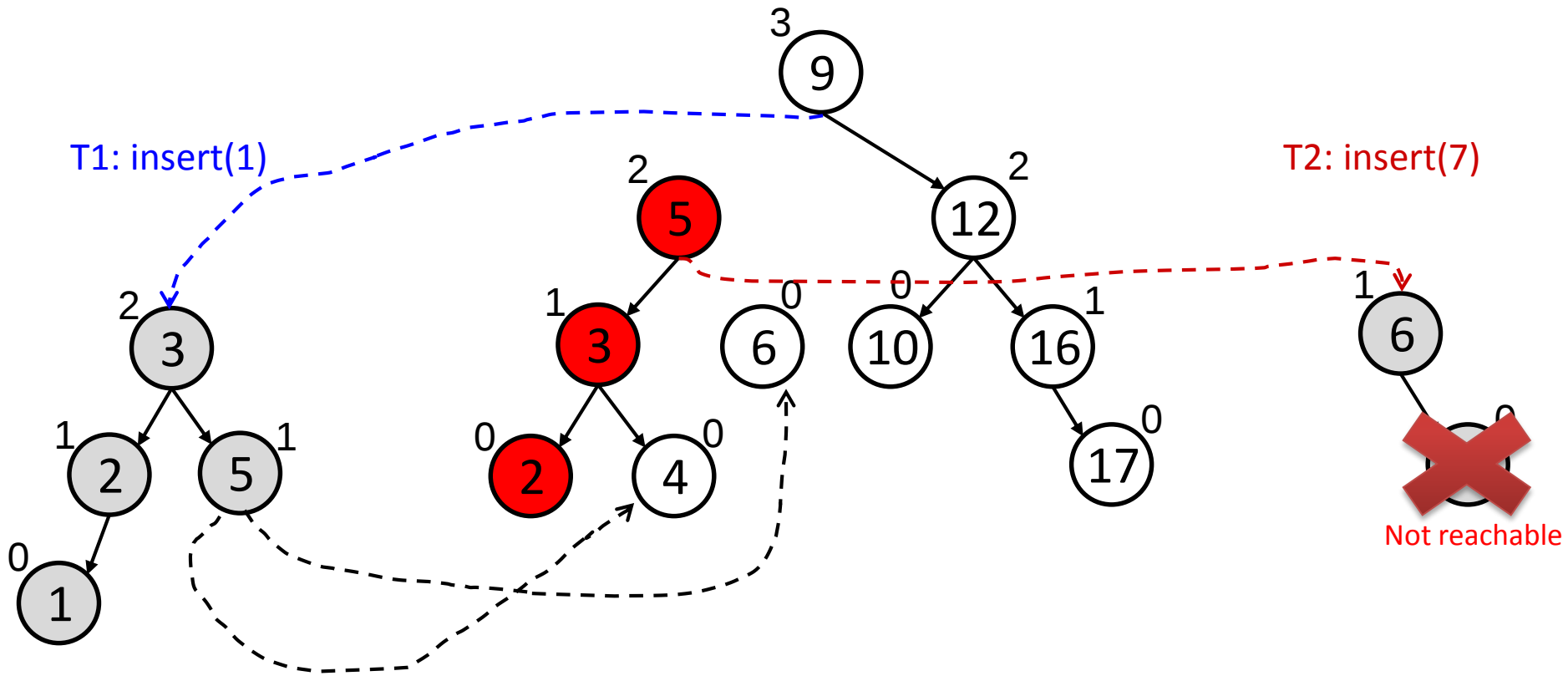


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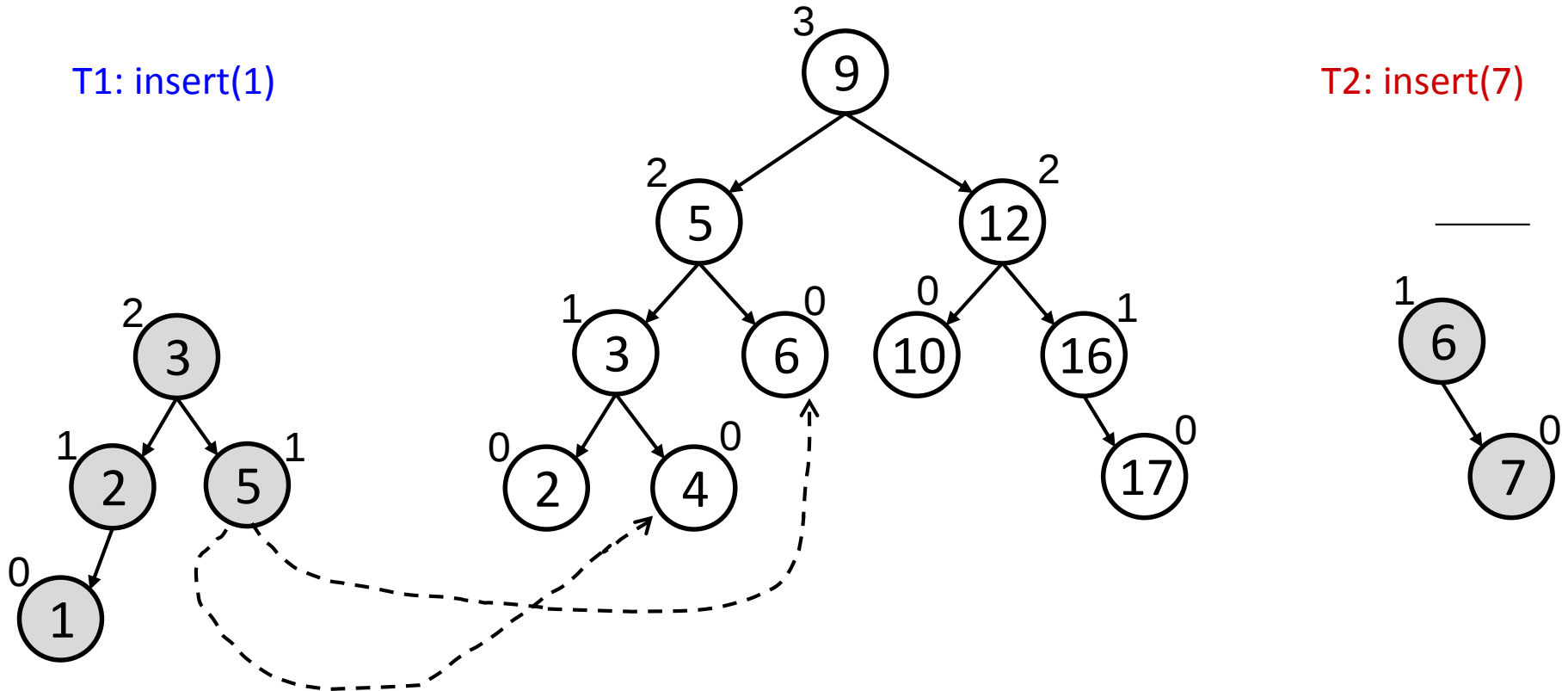
RCU-HTM: Multiple Updaters

RCU-HTM overcomes the problem of “lost” updates by exploiting HTM in the following way:

- Updaters keep track of the state of the traversed and the copied nodes, i.e., the addresses of the children pointers
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 - validation and installation are performed atomically using an HTM transaction

T1: insert(1)

T2: insert(7)



RCU-HTM: Multiple Updaters

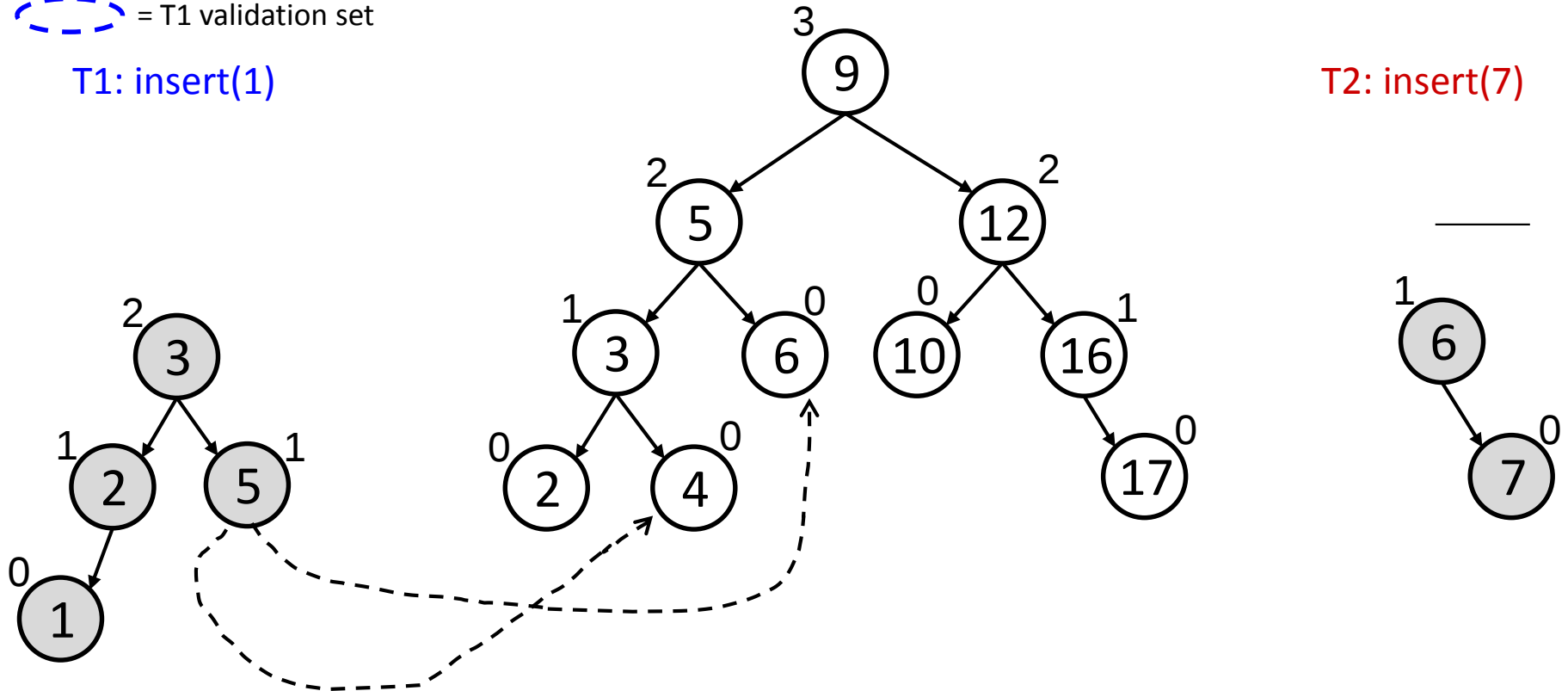
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 = T1 validation set

T1: insert(1)

T2: insert(7)



RCU-HTM: Multiple Updaters

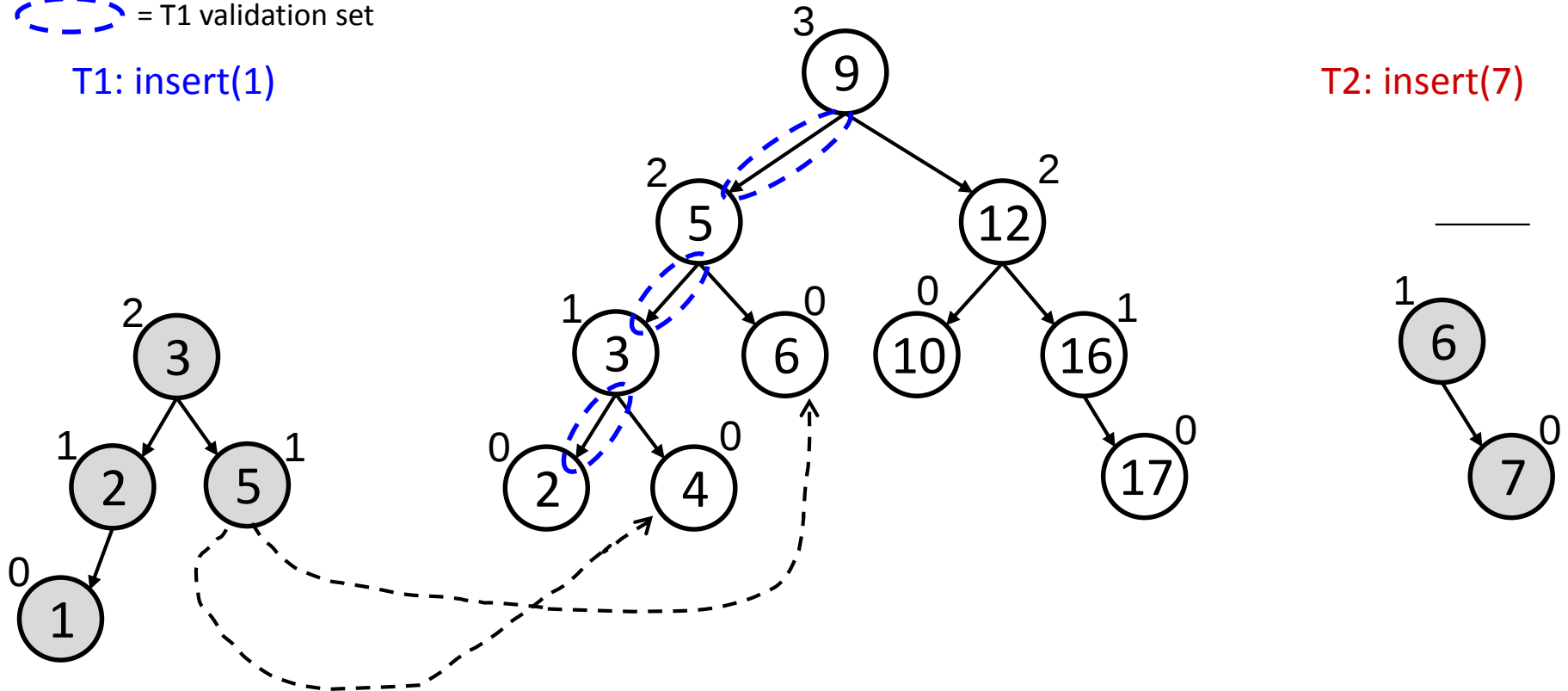
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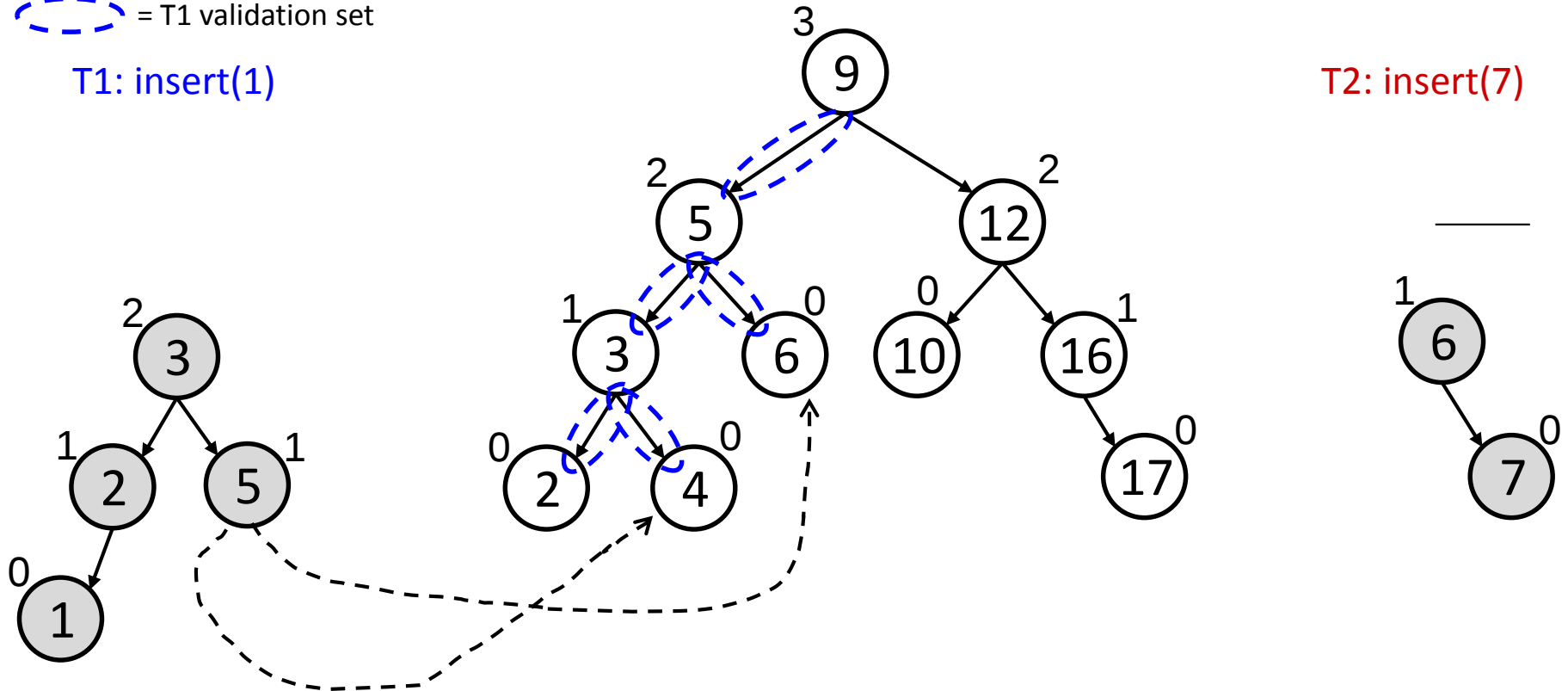
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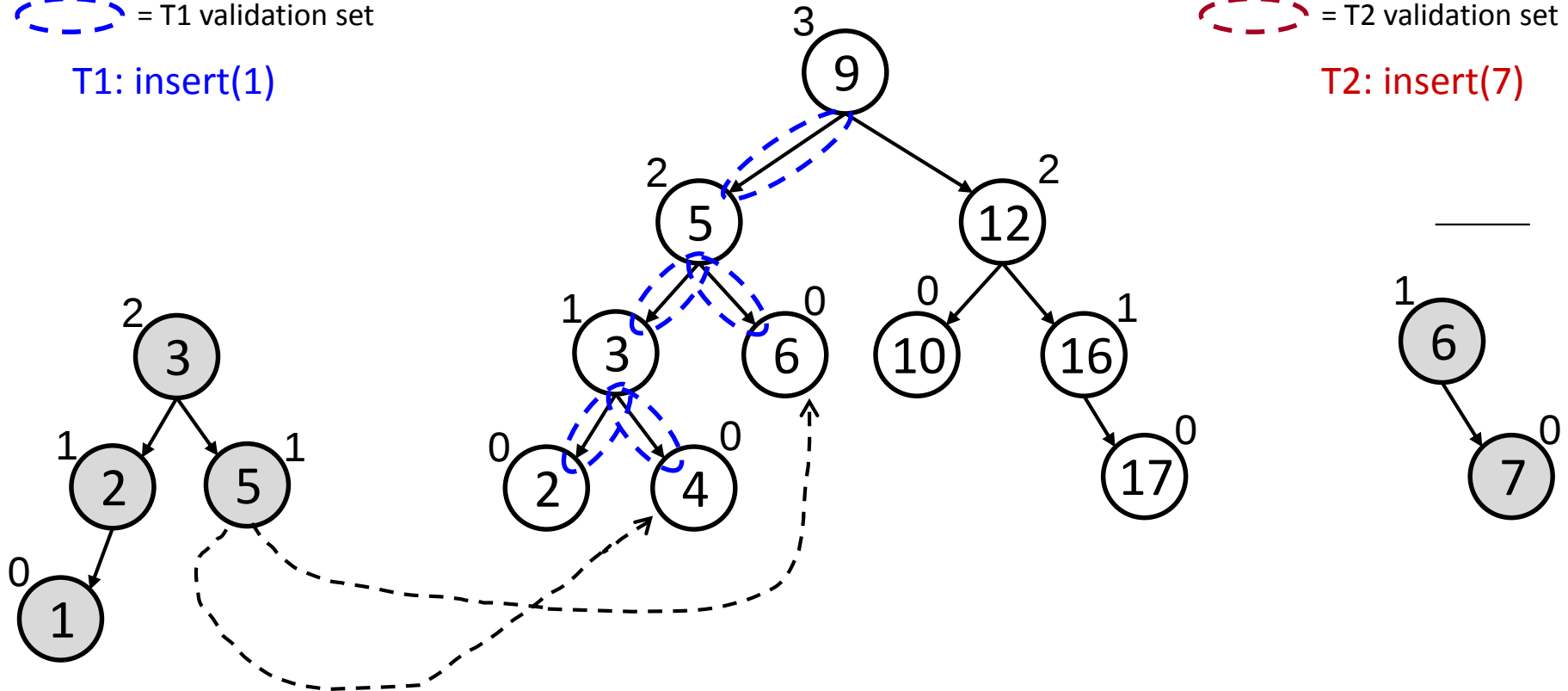
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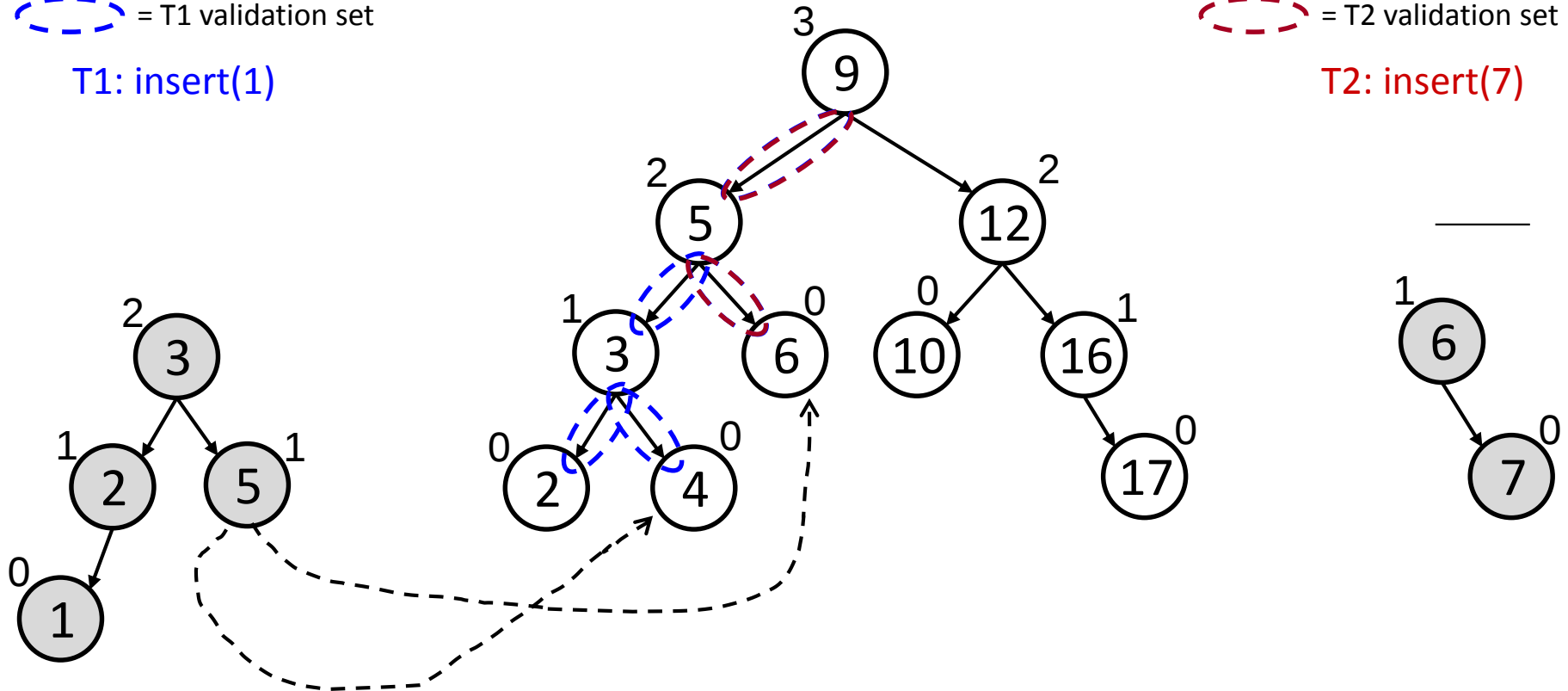
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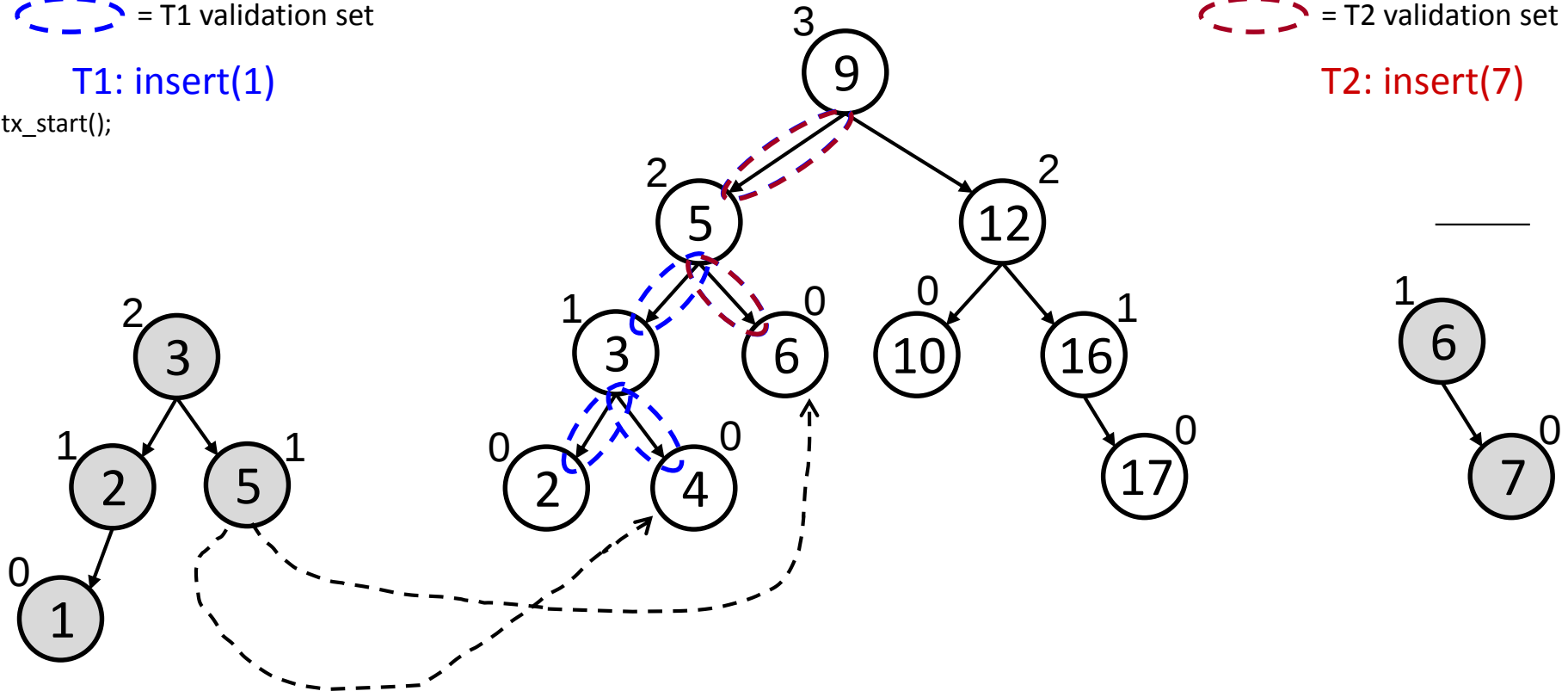
 = T1 validation set

T1: insert(1)

tx_start();

 = T2 validation set

T2: insert(7)



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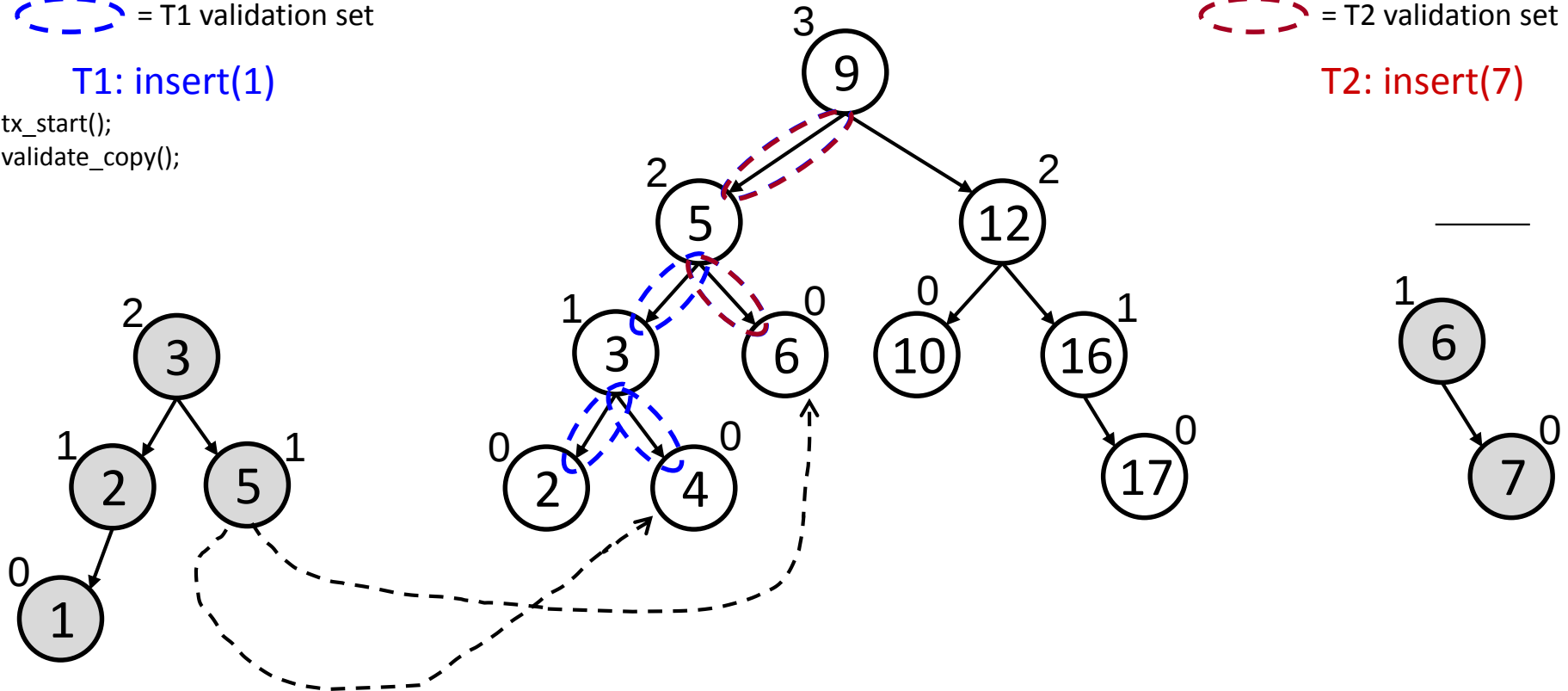
 = T1 validation set

T1: insert(1)

tx_start();
validate_copy();

 = T2 validation set

T2: insert(7)



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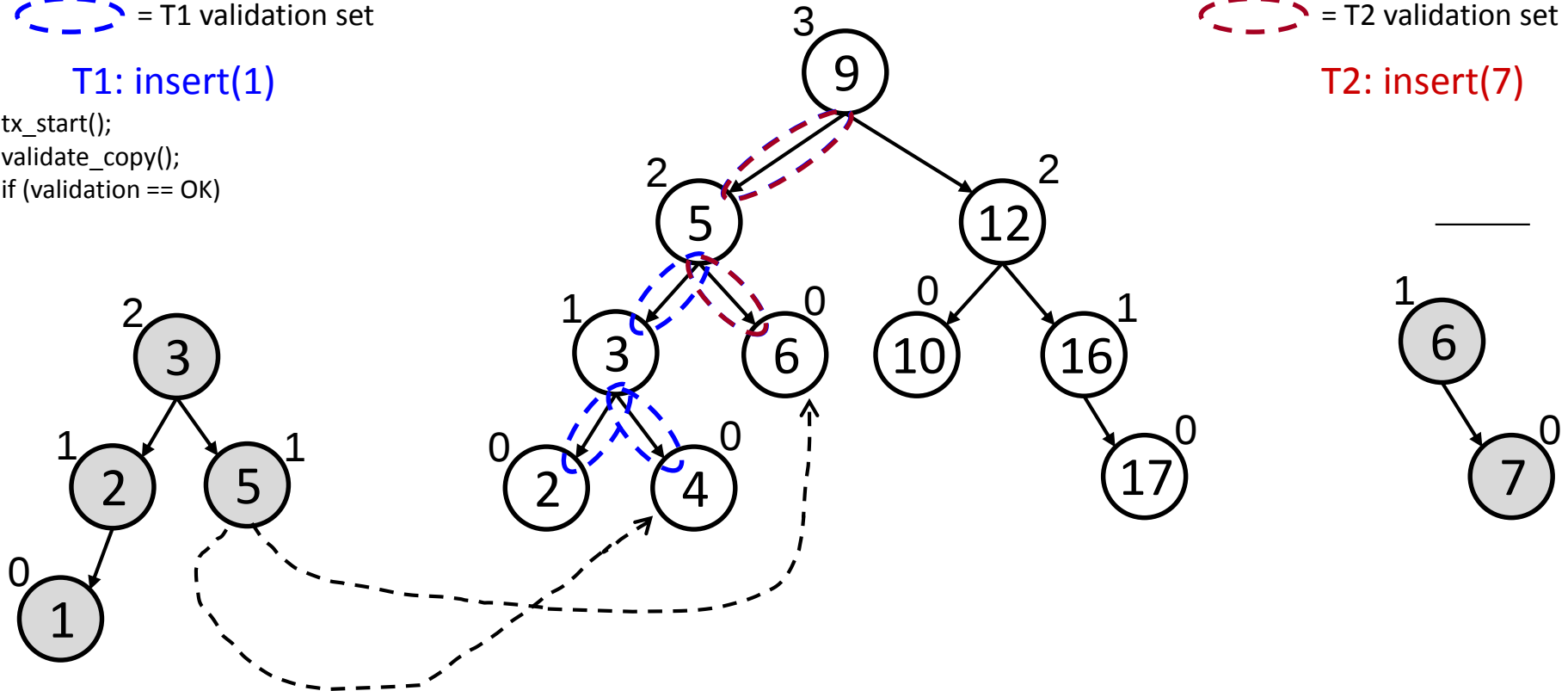
 = T1 validation set

T1: insert(1)

```
tx_start();  
validate_copy();  
if (validation == OK)
```

 = T2 validation set

T2: insert(7)



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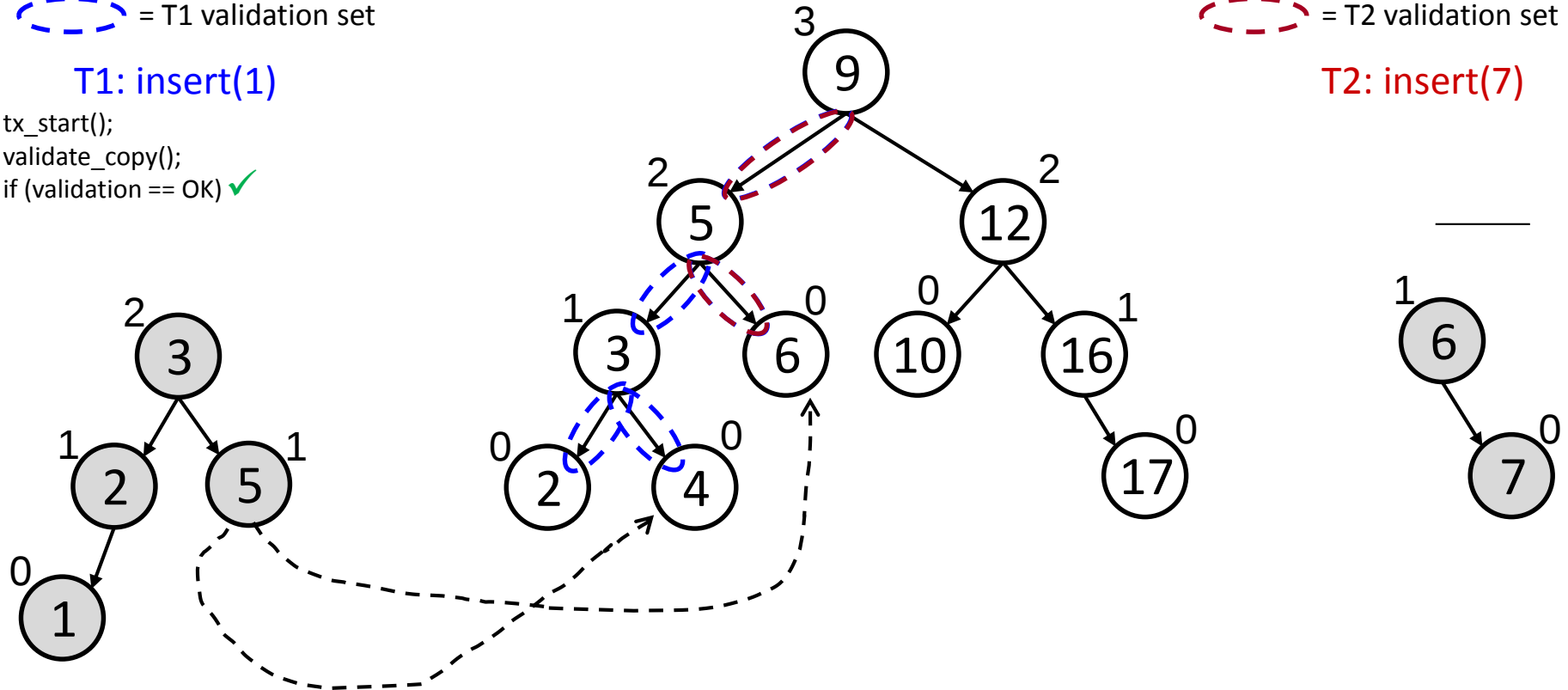
 = T1 validation set

T1: insert(1)

```
tx_start();  
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if (validation == OK) ✓
```

 = T2 validation set

T2: insert(7)



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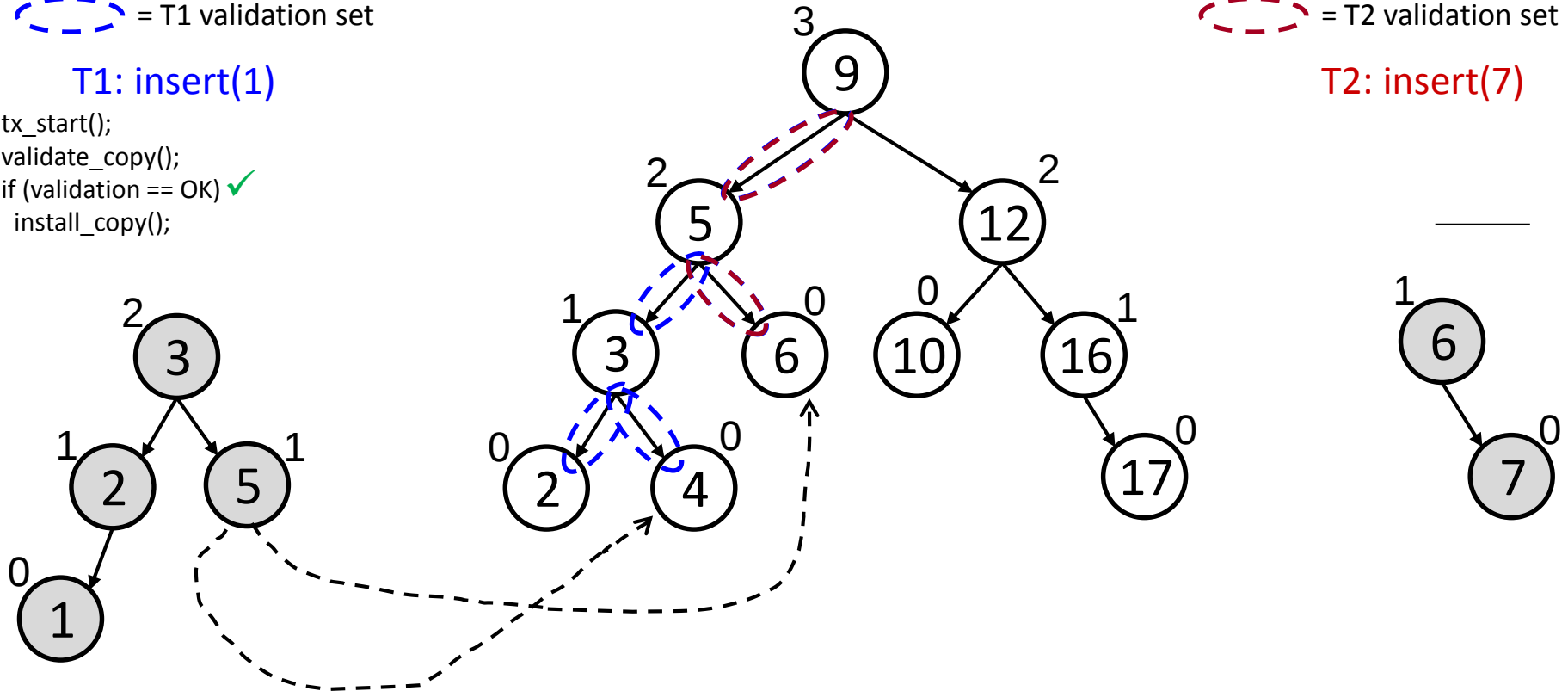
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T1: insert(1)

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install_copy();
```

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T2: insert(7)



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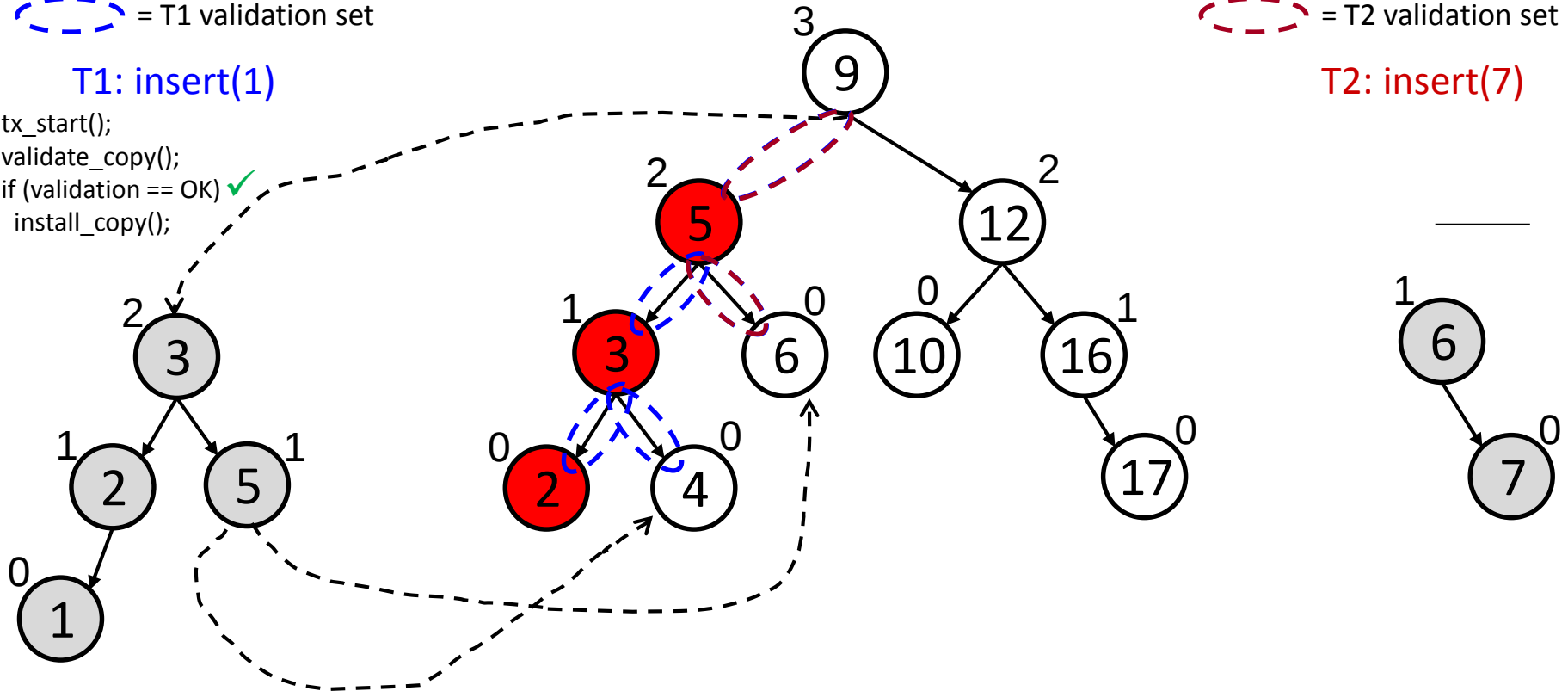
 = T1 validation set

T1: insert(1)

 = T2 validation set

T2: insert(7)

```
tx_start();  
validate_copy();  
if (validation == OK) ✓  
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```



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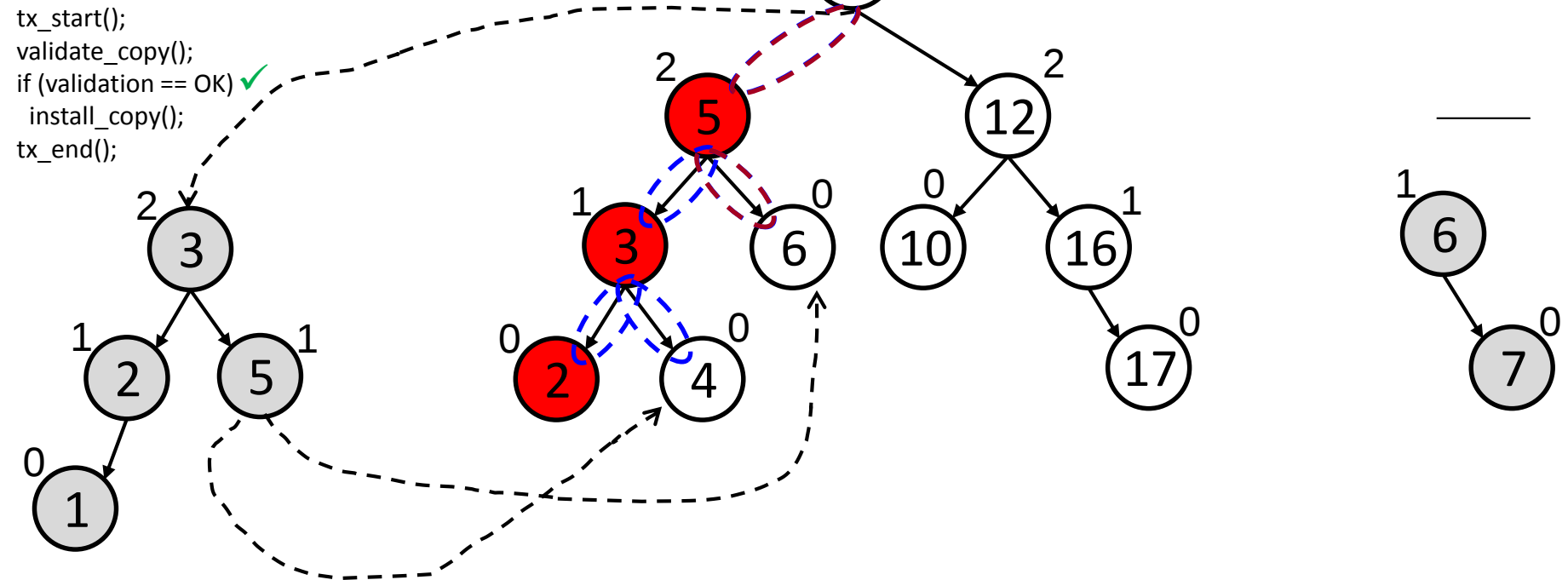
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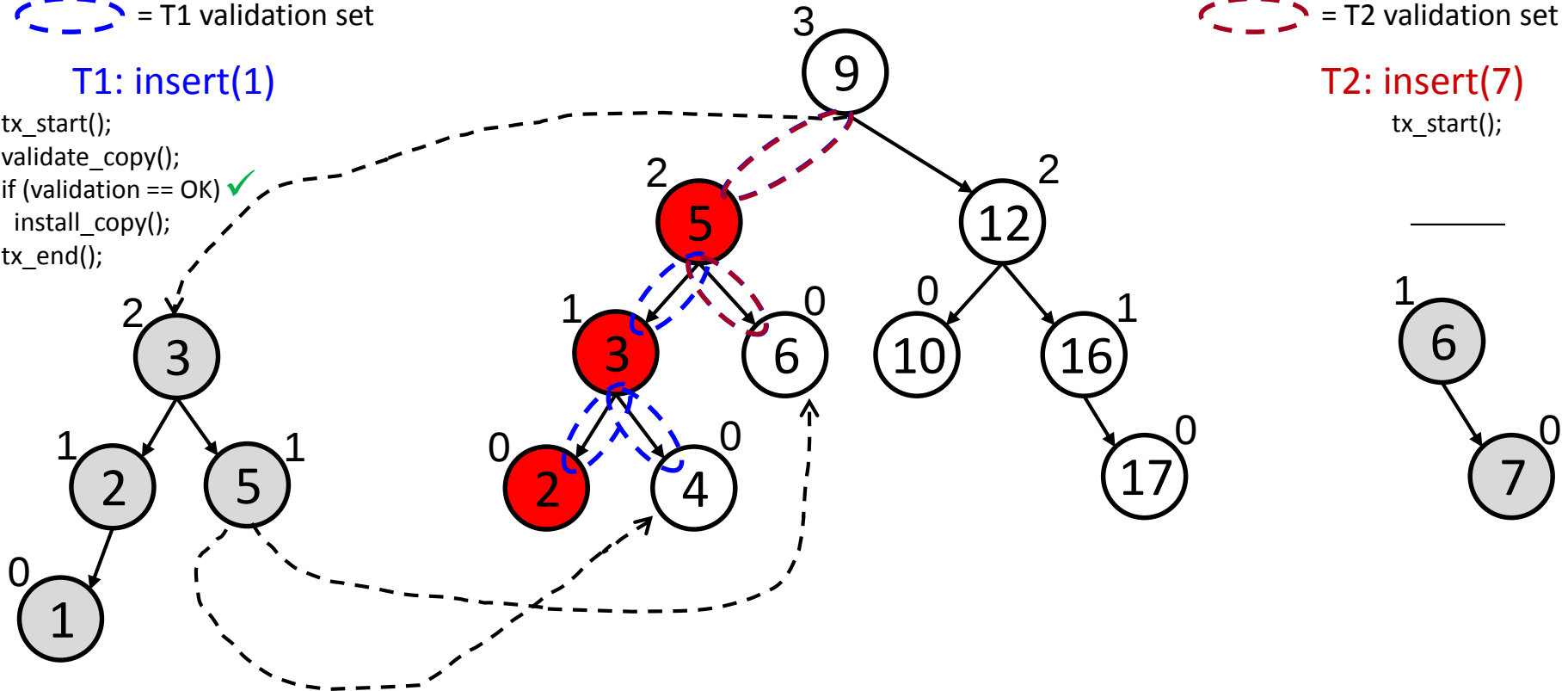
T1: insert(1)

```
tx_start();  
validate_copy();  
if (validation == OK) ✓  
    install_copy();  
tx_end();
```

 = T2 validation set

T2: insert(7)

```
tx_start();
```



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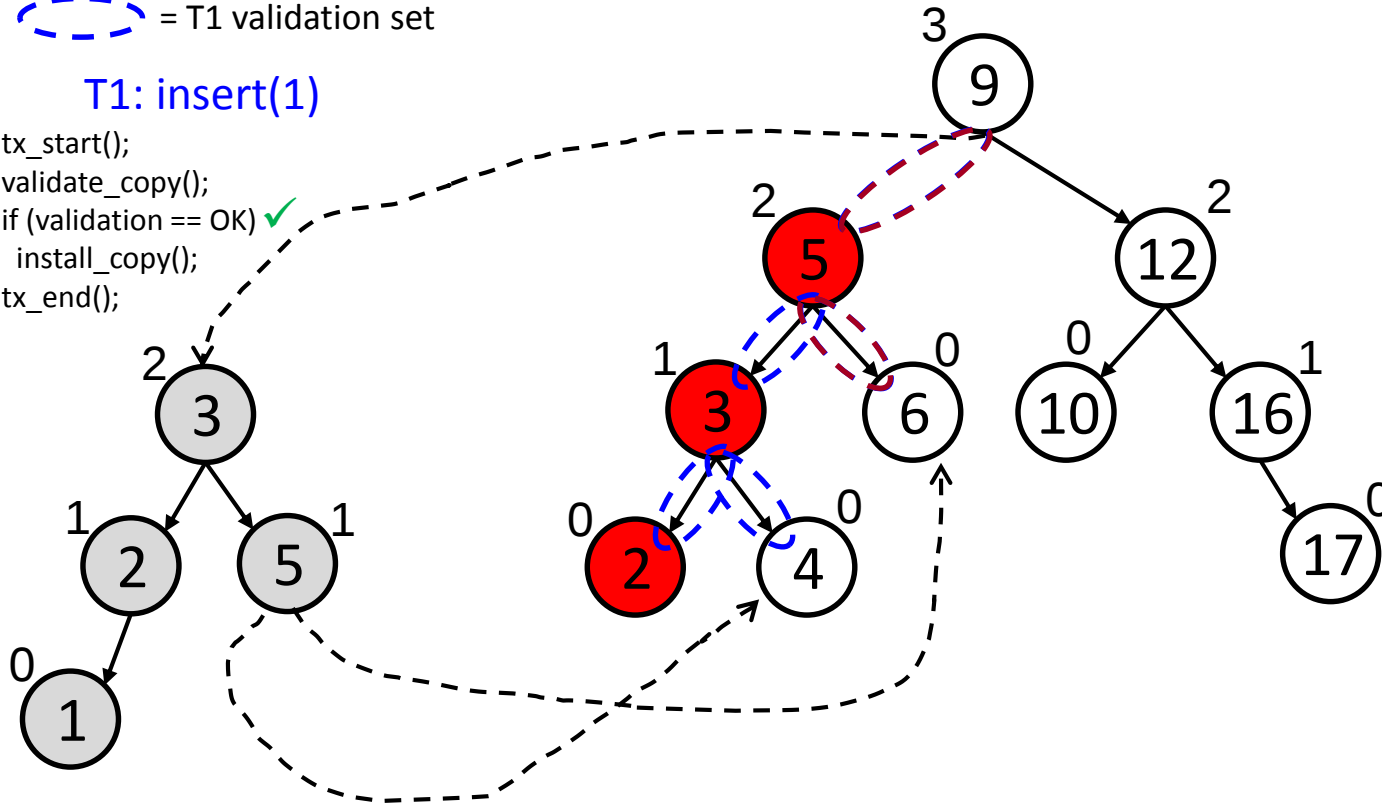
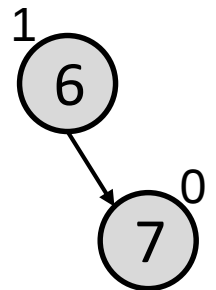
```
tx_start();  
validate_copy();  
if (validation == OK) ✓  
    install_copy();  
tx_end();
```

 = T2 validation set

T2: insert(7)

```
tx_start();  
validate_copy();
```

—————



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 = T1 validation set

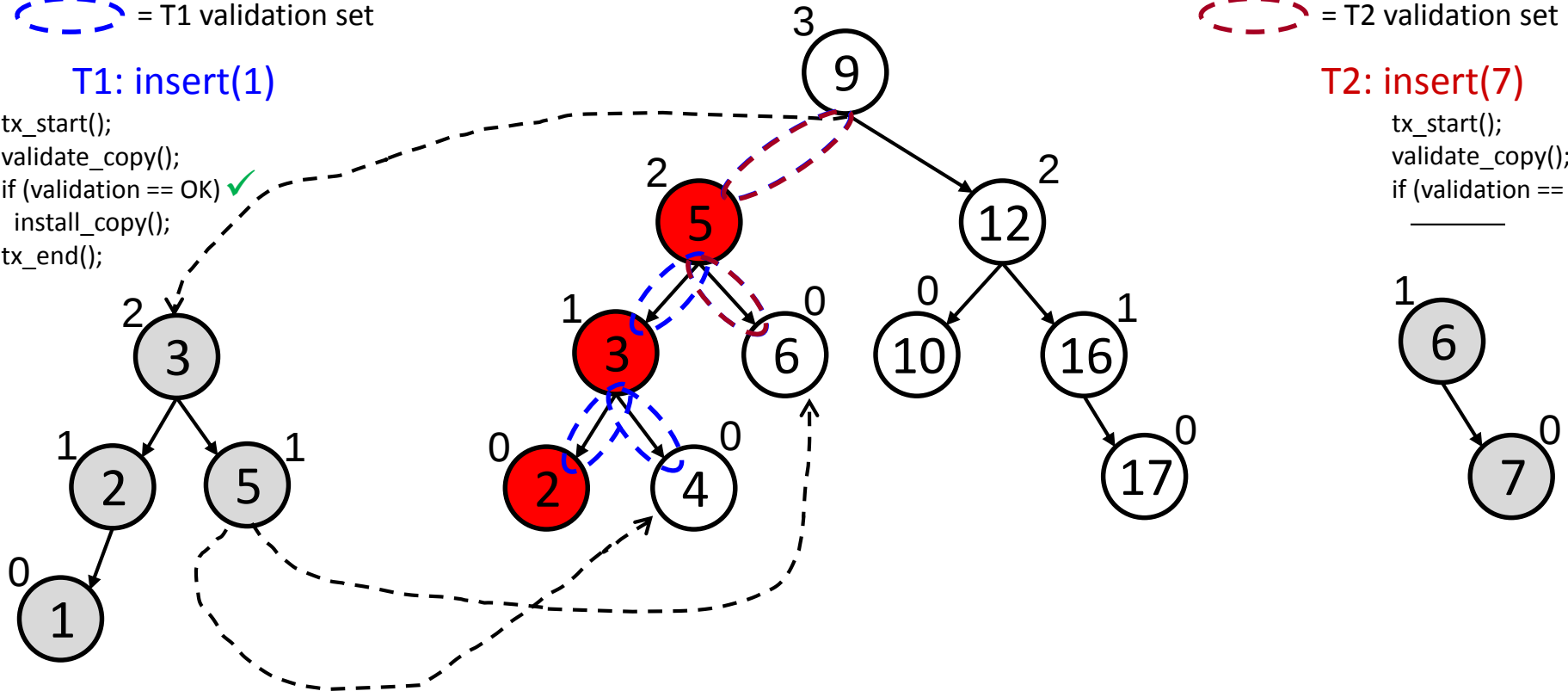
T1: insert(1)

```
tx_start();  
validate_copy();  
if (validation == OK) ✓  
    install_copy();  
tx_end();
```

 = T2 validation set

T2: insert(7)

```
tx_start();  
validate_copy();  
if (validation == OK)  
    _____
```



RCU-HTM: Multiple Updaters

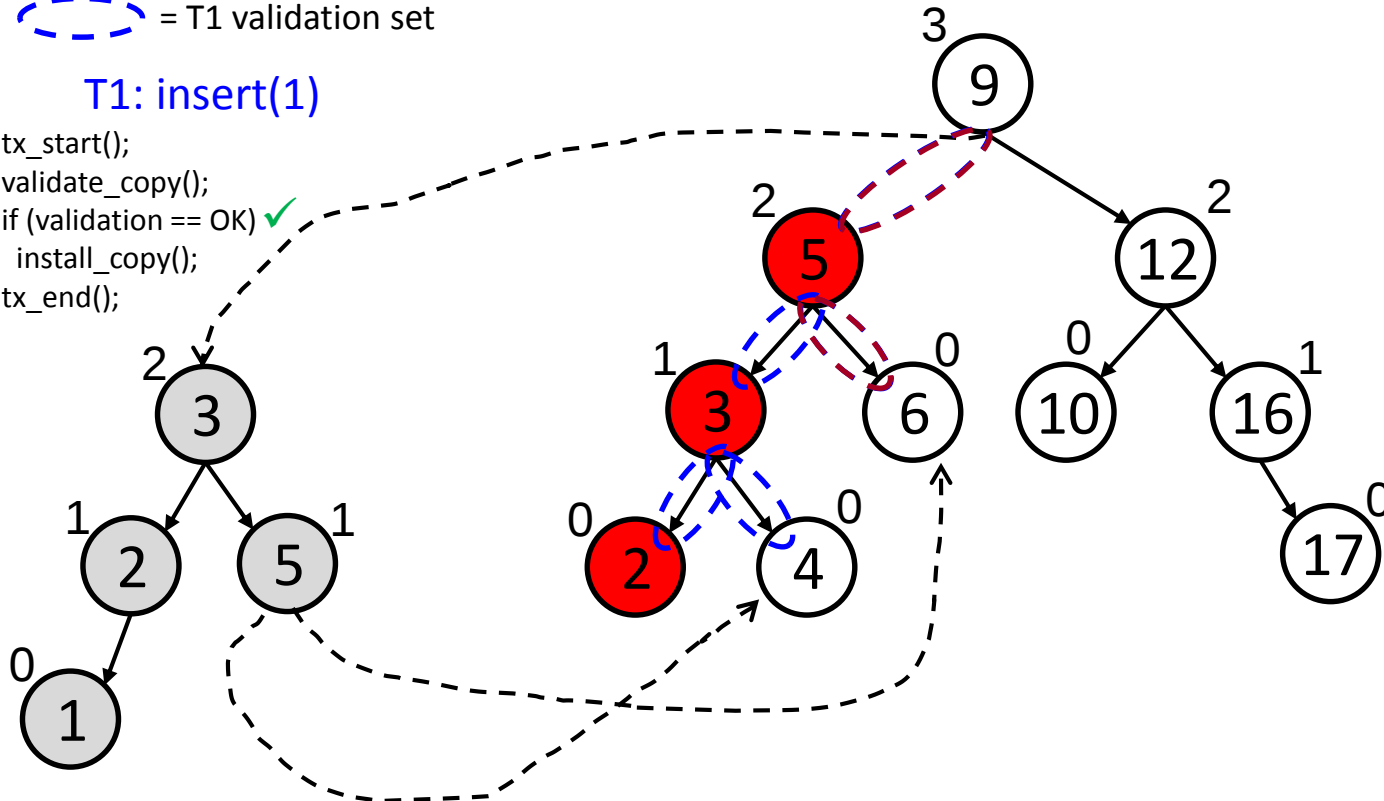
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 = T1 validation set

T1: insert(1)

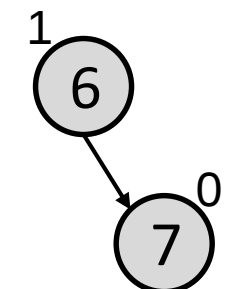
```
tx_start();
validate_copy();
if (validation == OK) ✓
    install_copy();
tx_end();
```



 = T2 validation set

T2: insert(7)

```
tx_start();
validate_copy();
✗ if (validation == OK)
```



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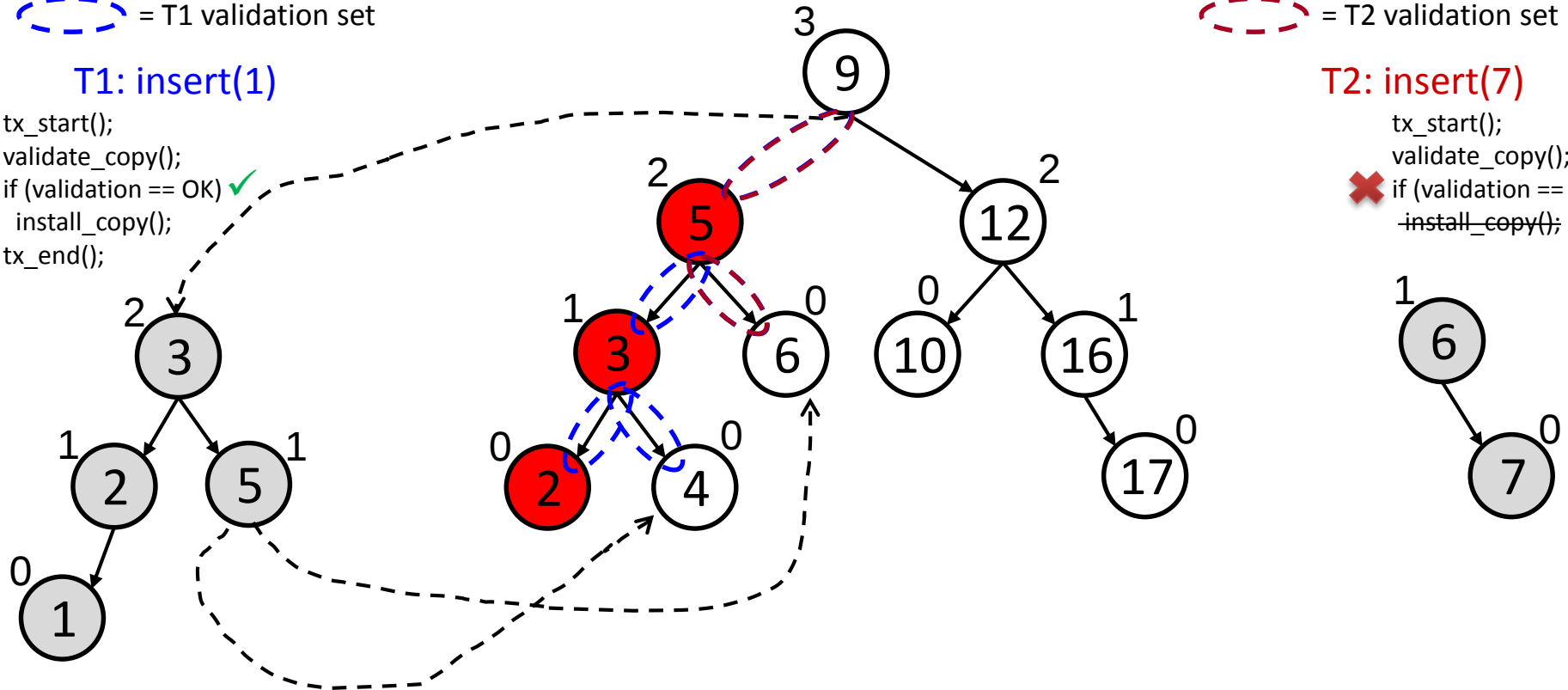
T1: insert(1)

```
tx_start();  
validate_copy();  
if (validation == OK) ✓  
    install_copy();  
tx_end();
```

 = T2 validation set

T2: insert(7)

```
tx_start();  
validate_copy();  
✗ if (validation == OK)  
    -install_copy();
```



RCU-HTM

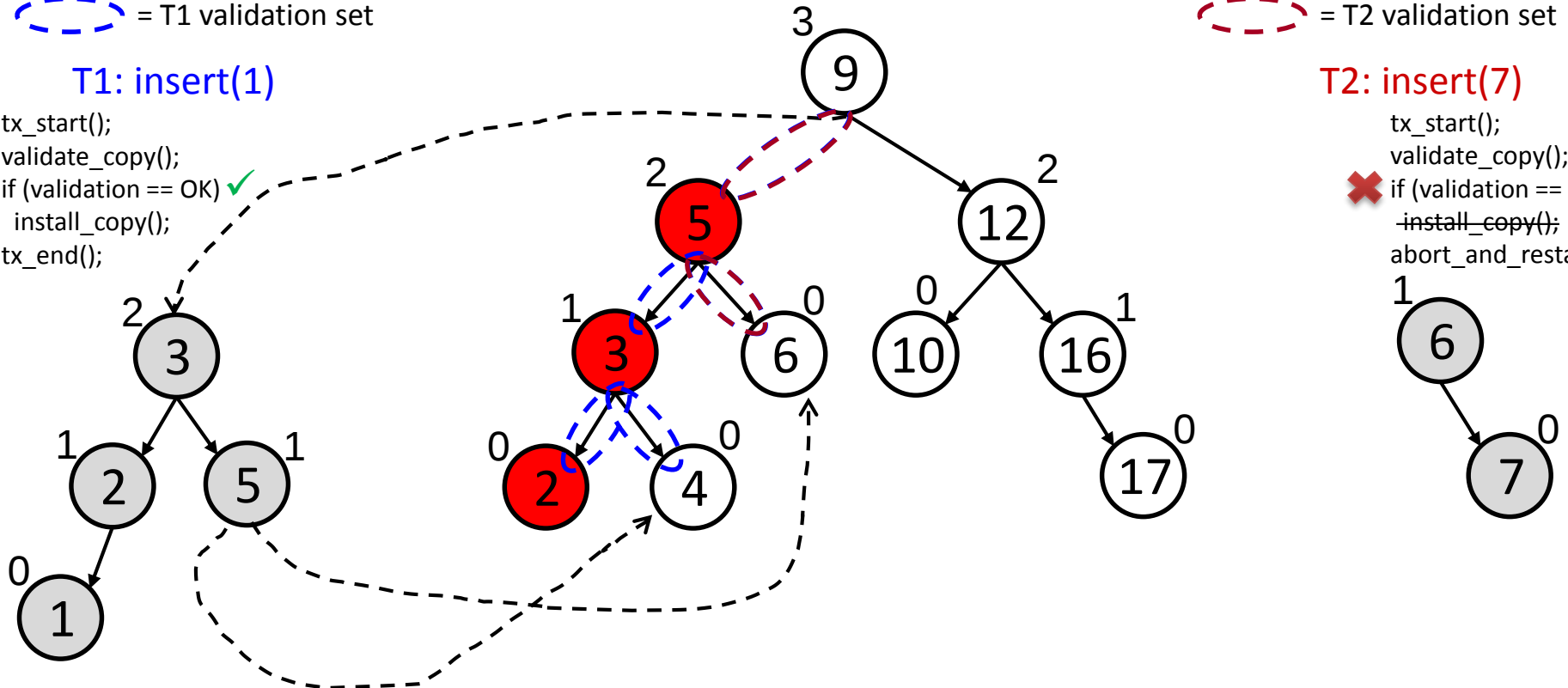
14

-  National Technical University of Athens
CSLab 

 = T2 validation set

T2: insert(7)

```
tx_start();
validate_copy();
❌ if (validation == OK)
    install_copy();
    abort_and_restart();
```



Experimental Setup

- Intel Broadwell-EP Xeon E5-2699 v4
 - 22 cores / 44 hyperthreads @ 2.2GHz
 - 64GB RAM
- Experimental methodology:
 - Threads run for 2 seconds, executing randomly chosen operations (lookups/inserts/deletes)
 - 3 Workloads:
 - Read-only: 100% lookups
 - Read-dominated: 80% lookups, 10% inserts, 10% deletes
 - Write-only: 0% lookups, 50% inserts, 50% deletes
 - 5 tree sizes
 - Small (200 keys) to large (20M keys)

Concurrent BST implementations

	Name	Balanced	Internal	On-time deletion	Asynchronous traversals	Multiple updaters
Lock-free	Ellen et al. [PODC'10]	✗	✗	✓	✓	✓
	Howley et al. [SPAA'12]	✗	✓	✗	✗	✓
	Natarajan et al. [PPoPP'14]	✗	✗	✓	✓	✓
	Chatterjee et al. [PODC'14]	✗	✓	✗	✗	✓
	Brown et al. [PPoPP'14]	✗	✗	✓	✓	✓
Locks	Bronson et al. [PPoPP'10]	✗	✗	✗	✗	✓
	Crain et al. [EuroPar'13]	✗	✗	✗	✓	✓
	Drachsler et al. [PPoPP'14]	✗	✓	✓	✓	✓
RCU	Howard et al. [CCPE'14]	✓	✓	✗	✓	✗
	Arbel et al. [PODC'14]	✗	✓	✗	✓	✓
TM	Crain et al. [PPoPP'14]	✗	✓	✗	✗	✓
	Avni et al. [TRANSACT'14]	✓	✓	✓	✗	✓
	RCU-HTM	✓	✓	✓	✓	✓

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	Natarajan et al. [PPoPP'14]	✗	✗	✓	✓	✓
	Chatterjee et al. [PODC'14]	✗	✓	✗	✗	✓
	Brown et al. [PPoPP'14]	✗	✗	✓	✓	✓
Locks	Bronson et al. [PPoPP'10]	✗	✗	✗	✗	✓
	Crain et al. [EuroPar'13]	✗	✗	✗	✓	✓
	Drachsler et al. [PPoPP'14]	✗	✓	✓	✓	✓
RCU	Howard et al. [CCPE'14]	✓	✓	✗	✓	✗
	Arbel et al. [PODC'14]	✗	✓	✗	✓	✓
TM	Crain et al. [PPoPP'14]	✗	✓	✗	✗	✓
	Avni et al. [TRANSACT'14]	✓	✓	✓	✗	✓
	RCU-HTM	✓	✓	✓	✓	✓

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	Natarajan et al. [PPoPP'14]	✗	✗	✓	✓	✓
	Chatterjee et al. [PODC'14]	✗	✓	✗	✗	✓
	Brown et al. [PPoPP'14]	✗	✗	✓	✓	✓
Locks	Bronson et al. [PPoPP'10]	✗	✗	✗	✗	✓
	Crain et al. [EuroPar'13]	✗	✗	✗	✓	✓
	Drachsler et al. [PPoPP'14]	✗	✓	✓	✓	✓
RCU	Howard et al. [CCPE'14]	✓	✓	✗	✓	✗
	Arbel et al. [PODC'14]	✗	✓	✗	✓	✓
TM	Crain et al. [PPoPP'14]	✗	✓	✗	✗	✓
	Avni et al. [TRANSACT'14]	✓	✓	✓	✗	✓
	RCU-HTM	✓	✓	✓	✓	✓

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	Chatterjee et al. [PODC'14]	✗	✓	✗	✗	✓
	Brown et al. [PPoPP'14]	✗	✗	✓	✓	✓
Locks	Bronson et al. [PPoPP'10]	✗	✗	✗	✗	✓
	Crain et al. [EuroPar'13]	✗	✗	✗	✓	✓
	Drachsler et al. [PPoPP'14]	✗	✓	✓	✓	✓
RCU	Howard et al. [CCPE'14]	✓	✓	✗	✓	✗
	Arbel et al. [PODC'14]	✗	✓	✗	✓	✓
TM	Crain et al. [PPoPP'14]	✗	✓	✗	✗	✓
	Avni et al. [TRANSACT'14]	✓	✓	✓	✗	✓
	RCU-HTM	✓	✓	✓	✓	✓

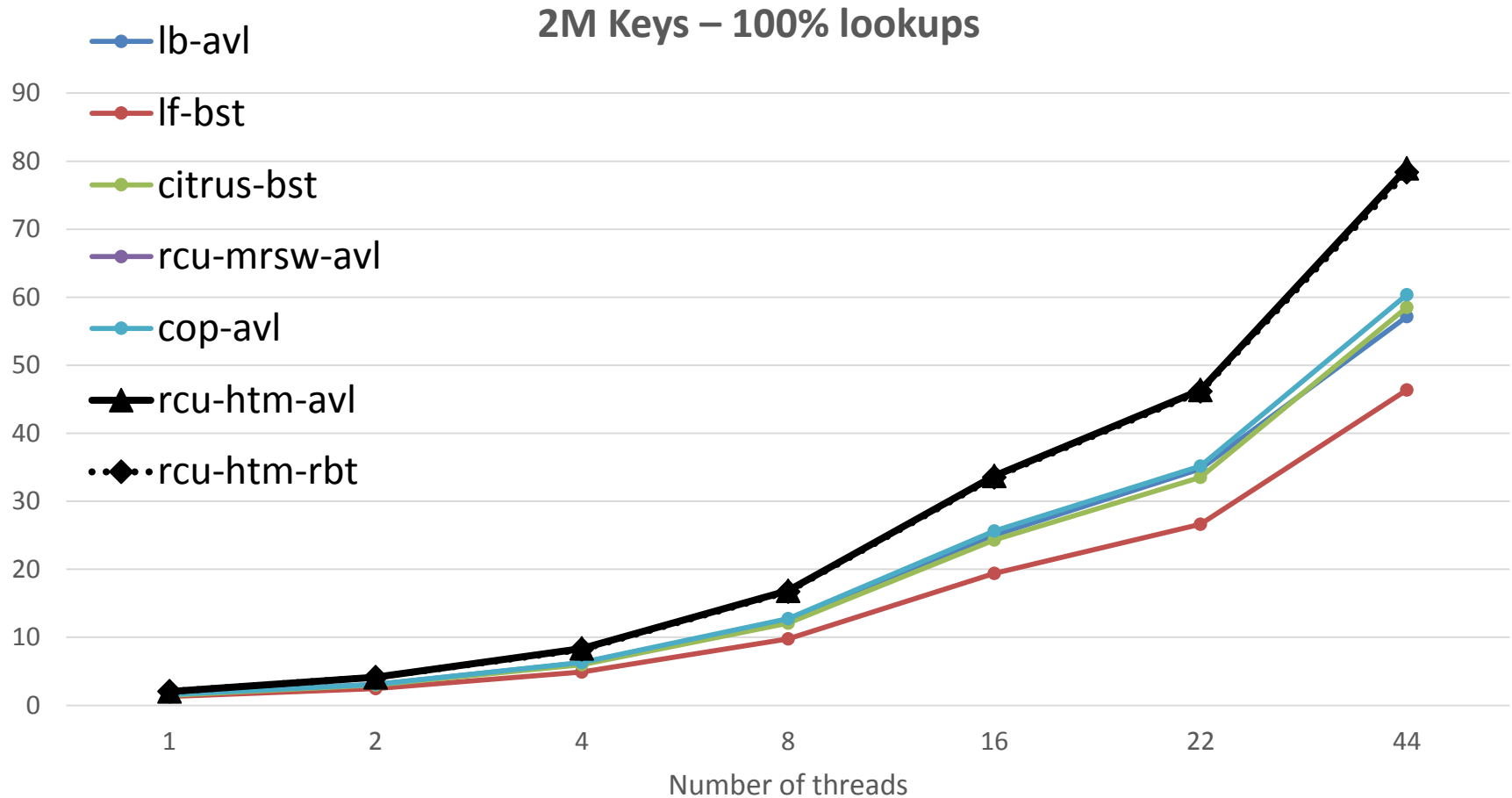


Concurrent BST implementations

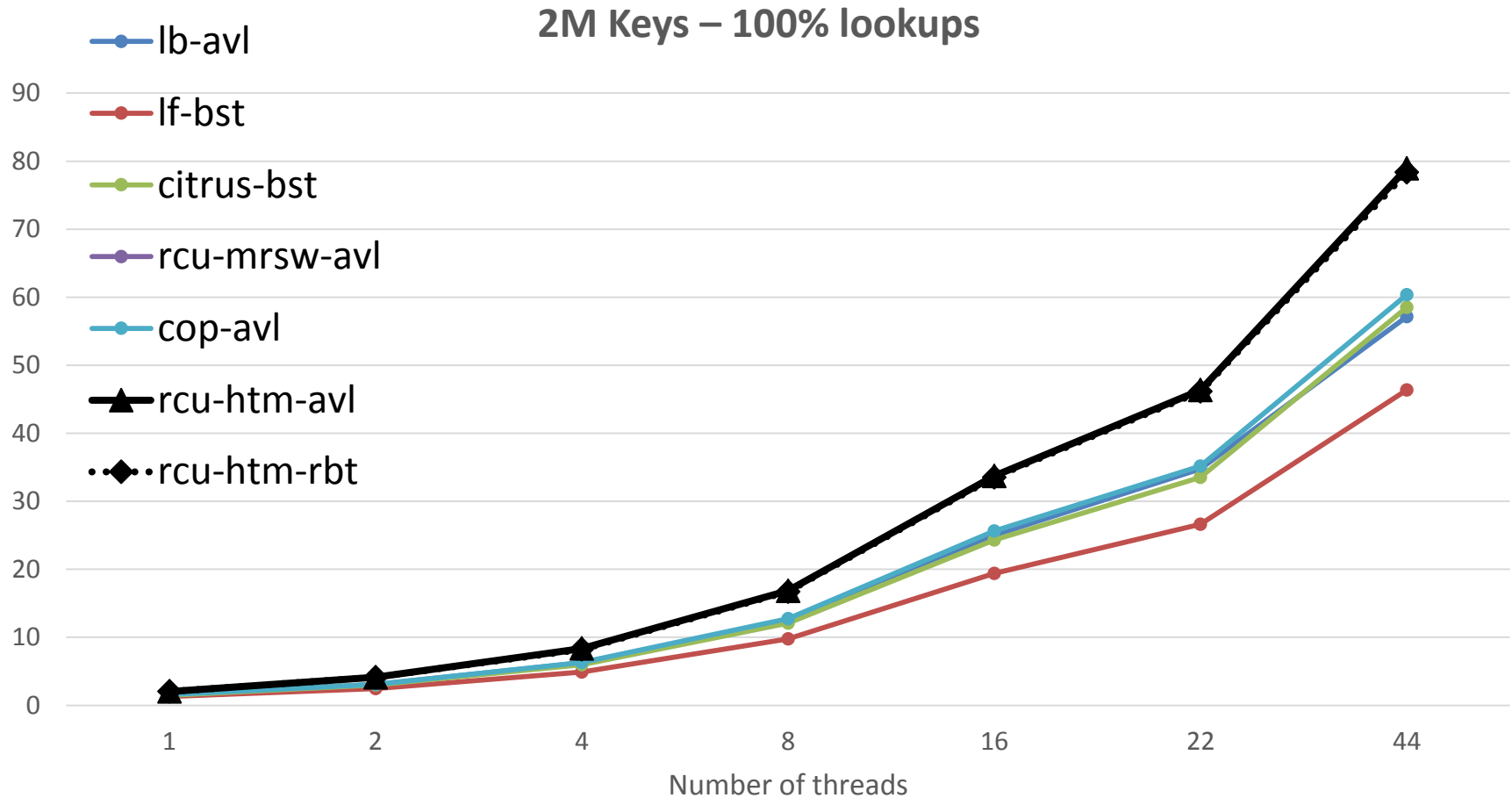
	Name	Balanced	Internal	On-time deletion	Asynchronous traversals	Multiple updaters
Lock-free	Ellen et al. [PODC'10]	✗	✗	✓	✓	✓
	Howley et al. [SPAA'12]	✗	✓	✗	✗	✓
	Natarajan et al. [PPoPP'14]	✗	✗	✓	✓	✓
	Chatterjee et al. [PODC'14]	✗	✓	✗	✗	✓
	Brown et al. [PPoPP'14]	✗	✗	✓	✓	✓
Locks	Bronson et al. [PPoPP'10]	✗	✗	✗	✗	✓
	Crain et al. [EuroPar'13]	✗	✗	✗	✓	✓
	Drachsler et al. [PPoPP'14]	✗	✓	✓	✓	✓
RCU	Howard et al. [CCPE'14]	✓	✓	✗	✓	✗
	Arbel et al. [PODC'14]	✗	✓	✗	✓	✓
TM	Crain et al. [PPoPP'14]	✗	✓	✗	✗	✓
	Avni et al. [TRANSACT'14]	✓	✓	✓	✗	✓
	RCU-HTM	✓	✓	✓	✓	✓

Performance: read-only workloads

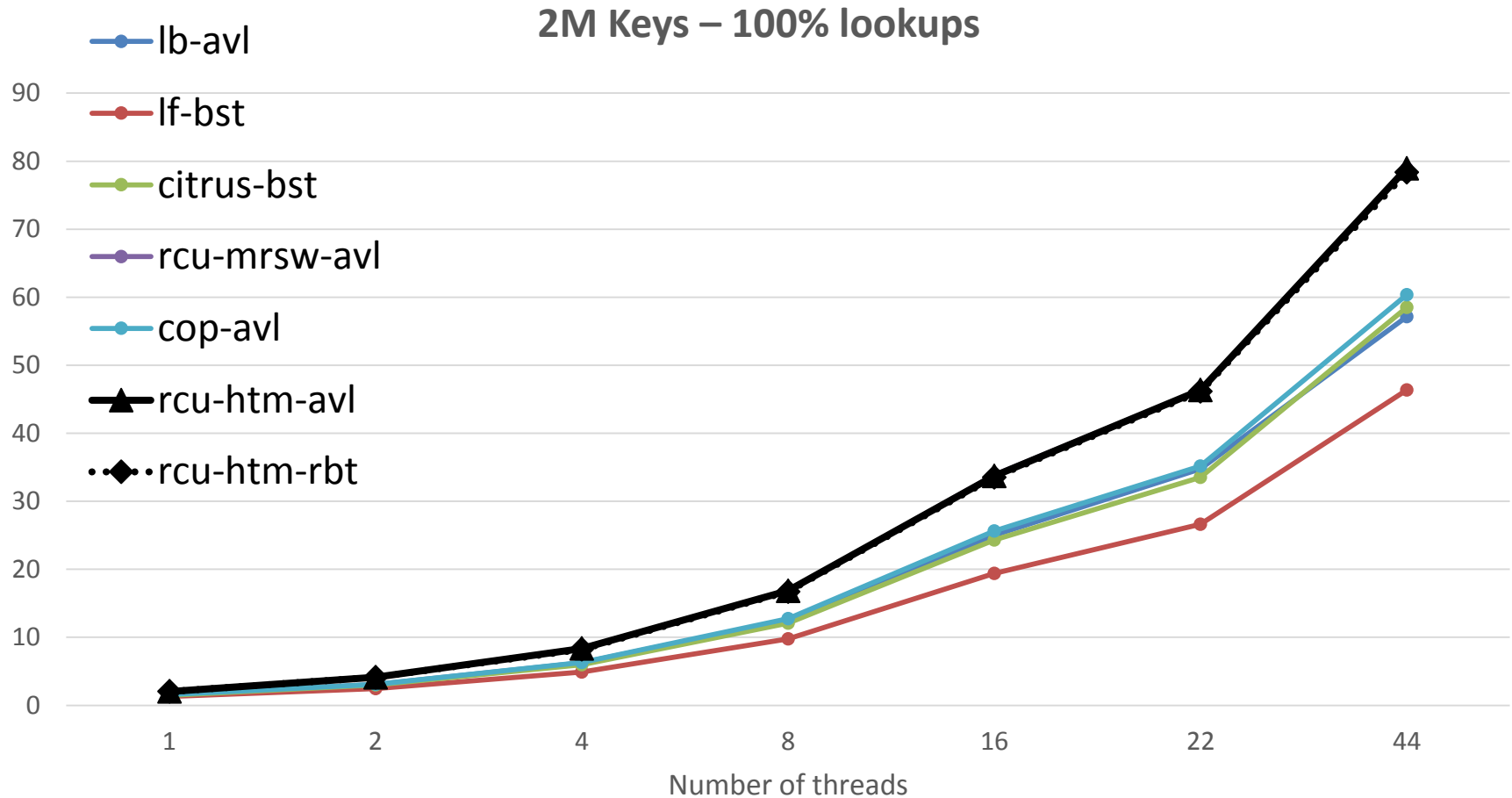
Performance: read-only workloads



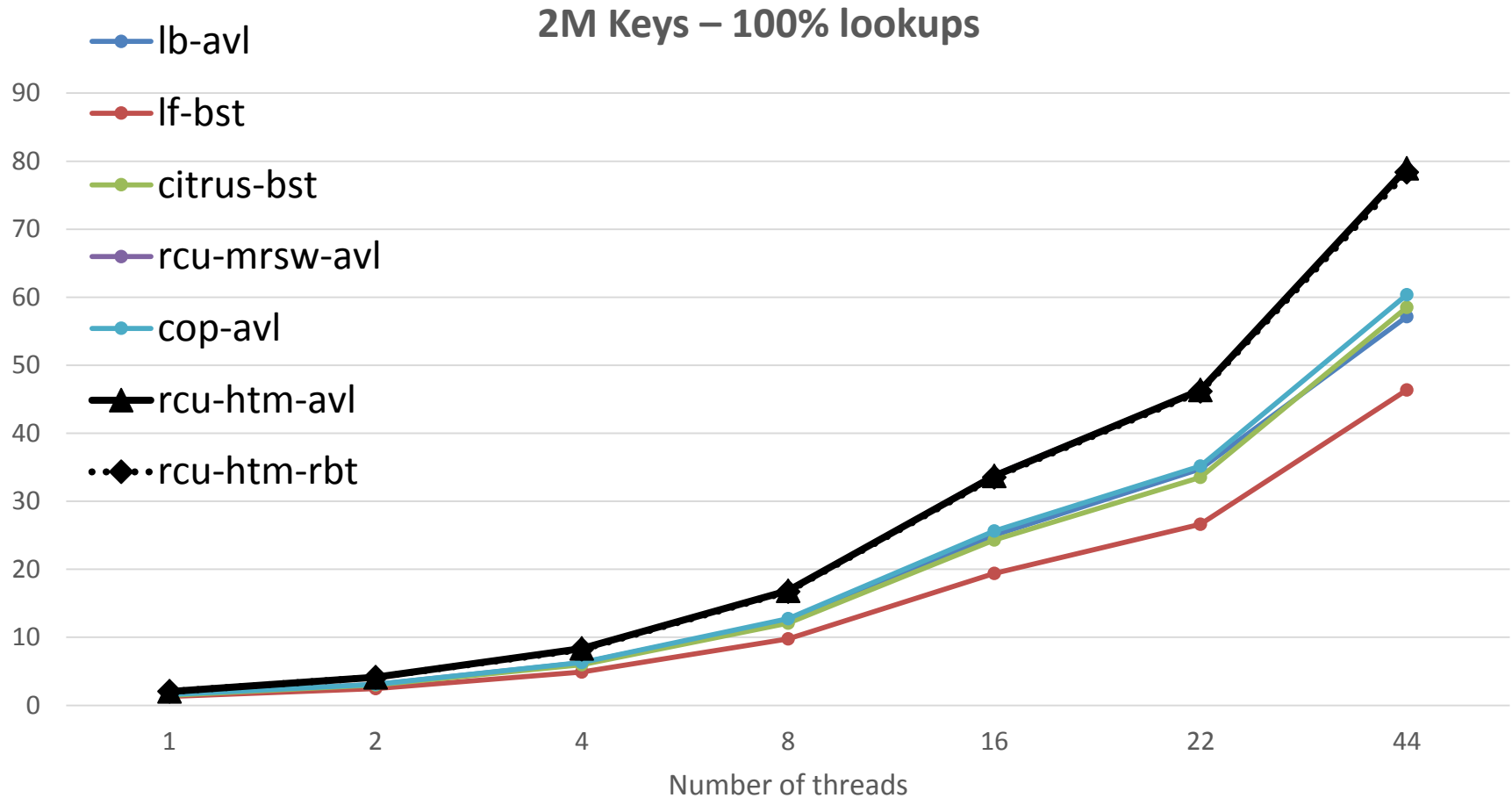
Performance: read-only workloads



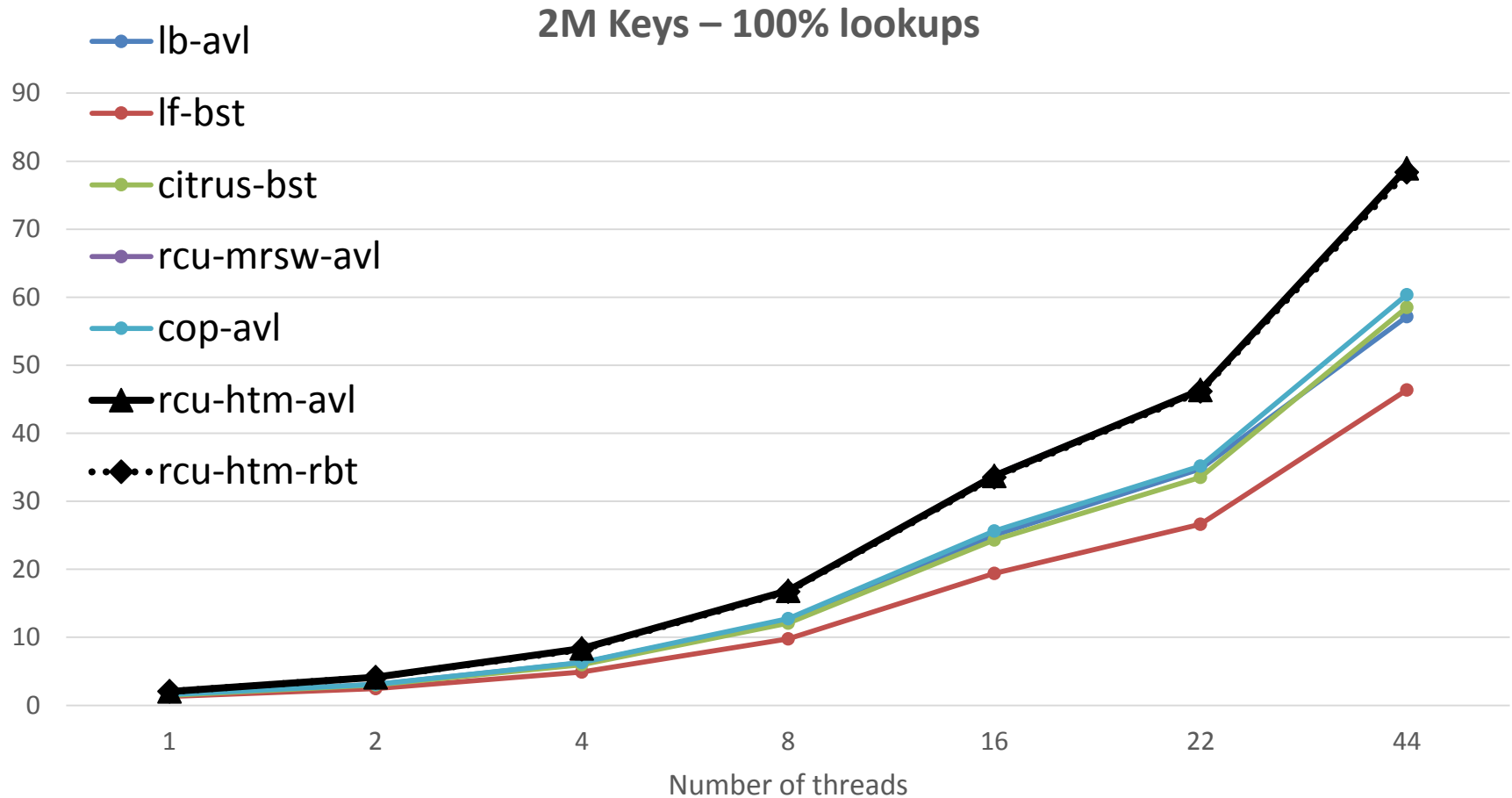
Performance: read-only workloads



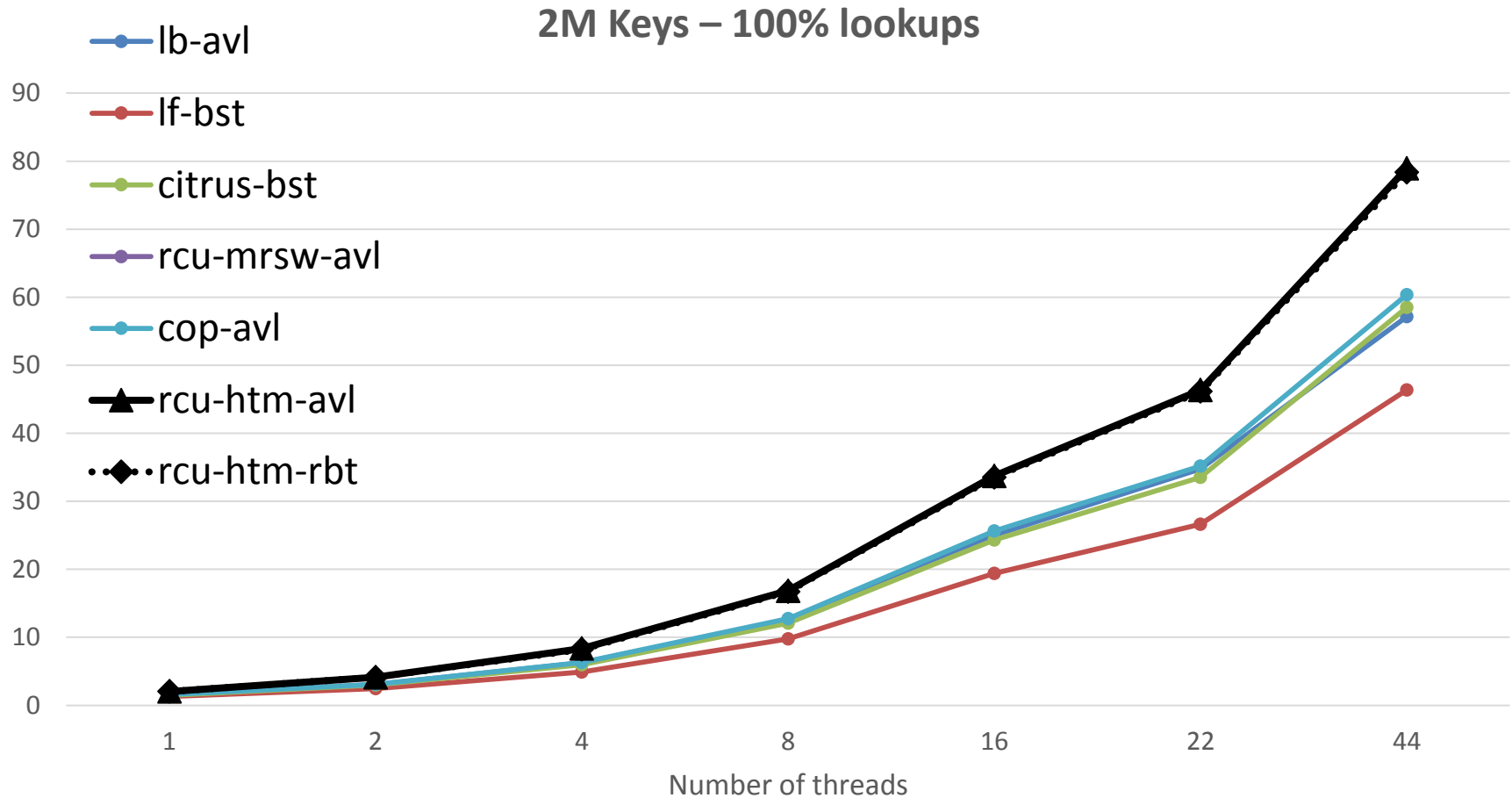
Performance: read-only workloads



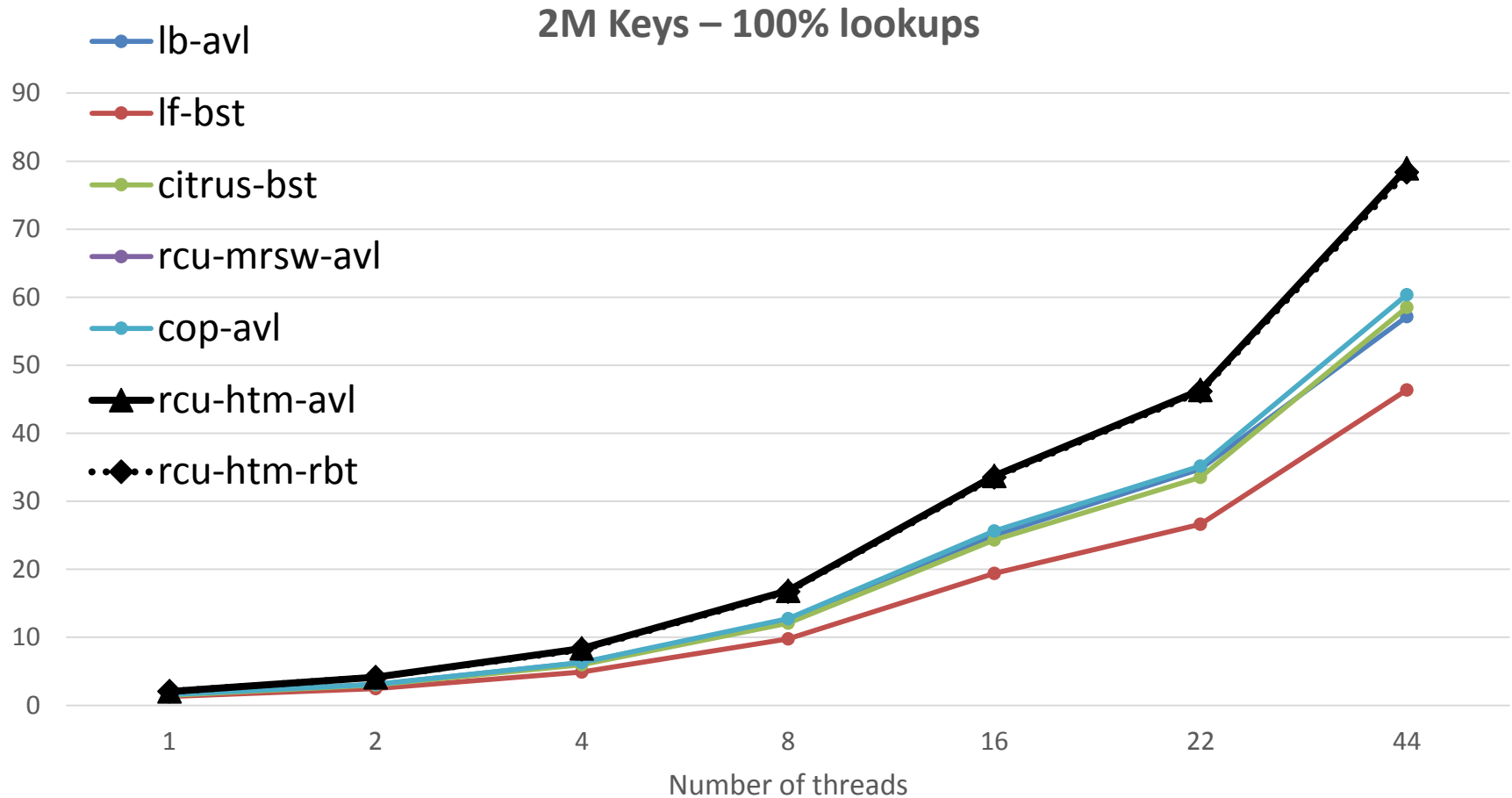
Performance: read-only workloads



Performance: read-only workloads



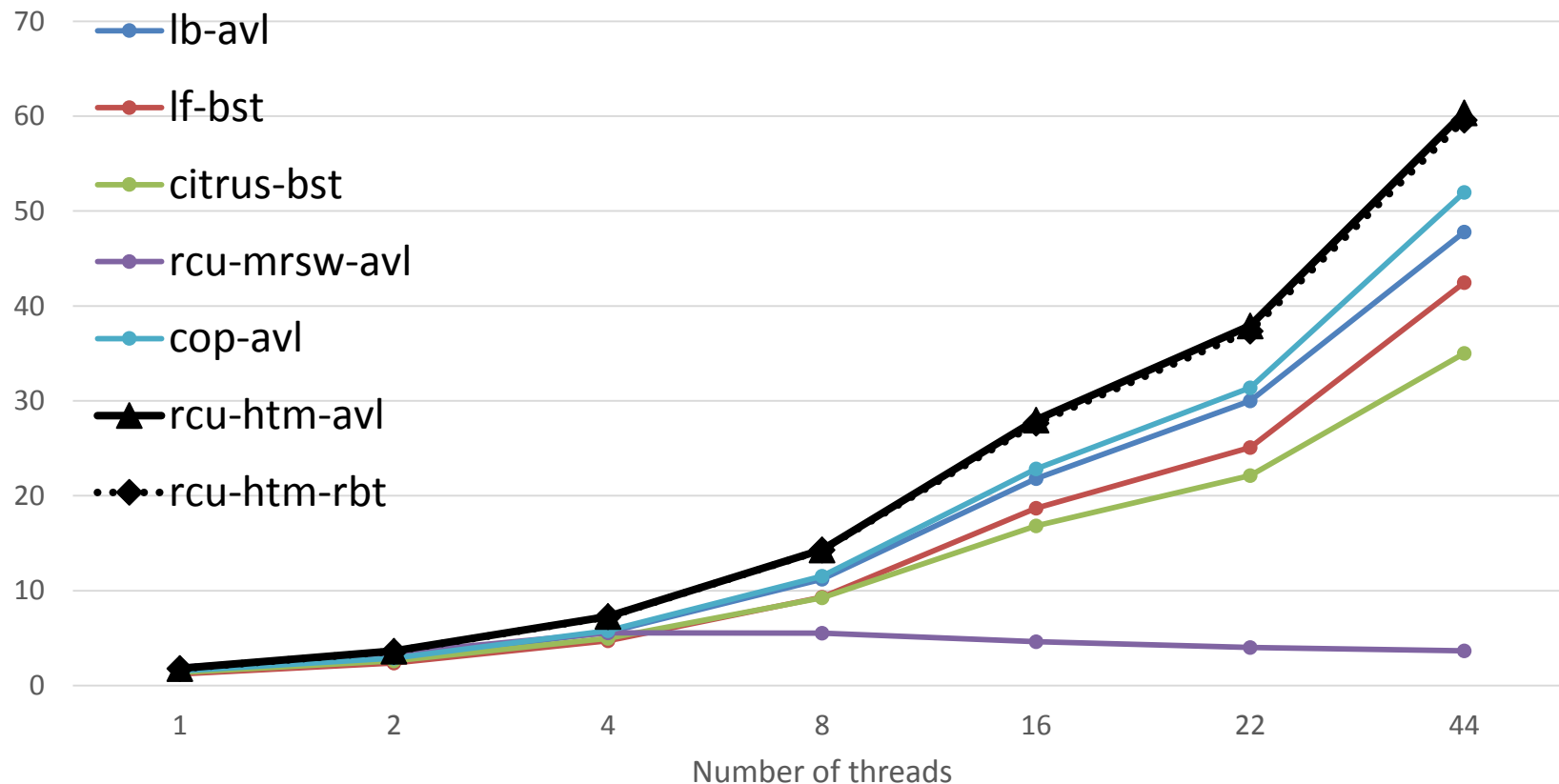
Performance: read-only workloads



Performance: read-dominated workloads

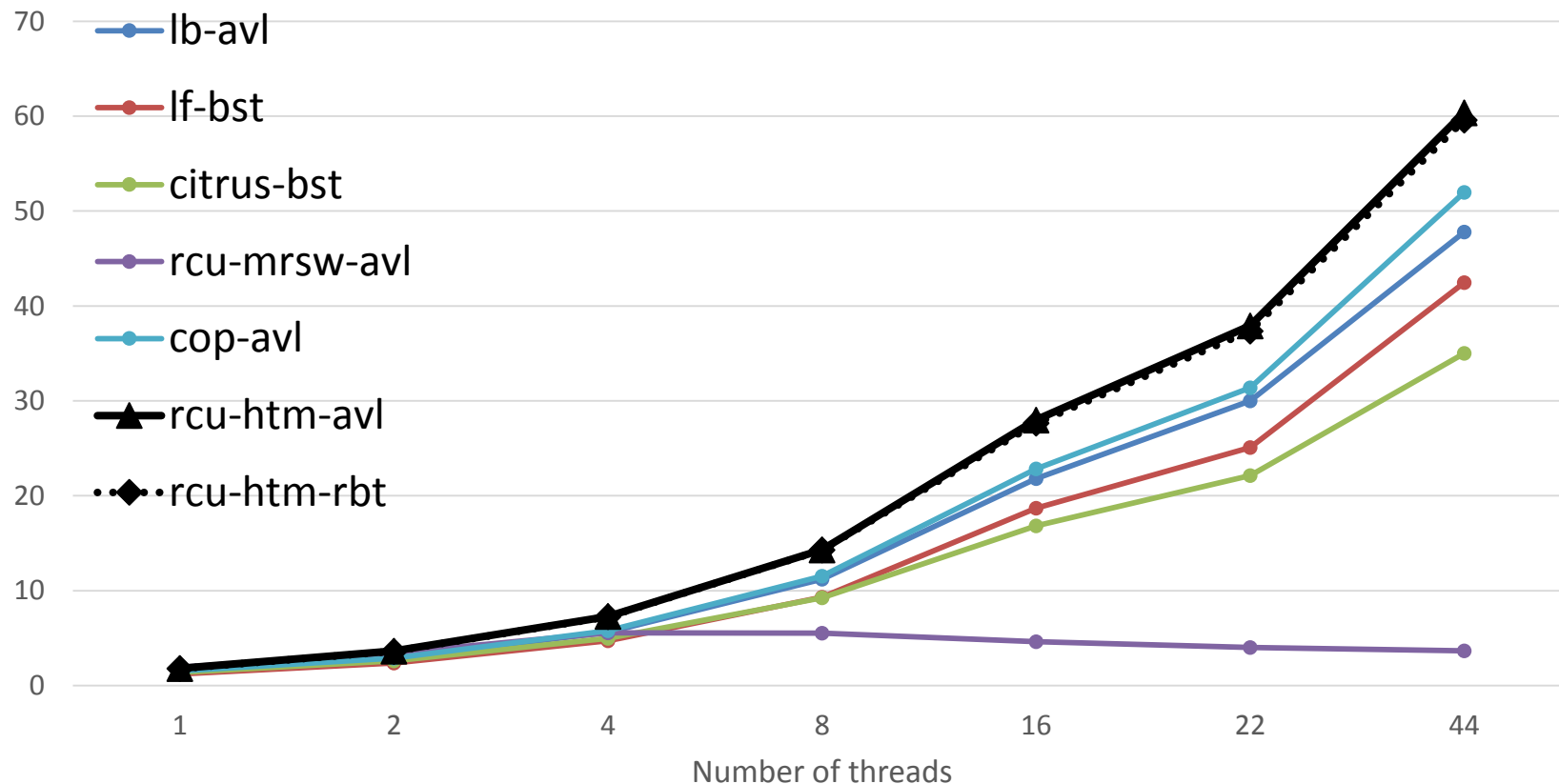
Performance: read-dominated workloads

2M Keys – 80% lookups



Performance: read-dominated workloads

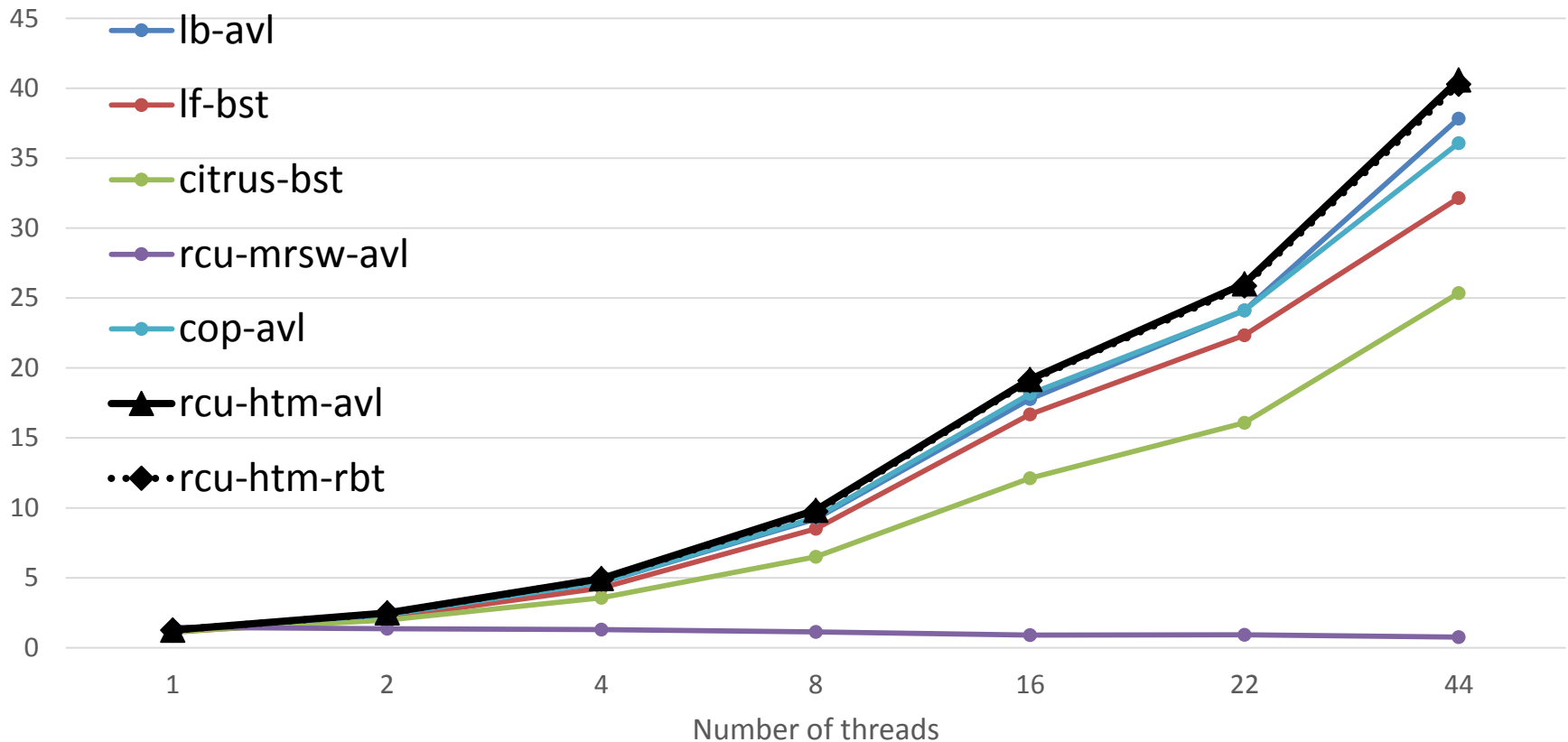
2M Keys – 80% lookups



Performance: write-only workloads

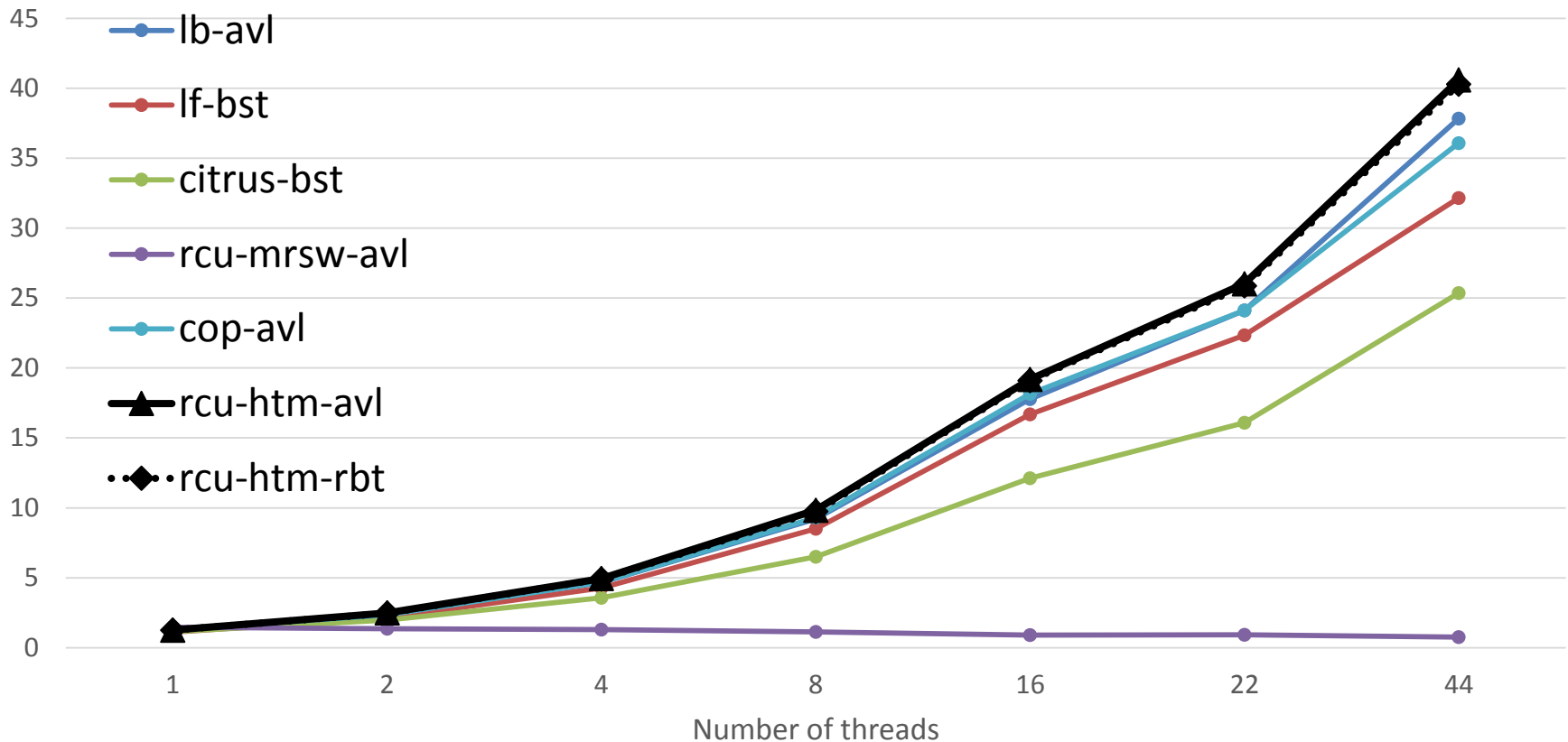
Performance: write-only workloads

2M Keys – 0% lookups



Performance: write-only workloads

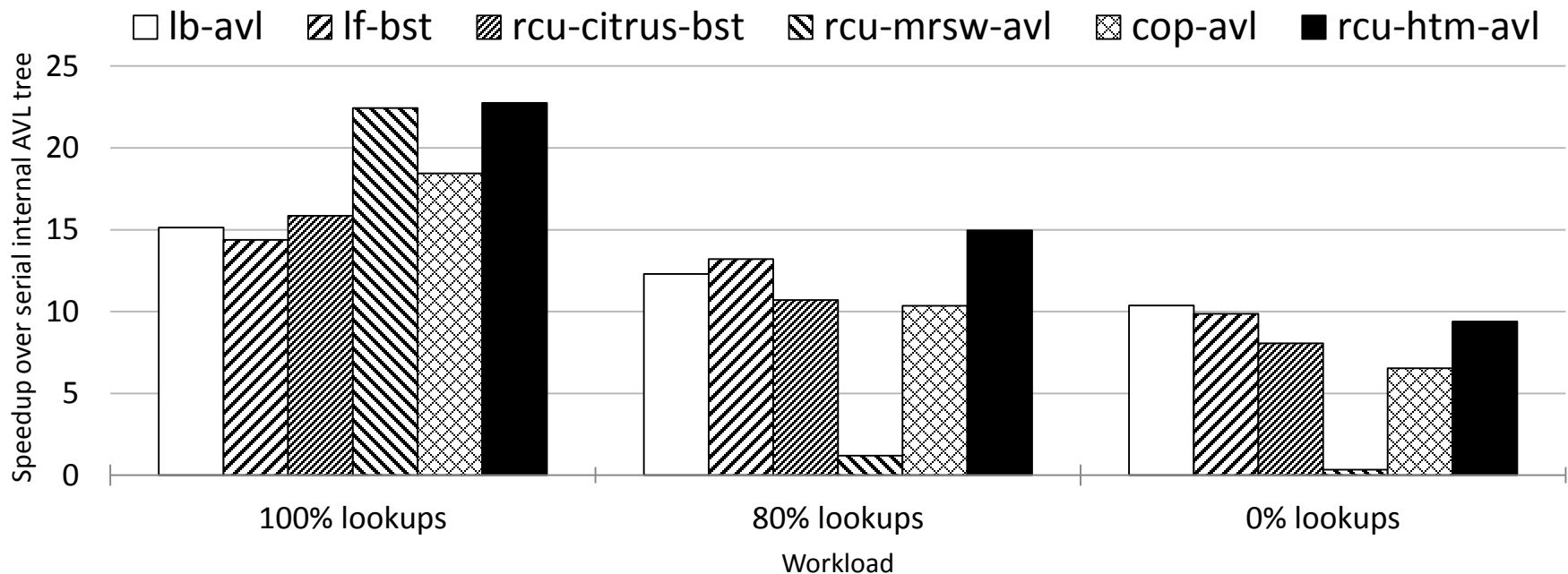
2M Keys – 0% lookups



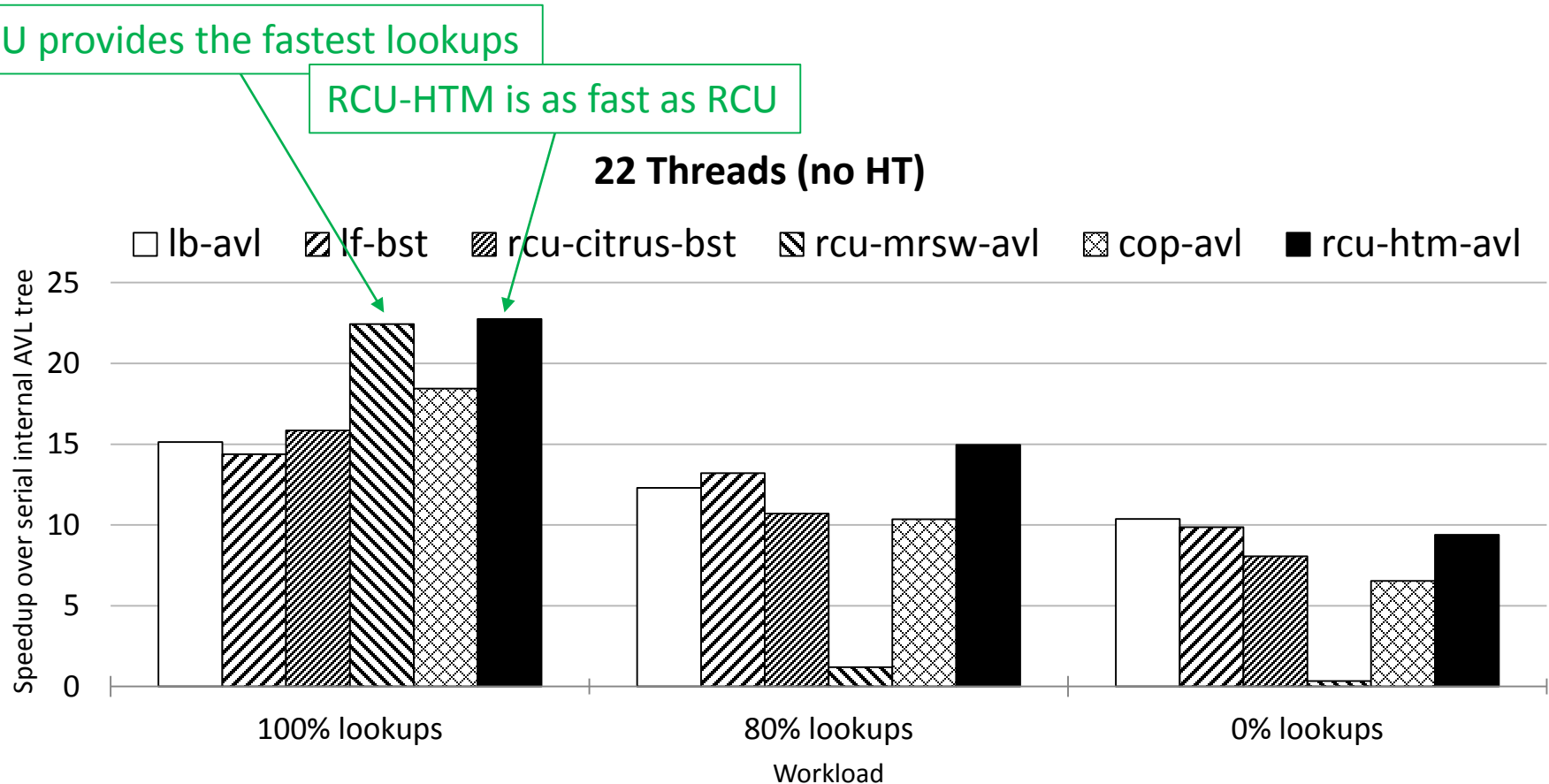
Performance: per workload average

Performance: per workload average

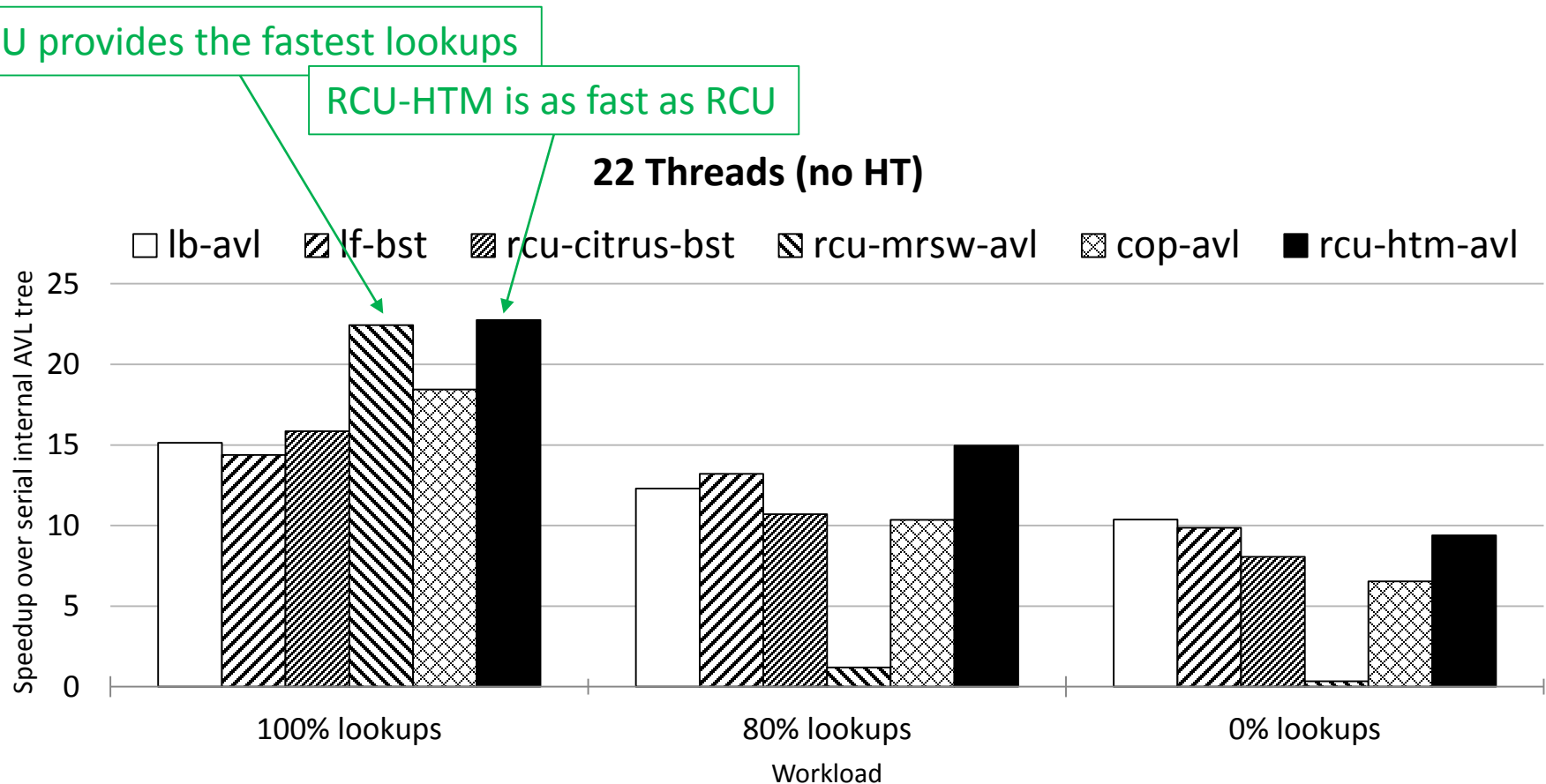
22 Threads (no HT)



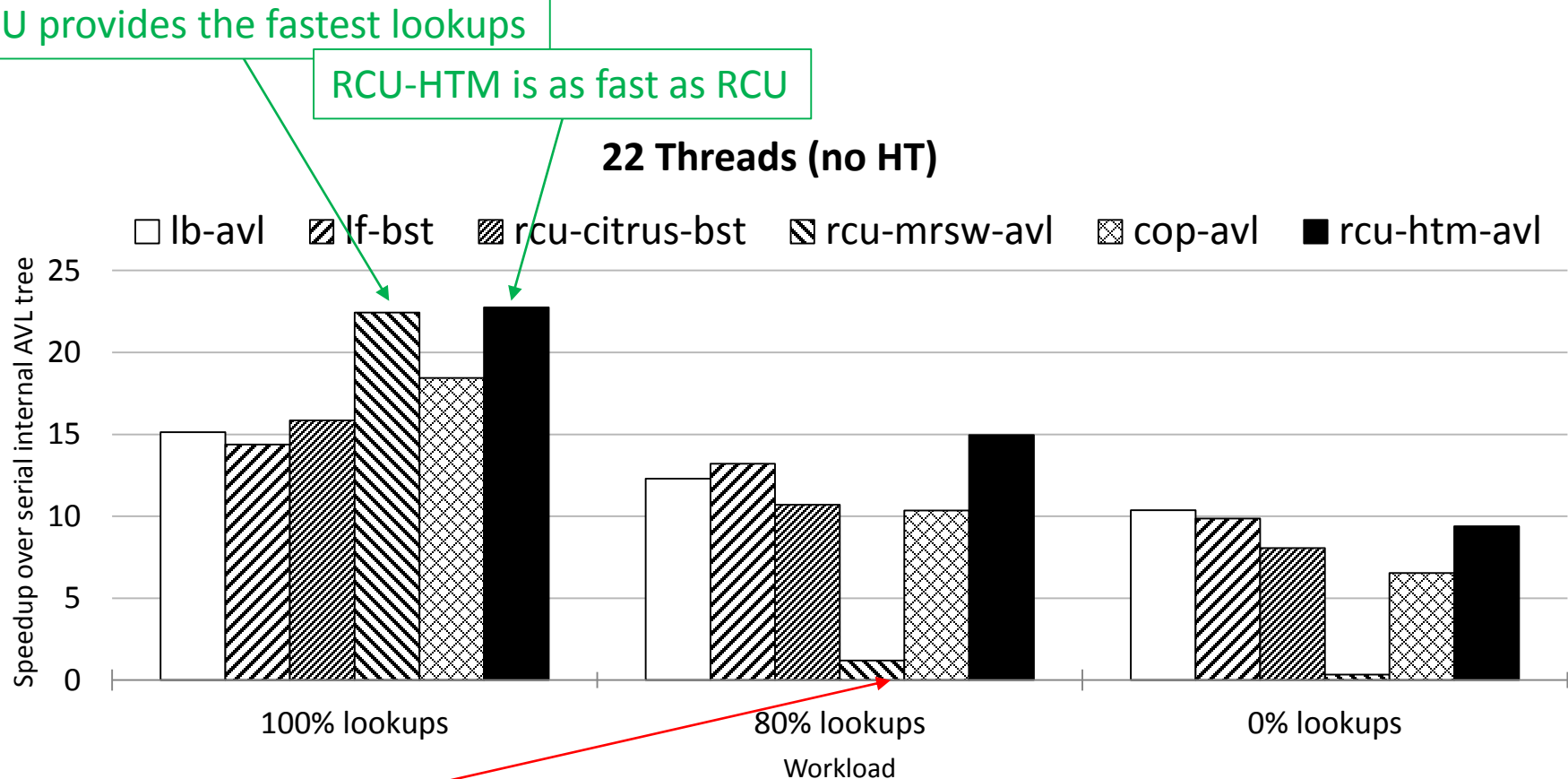
Performance: per workload average



Performance: per workload average

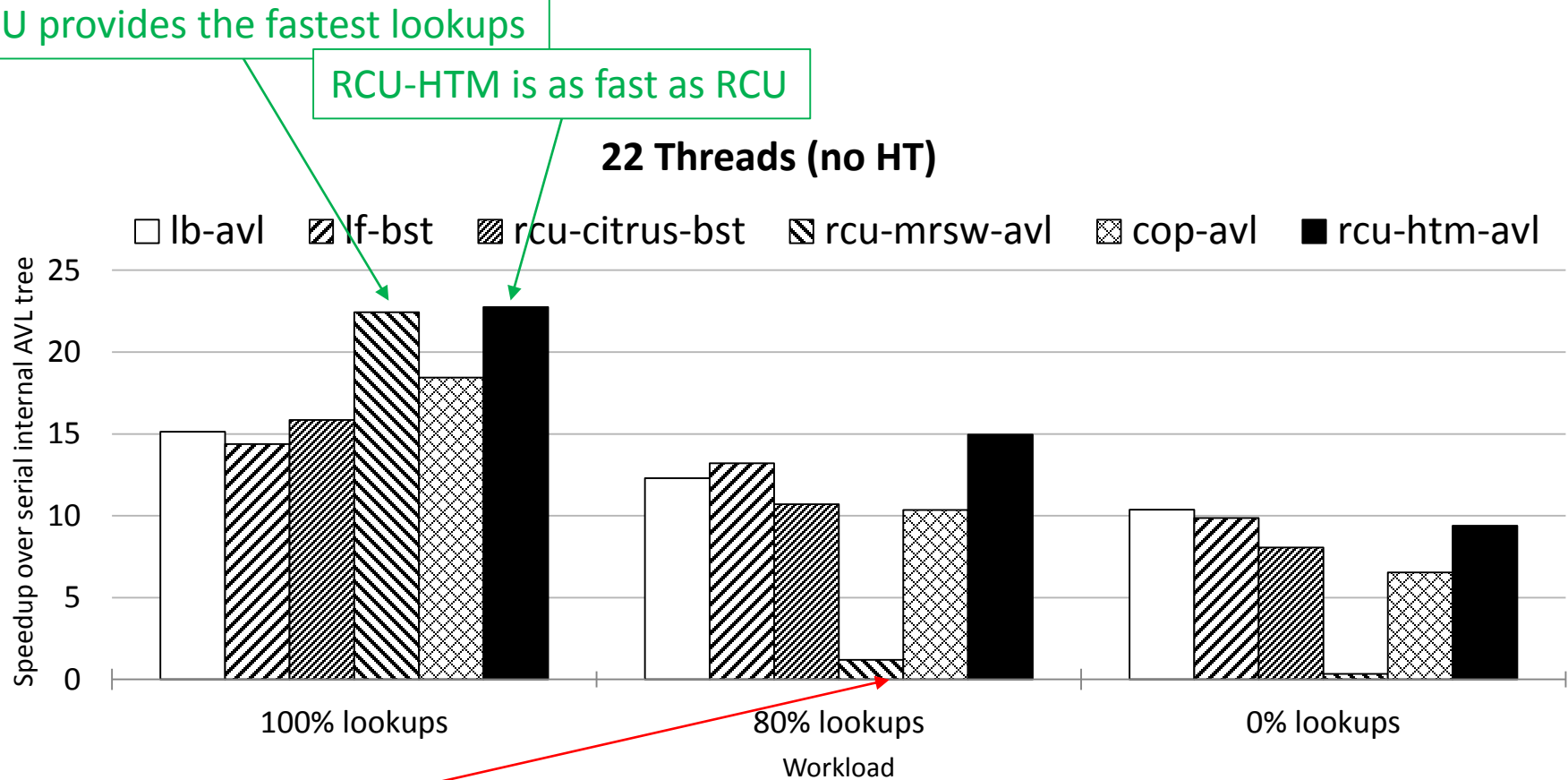


Performance: per workload average



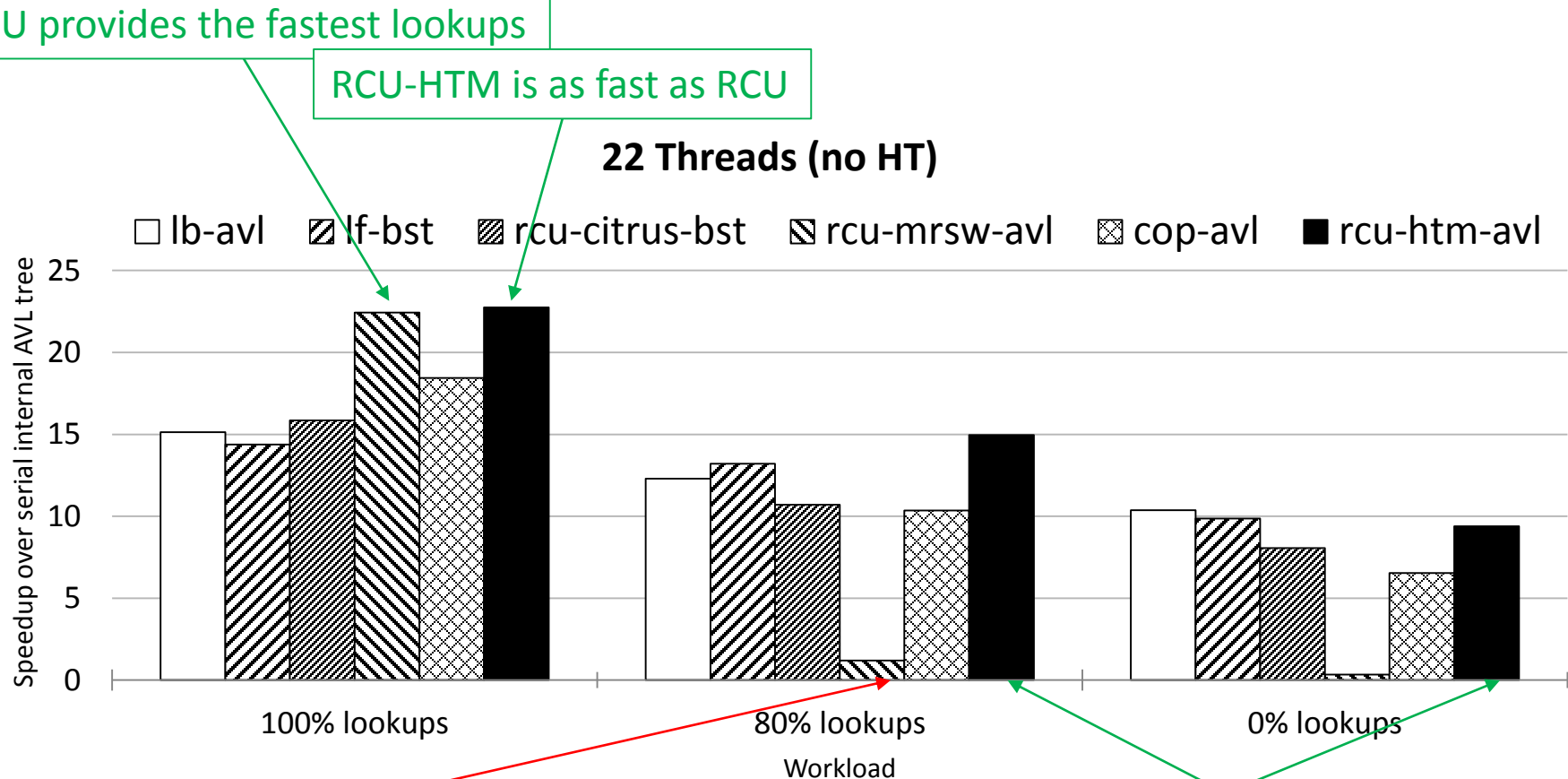
Even with 20% update operations RCU performance collapses due to the writers' single global lock

Performance: per workload average



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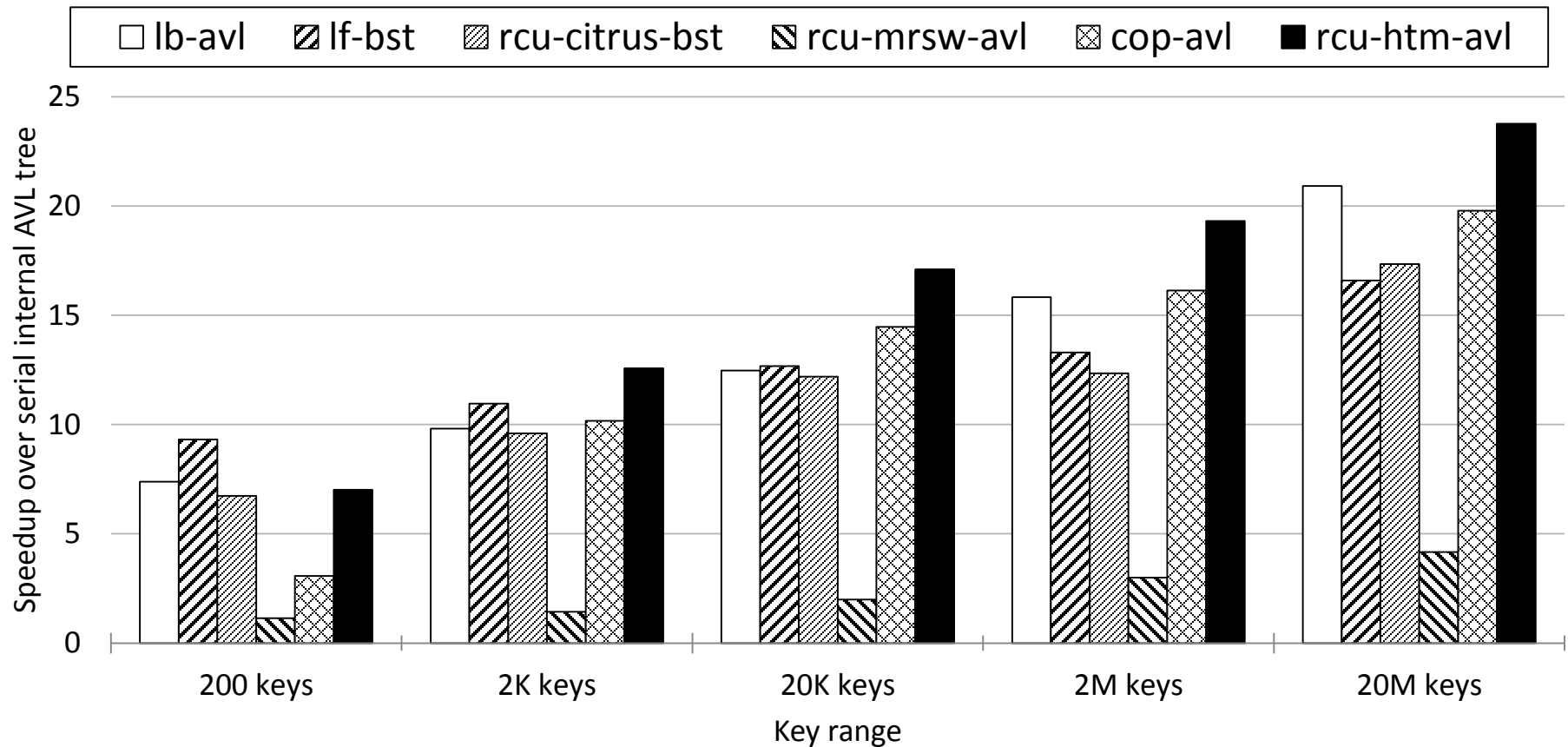


Even with 20% update operations RCU performance collapses due to the writers' single global lock

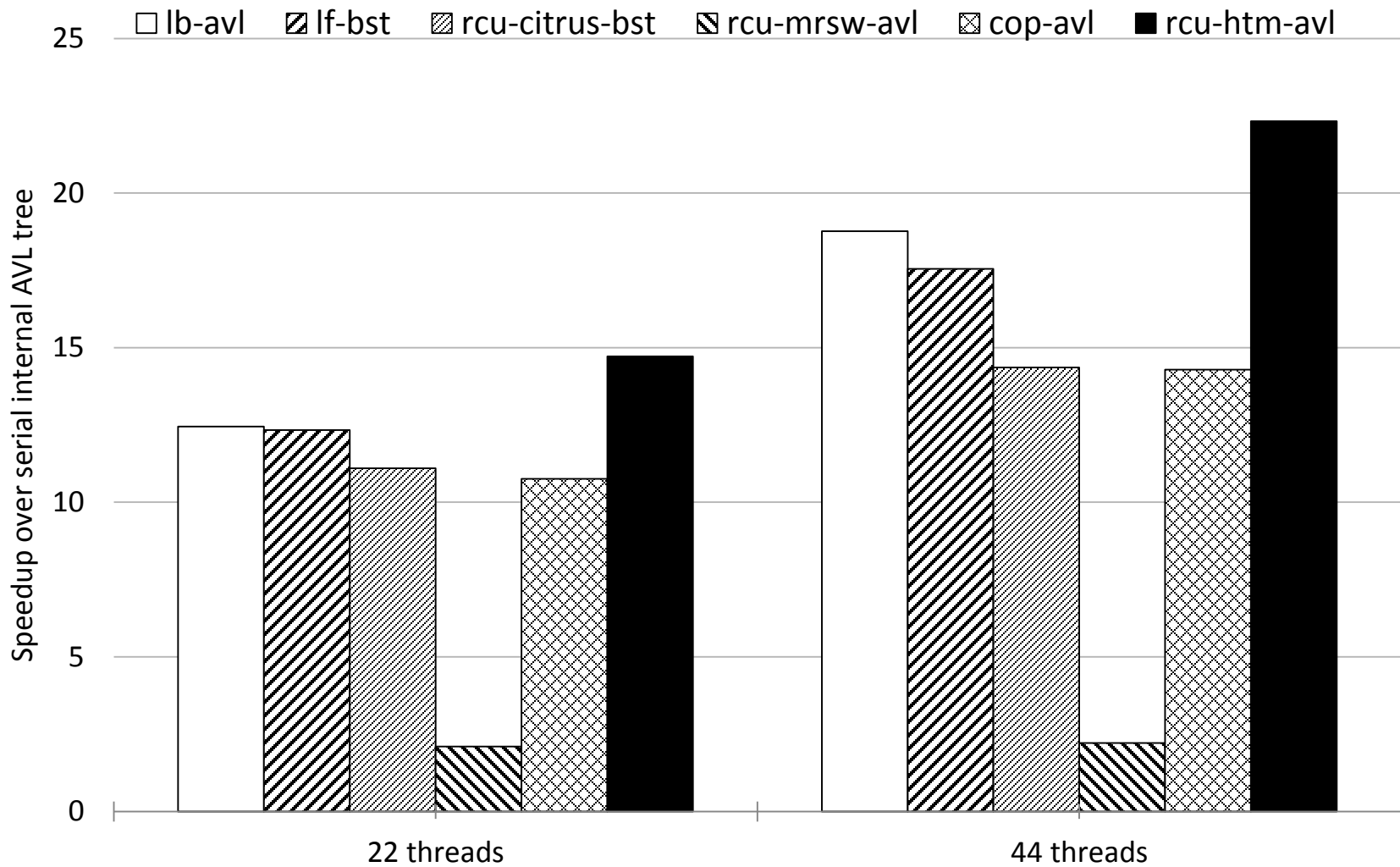
RCU-HTM efficiently allows multiple updaters and manages to retain its performance on workloads with high update ratio

Performance: per tree size average

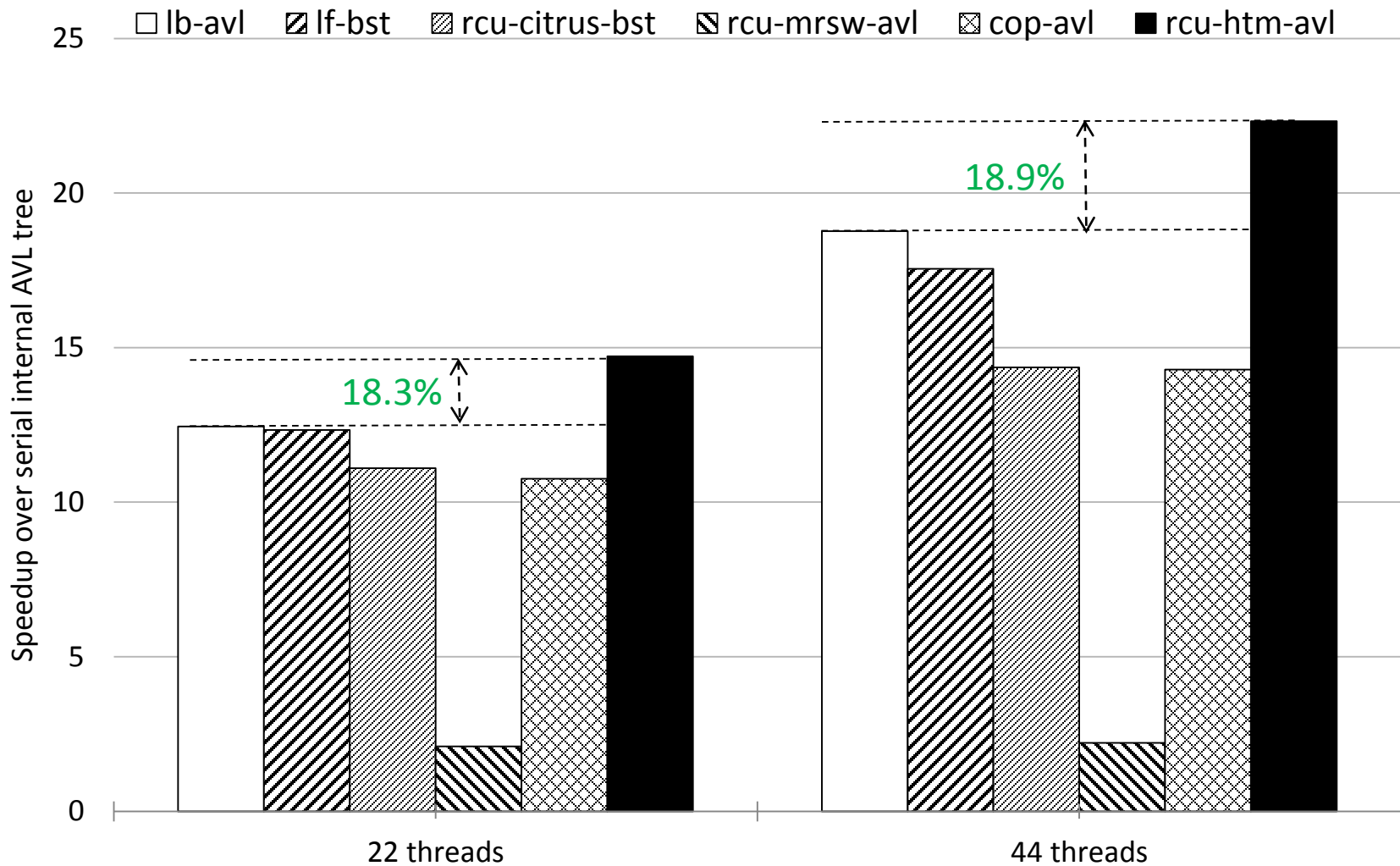
22 threads (no HT)



Performance: overall



Performance: overall



Conclusions

RCU-HTM

- Efficiently combines RCU with HTM
- Provides concurrent binary search trees:
 1. Balanced
 2. Internal
 3. On-time deletion
 4. Asynchronized traversals
 5. Multiple updaters
- **18%** better performance than state-of-the-art BSTs

RCU-HTM AVL and Red-Black trees publicly available:

- <https://github.com/rcu-htm/rcu-htm>



RCU-HTM: Combining RCU with HTM to Implement Highly Efficient Concurrent Binary Search Trees

*Dimitrios Siakavaras, Konstantinos Nikas, Georgios Goumas
and Nectarios Koziris*

National Technical University of Athens (NTUA)
School of Electrical and Computer Engineering (ECE)
Computing Systems Laboratory (CSLab)
{jimsiak,knikas,goumas,nkoziris}@cslab.ece.ntua.gr
<http://research.cslab.ece.ntua.gr>

THANK YOU!
QUESTIONS?

Backup Slides

Concurrent BSTs

Name	Balanced	Internal	On-time deletion
Ellen et al. [PODC'10]	✗	✗	✓
Howley et al. [SPAA'12]	✗	✓	✗
Natarajan et al. [PPoPP'14]	✗	✗	✓
Chatterjee et al. [PODC'14]	✗	✓	✗
Brown et al. [PPoPP'14]	✗	✗	✓

Lock-free

Atomic operations (e.g., CAS) can only modify a single memory word

Concurrent BSTs

Name	Balanced	Internal	On-time deletion
Ellen et al. [PODC'10]	✗	✗	✓
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Chatterjee et al. [PODC'14]	✗	✓	✗
Brown et al. [PPoPP'14]	✗	✗	✓

Bronson et al. [PPoPP'10]	✗	✗	✗
Crain et al. [EuroPar'13]	✗	✗	✗
Drachsler et al. [PPoPP'14]	✗	✓	✓

Lock-free

Atomic operations (e.g., CAS) can only modify a single memory word

Lock-based

Rebalancing would require multiple lock acquisitions and extra effort to avoid deadlocks

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Brown et al. [PPoPP'14]	✗	✗	✓

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Crain et al. [EuroPar'13]	✗	✗	✗
Drachsler et al. [PPoPP'14]	✗	✓	✓

Howard et al. [CCPE'14]	✓	✓	✗
Arbel et al. [PODC'14]	✗	✓	✗

Lock-free

Atomic operations (e.g., CAS) can only modify a single memory word

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RCU-based

No on-time deletion

Concurrent BSTs

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Howard et al. [CCPE'14]	✓	✓	✗
Arbel et al. [PODC'14]	✗	✓	✗

Crain et al. [PPoPP'14]	✗	✓	✗
Avni et al. [TRANSACT'14]	✓	✓	✓

Lock-free

Atomic operations (e.g., CAS) can only modify a single memory word

Lock-based

Rebalancing would require multiple lock acquisitions and extra effort to avoid deadlocks

RCU-based

No on-time deletion

TM-based

Avni provides all three characteristics but has other drawbacks

RCU-HTM vs previous works

	Name	Balanced	Internal	On-time deletion	Asynchronized traversals	Multiple updaters
Lock-free	Ellen et al. [PODC'10]	✗	✗	✓	✓	✓
	Howley et al. [SPAA'12]	✗	✓	✗	✗	✓
	Natarajan et al. [PPoPP'14]	✗	✗	✓	✓	✓
	Chatterjee et al. [PODC'14]	✗	✓	✗	✗	✓
	Brown et al. [PPoPP'14]	✗	✗	✓	✓	✓
Locks	Bronson et al. [PPoPP'10]	✗	✗	✗	✗	✓
	Crain et al. [EuroPar'13]	✗	✗	✗	✓	✓
	Drachsler et al. [PPoPP'14]	✗	✓	✓	✓	✓
RCU	Howard et al. [CCPE'14]	✓	✓	✗	✓	✗
	Arbel et al. [PODC'14]	✗	✓	✗	✓	✓
TM	Crain et al. [PPoPP'14]	✗	✓	✗	✗	✓
	Avni et al. [TRANSACT'14]	✓	✓	✓	✗	✓

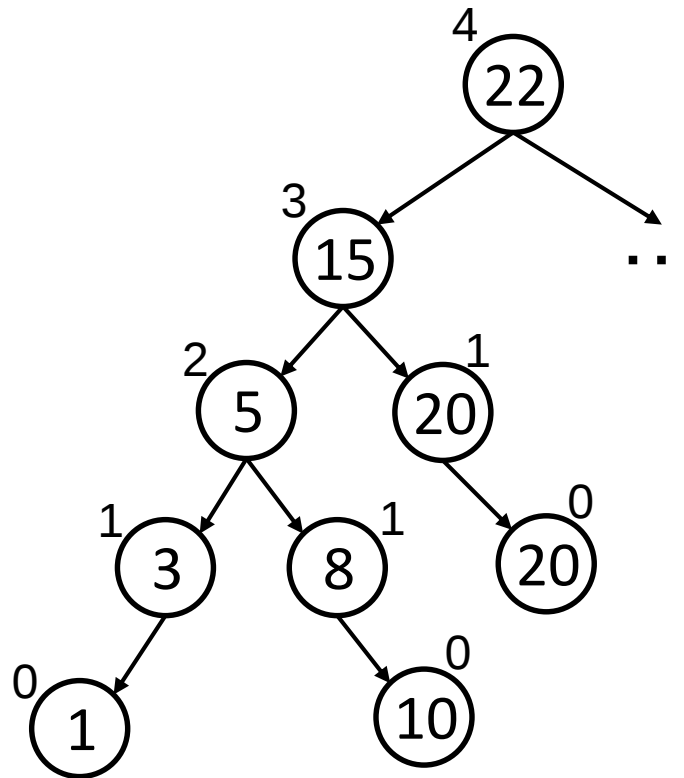
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	Crain et al. [EuroPar'13]	✗	✗	✗	✓	✓
	Drachsler et al. [PPoPP'14]	✗	✓	✓	✓	✓
RCU	Howard et al. [CCPE'14]	✓	✓	✗	✓	✗
	Arbel et al. [PODC'14]	✗	✓	✗	✓	✓
TM	Crain et al. [PPoPP'14]	✗	✓	✗	✗	✓
	Avni et al. [TRANSACT'14]	✓	✓	✓	✗	✓
	RCU-HTM	✓	✓	✓	✓	✓

RCU-HTM: Internal Deletion

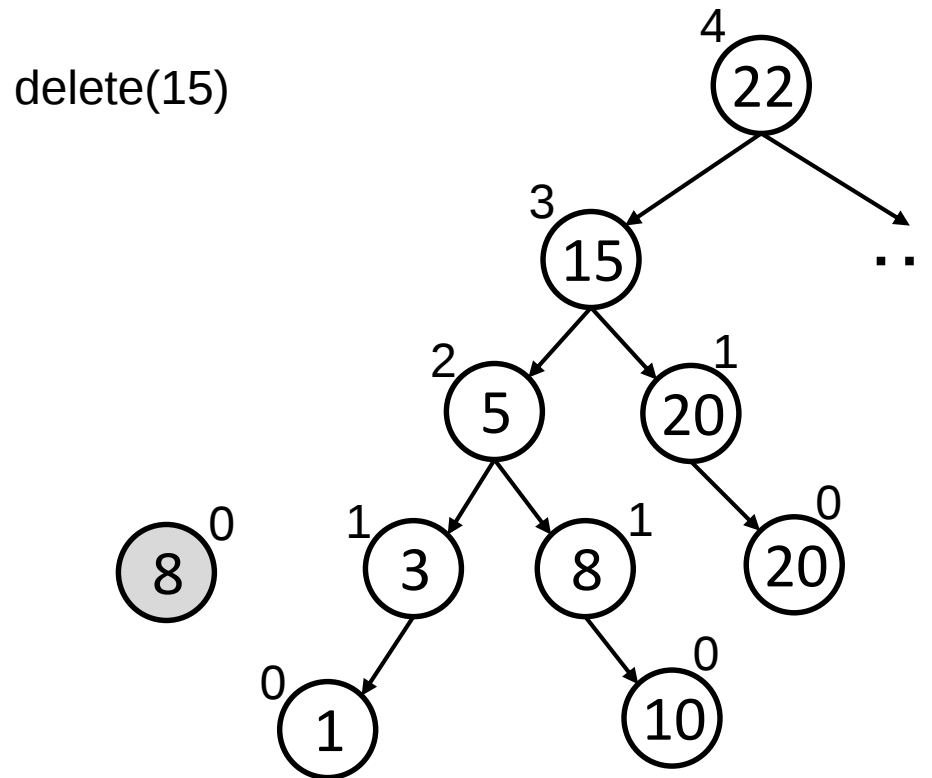
RCU-HTM replaces the whole path from the internal node to the successor

delete(15)



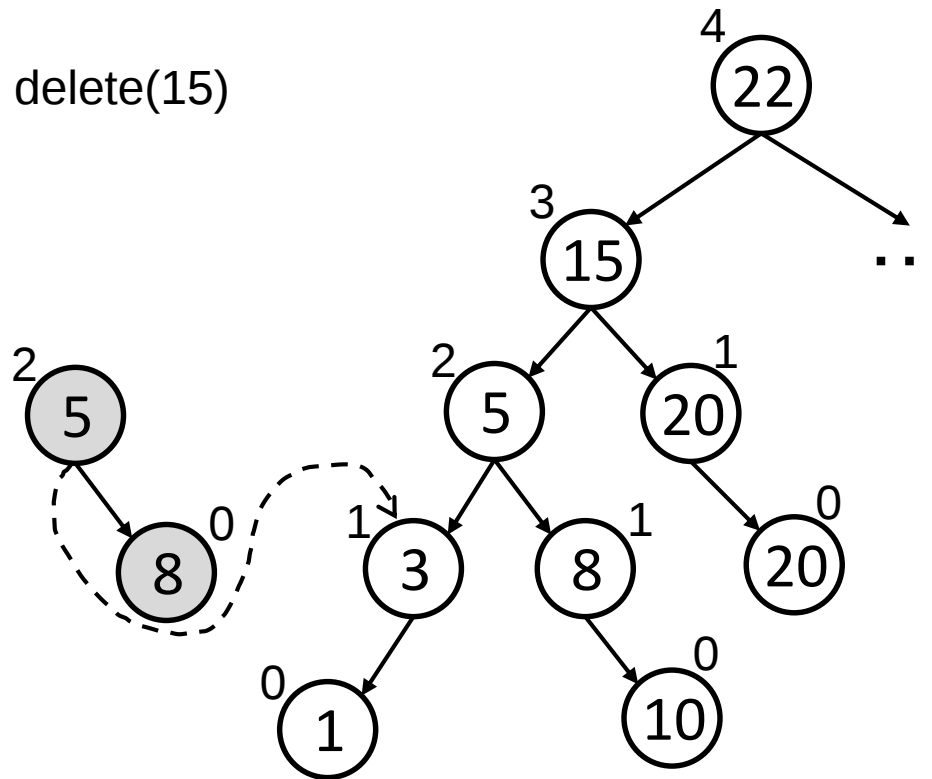
RCU-HTM: Internal Deletion

RCU-HTM replaces the whole path from the internal node to the successor



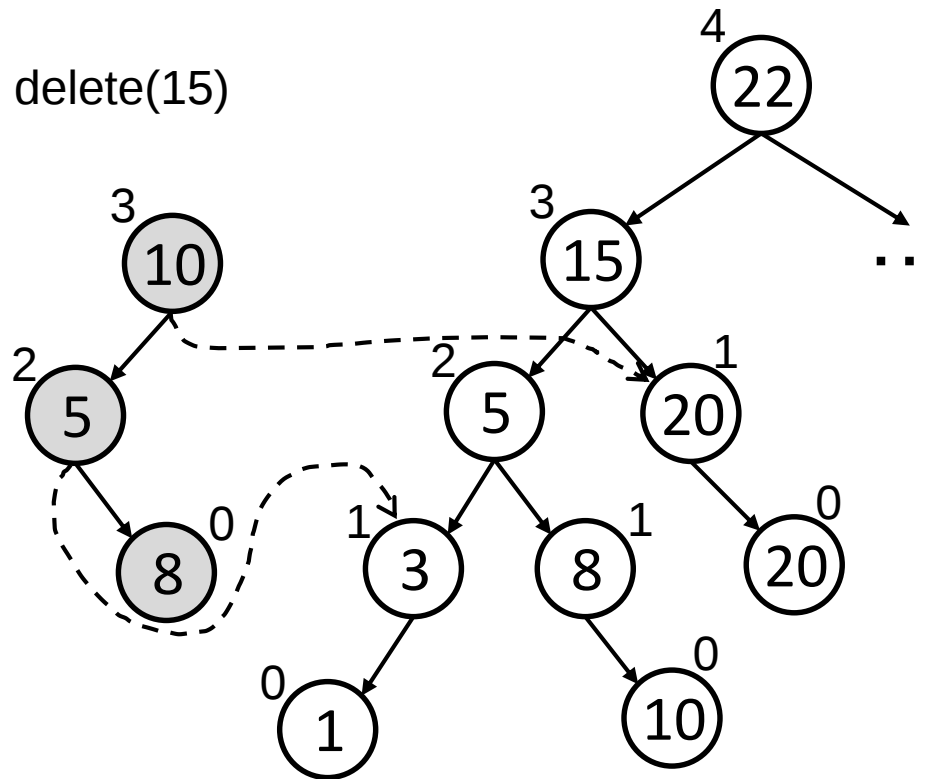
RCU-HTM: Internal Deletion

RCU-HTM replaces the whole path from the internal node to the successor



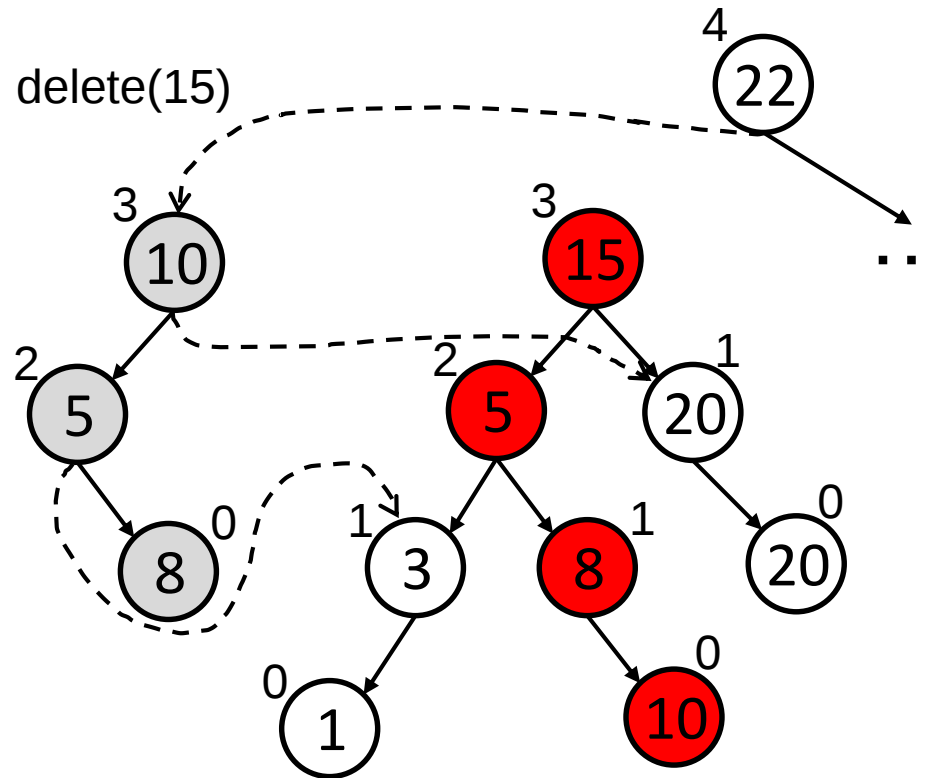
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